

Verifying Concurrent Graph Algorithms

Azalea Raad **Aquinas Hobor** **Jules Villard** **Philippa Gardner**

Imperial College London
National University of Singapore

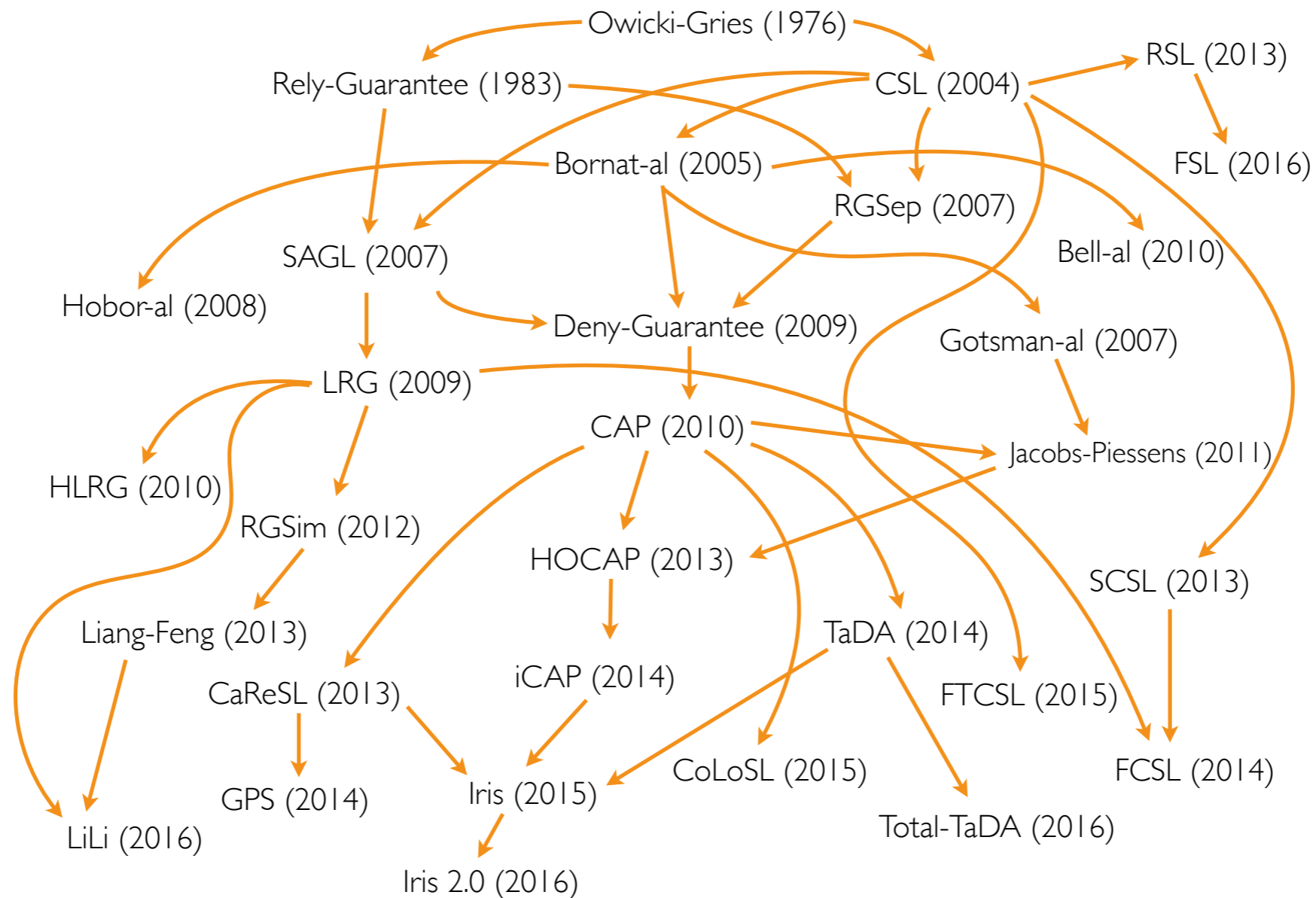
APLAS'16
22 November 2016

Concurrent Program Logic Genealogy

Verifying **concurrent** algorithms is difficult...

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Graph credit: Ilya Sergey

Graph Algorithms

Verifying **graph** algorithms is difficult...

- Subtle correctness argument
- Overlapping structure (unspecified sharing via pointer aliasing)
 - Non-compositional reasoning (preventing the use of the frame rule)

Graph Algorithms

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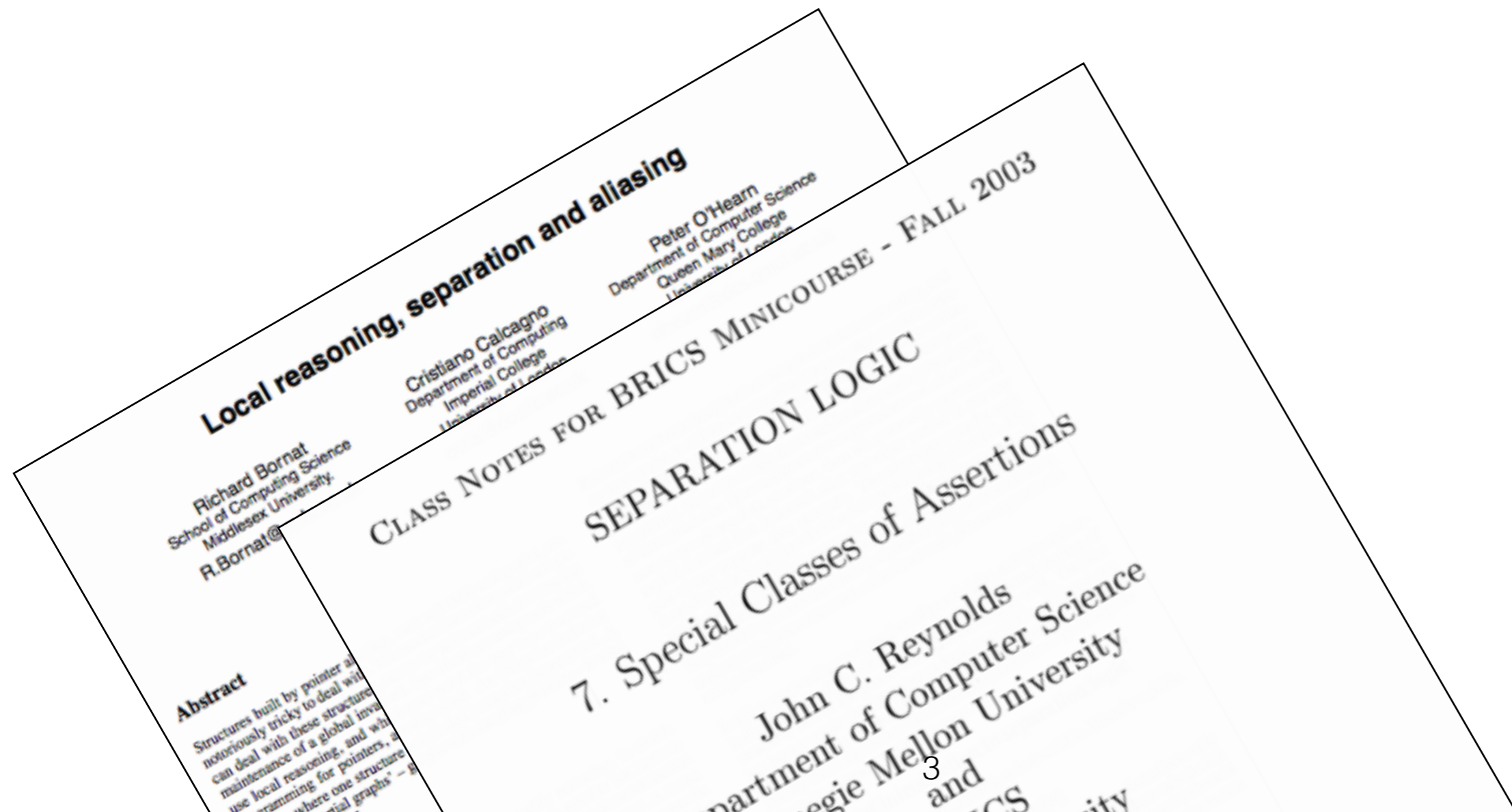
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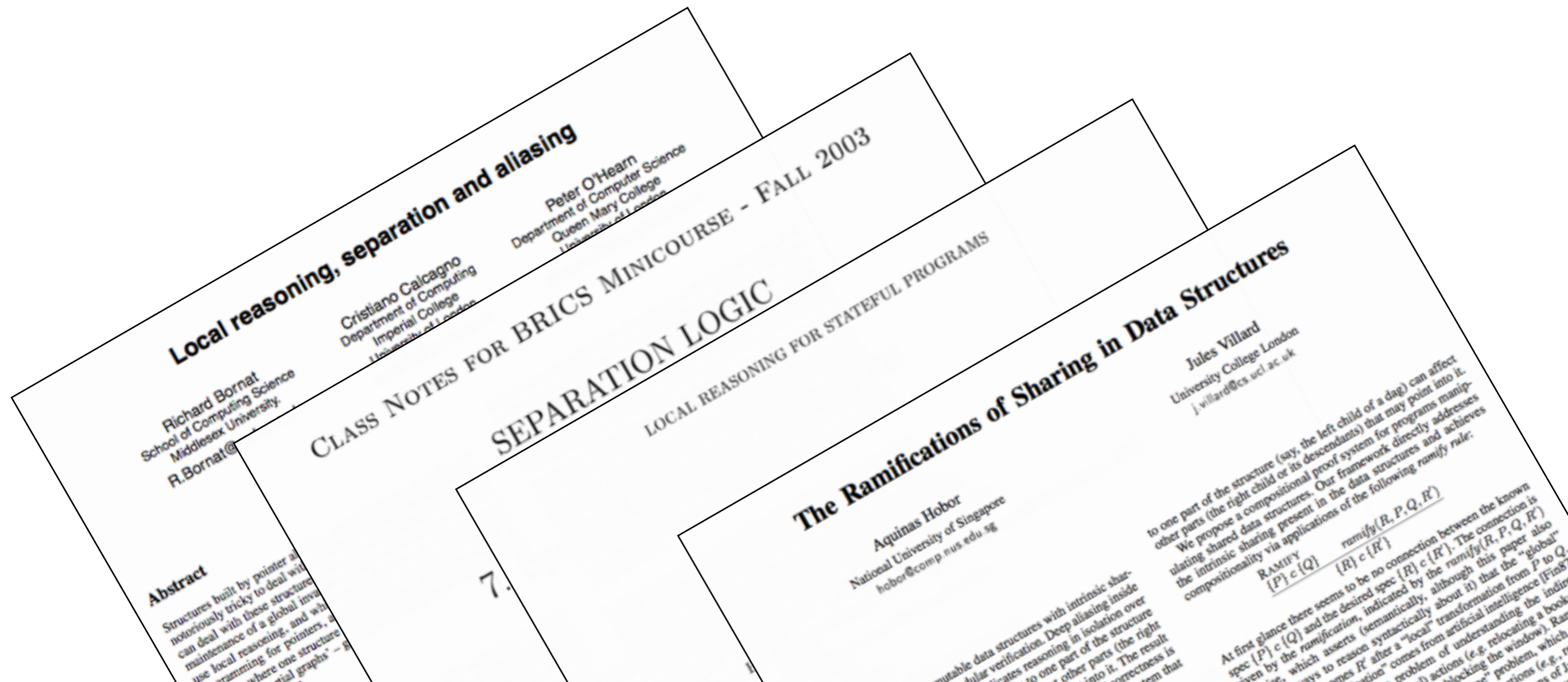
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Concurrent Graph Algorithms

Verifying **concurrent graph** algorithms is even more difficult...

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- Reasoning about each thread in isolation

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CoLoSL: Concurrent Local Subjective Logic

Azalea Raad, Jules Villard, and Philippa Gardner
Imperial College London
{azalea.j.villard,pg}@doc.ic.ac.uk

Abstract. A key difficulty in verifying shared-memory concurrent programs is reasoning compositionally about each thread in isolation. Existing verification techniques for fine-grained concurrency typically require reasoning about either the entire shared state or disjoint parts of the shared state, impeding compositionality. This paper introduces the program logic CoLoSL, where each thread is verified with respect to its subjective view of the global shared state. This subjective view describes only that part of the state accessed by the thread. Subjective views may arbitrarily overlap with each other, and expand and contract depending on the resources required by the thread. This flexibility gives rise to small specifications and, hence, more compositional reasoning for concurrent programs. We demonstrate our reasoning on a range of examples, including a concurrent computation of a spanning tree of a graph.

Introduction

...ing properties of fine-grained concurrent programs is being ...ly about each thread in isolation, even though in ... system is the collaborative result of intricately ...h compositional reasoning is essential for: ...llows them to be verified component- ... a programmer's intuition ... formal arguments are ... reasoning ... separation ...

Concurrent Graph Algorithms

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demonstrate our re
computation of a s

Mechanized Verification of Fine-grained Concurrent Programs

Ilya Sergey
IMDEA Software Institute
ilya.sergey@imdea.org

Abstract

Efficient concurrent programs and data structures rarely employ coarse-grained synchronization mechanisms (i.e., locks); instead, they implement custom synchronization patterns via fine-grained primitives, such as compare-and-swap. Due to sophisticated inter-processor scenarios and error-prone, reasoning about such programs is challenging and error-prone, and can benefit from mechanization. In this paper, we present the first completely formalized framework for mechanized verification of full functional correctness of concurrent programs. Our tool is based on the re-implementation of the dependent frame logic FCSL. It is implemented as an extensible higher-order functions and is powerful enough to reason about uniform concurrency in a uniform commutative setting. This work is a first step towards a general framework for mechanized verification of fine-grained concurrent programs.

Anindya Banerjee
IMDEA Software Institute
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resources, therefore, reducing the proof of correctness of concurrent code to the proof of correctness of sequential code. While sound, this approach to concurrency prevents one from taking full advantage of parallel computations. An alternative is to implement shared data structures in a fine-grained (i.e., lock-free) manner, so the threads manipulating such structures would be reaching a consensus (e.g., compare-and-swap) instead of locks. Despite the clear practical advantages of the fine-grained approach to the implementation of concurrent data structures, it requires significant expertise to devise such structures and establish correctness of their behavior. In this paper, we focus on program logics as a generic approach to specify a program and formally prove its correctness wrt. the given specification. In such logics, program specifications (or specs) are represented by Hoare triples $\{P\} c \{Q\}$, where c is a program being described, P is a precondition that constrains a state in which the program is safe to run, and Q is a postcondition describing a state upon the program's termination. Modern logics are sufficiently expressive: they can reason about programs operating with first-class executable code, locally-spawned threads and features omnipresent in modern programming. Verifying a program in a Hoare-style program logic can be done structurally, without the need of systematically applying syntax-directed inference rules. This work is a first step towards a general framework for mechanized verification of fine-grained concurrent programs. While sound, this approach to concurrency prevents one from taking full advantage of parallel computations. An alternative is to implement shared data structures in a fine-grained (i.e., lock-free) manner, so the threads manipulating such structures would be reaching a consensus (e.g., compare-and-swap) instead of locks. Despite the clear practical advantages of the fine-grained approach to the implementation of concurrent data structures, it requires significant expertise to devise such structures and establish correctness of their behavior. In this paper, we focus on program logics as a generic approach to specify a program and formally prove its correctness wrt. the given specification. In such logics, program specifications (or specs) are represented by Hoare triples $\{P\} c \{Q\}$, where c is a program being described, P is a precondition that constrains a state in which the program is safe to run, and Q is a postcondition describing a state upon the program's termination. Modern logics are sufficiently expressive: they can reason about programs operating with first-class executable code, locally-spawned threads and features omnipresent in modern programming. Verifying a program in a Hoare-style program logic can be done structurally, without the need of systematically applying syntax-directed inference rules. This work is a first step towards a general framework for mechanized verification of fine-grained concurrent programs.

Contributions

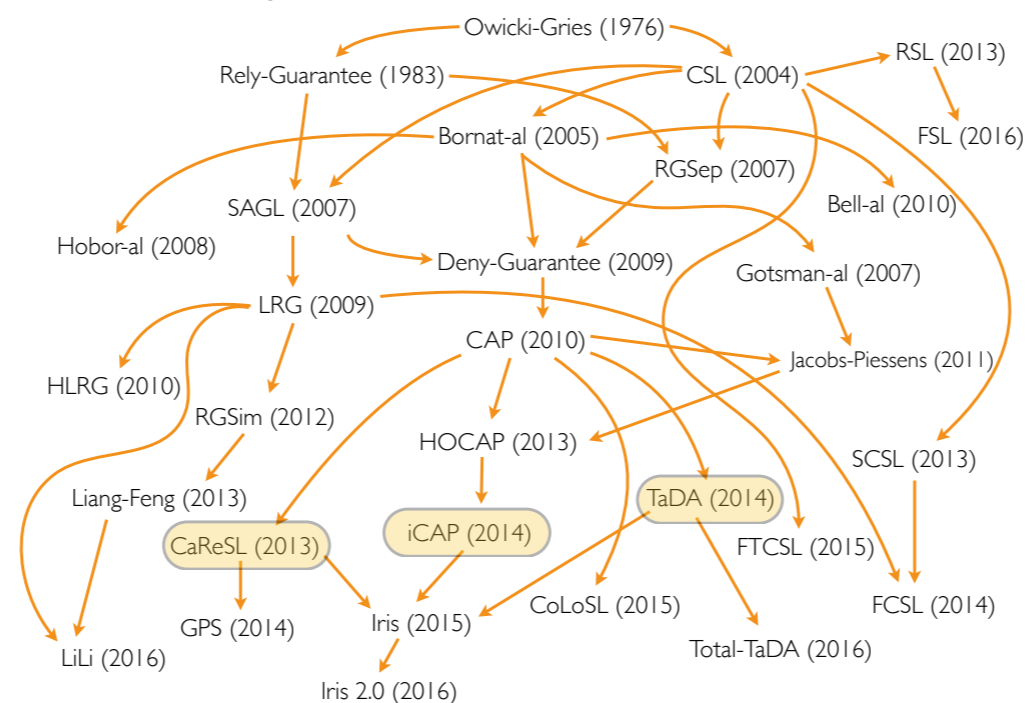
- Verified 4 concurrent fine-grained graph algorithms
 - ▶ Copying dags (directed acyclic graphs)
 - ▶ Speculative variant of Dijkstra's shortest path
 - ▶ Computing the spanning tree of a graph
 - ▶ Marking a graph

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 - ▶ Abstract mathematical graphs for Functional correctness
 - ▶ Concrete Spatial (heap-represented) graphs for memory safety
 - ▶ Combined reasoning for full proof

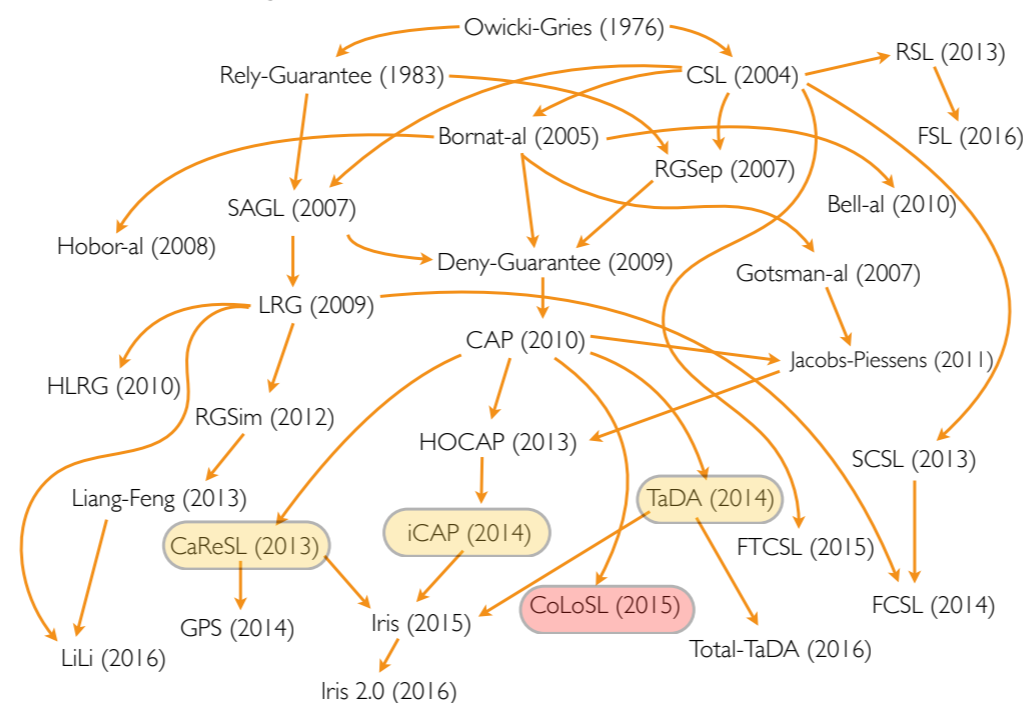
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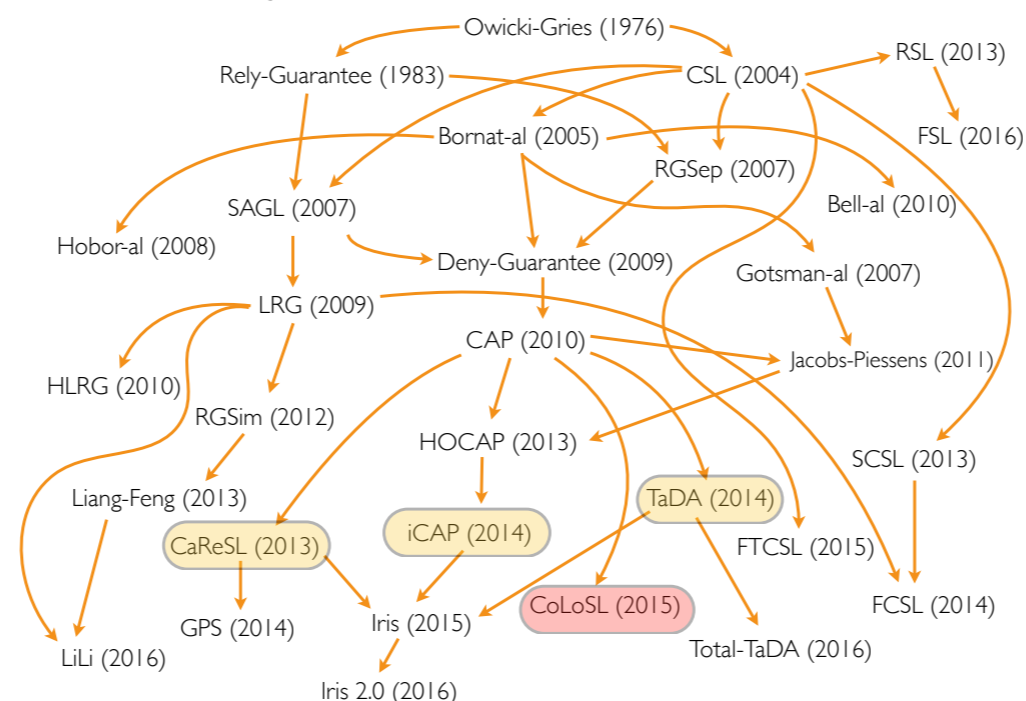
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This Talk

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Copying Binary DAGs

```
struct node {struct node *c, *l, *r}
copy_dag(struct node *x) {
    struct node *l, *r, *ll, *rr, *x';    bool b;
    if (!x) {return 0;}
    x' = malloc(sizeof(struct node));
    I b = <CAS(x->c, 0, x')>;
    if (b) {
        l = x->l; r = x->r;
        ll = copy_dag(l)    ||    rr = copy_dag(r)
        I <x'->l = ll>; <x'->r = rr>;
        return x';
    } else {
        free(x', sizeof(struct node));    return x->c;
    }
}
```

atomic blocks



copy_dag(x) Specification

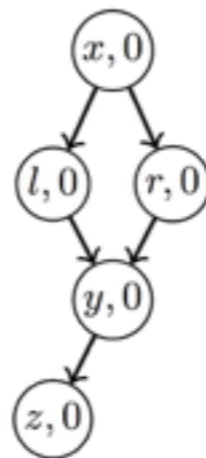
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    }
}
```

- Specification challenges

- ▶ When `copy_dag(x)` returns, `x` is copied but its children may not be
- ▶ If `x` is already copied, `copy_dag(x)` simply returns:
 - the thread that copied `x` has made a promise to visit `x`'s children and ensure they are copied

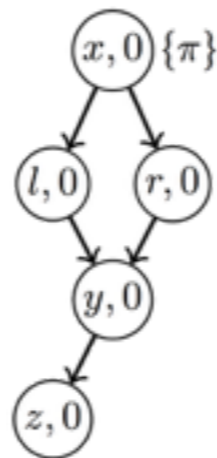
copy_dag(x): A Trace

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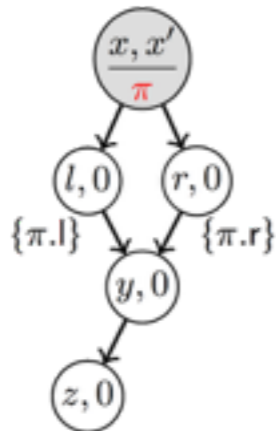
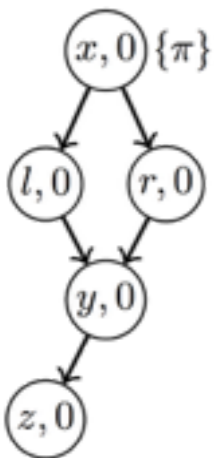
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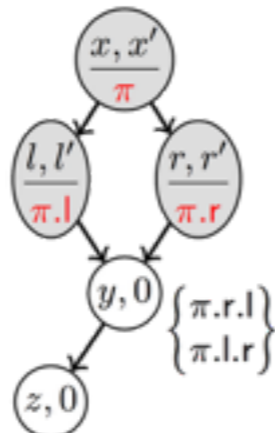
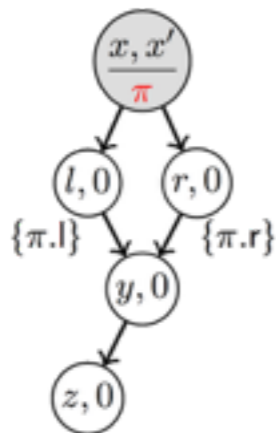
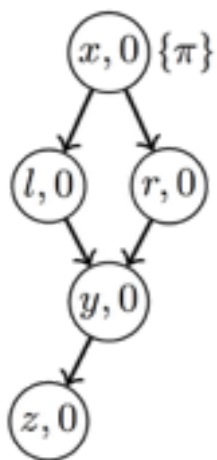


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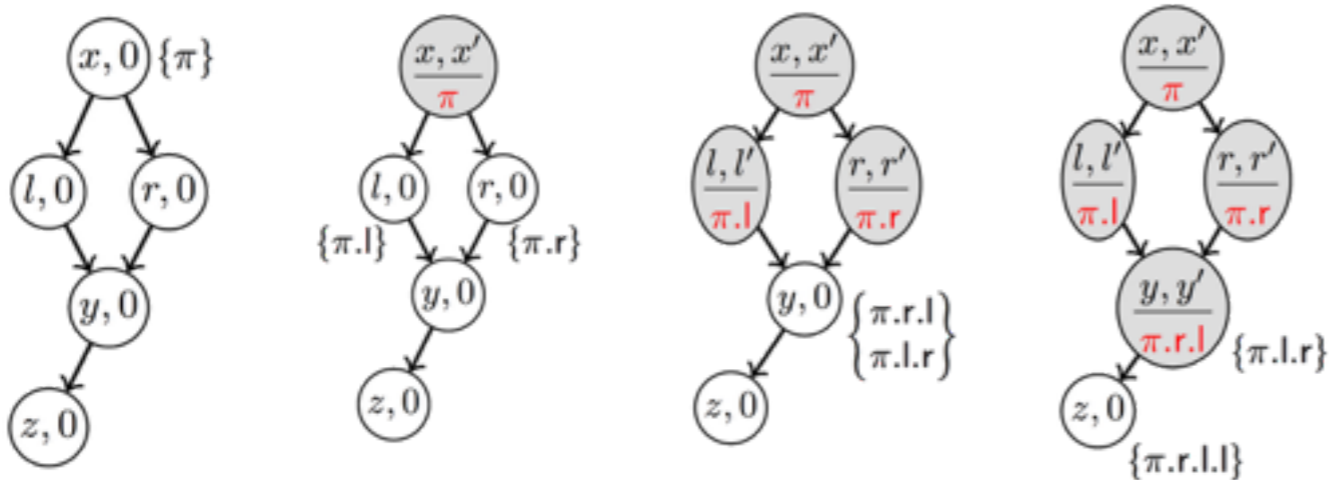


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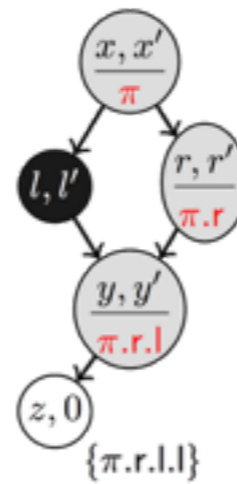
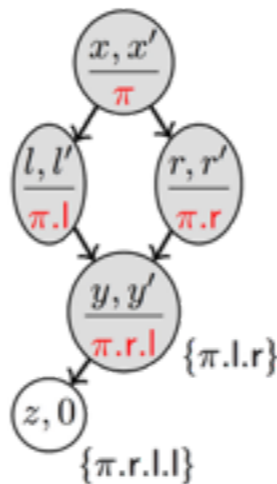
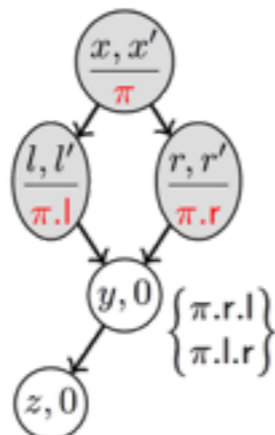
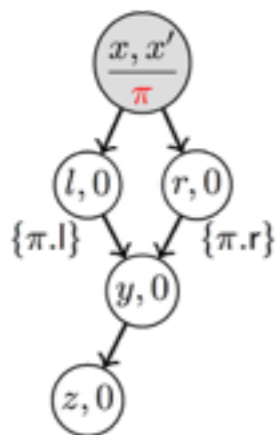
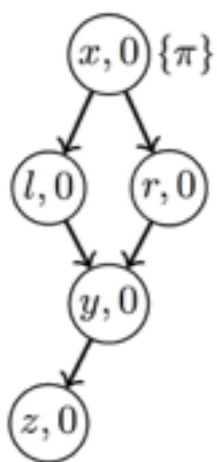


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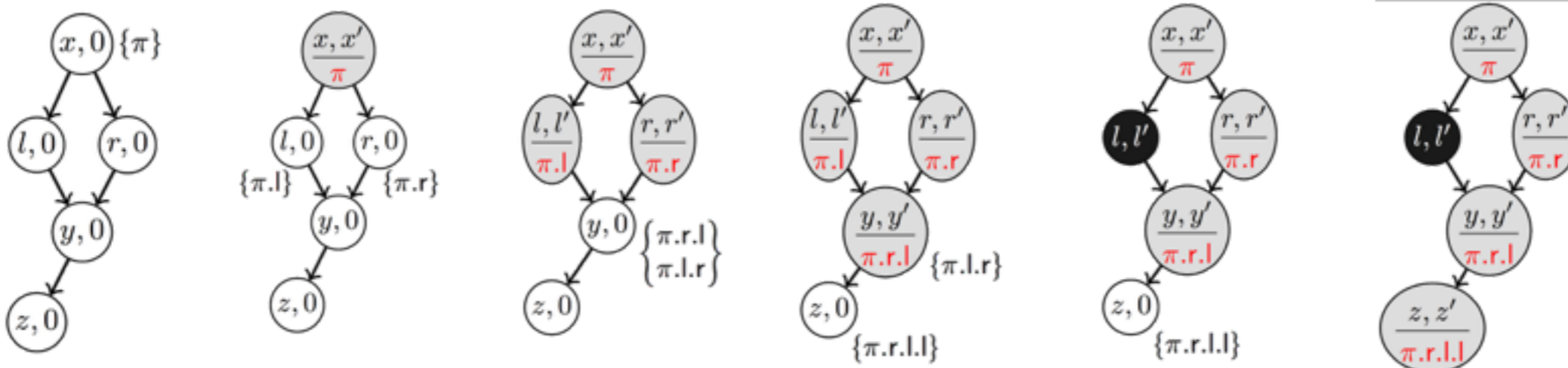


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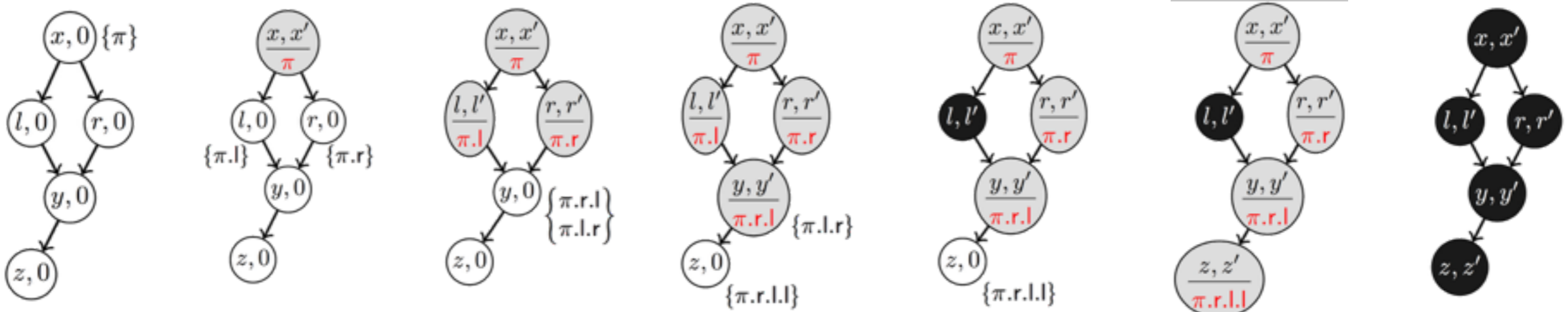


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1. Tokens

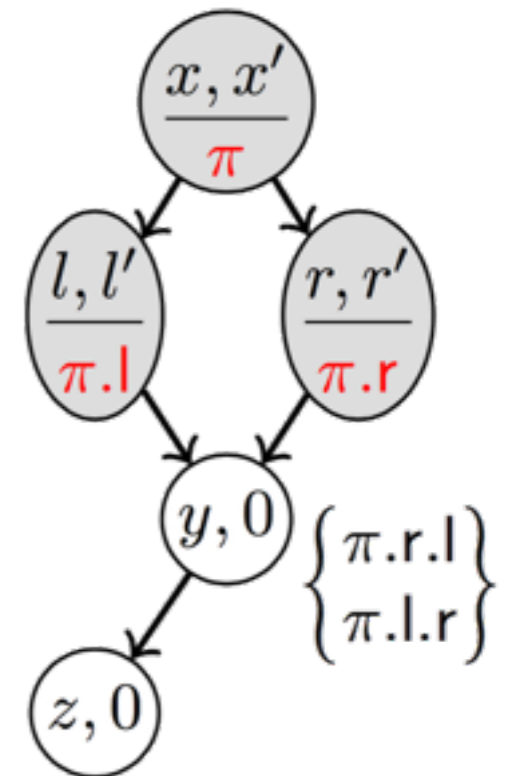
A token mechanism for

- Thread identification
- Thread progress tracking

1. Tokens

The `copy_dag` token mechanism for

- Thread identification
 - distinguish one token (thread) from another
 - identify two distinct sub-tokens given any token (at recursive call points)
 - model a parent-child relation (spawner-spawnee)
- Thread progress tracking
 - marking thread ids as tokens
 - promise sets as token sets

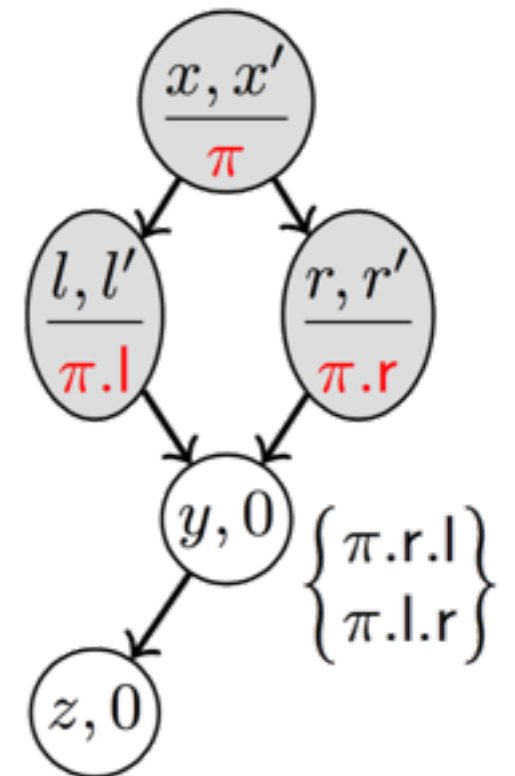


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$$\bullet.l = \widehat{\bullet \circ}$$

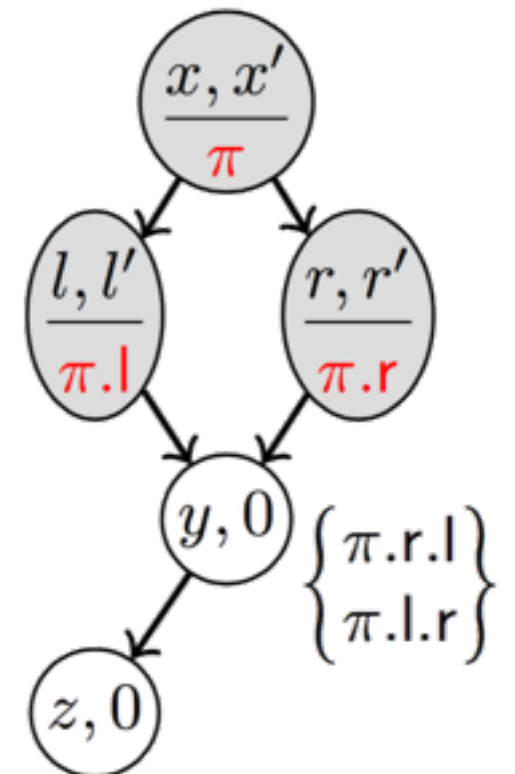
$$(\widehat{\circ \pi}).l = \widehat{\circ \pi.l}$$

$$(\widehat{\pi \circ}).l = \widehat{\pi.l \circ}$$

$$\bullet.r = \widehat{\circ \bullet}$$

$$(\widehat{\circ \pi}).r = \widehat{\circ \pi.r}$$

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$$(\widehat{\circ \pi}).l = \widehat{\circ \pi.l}$$

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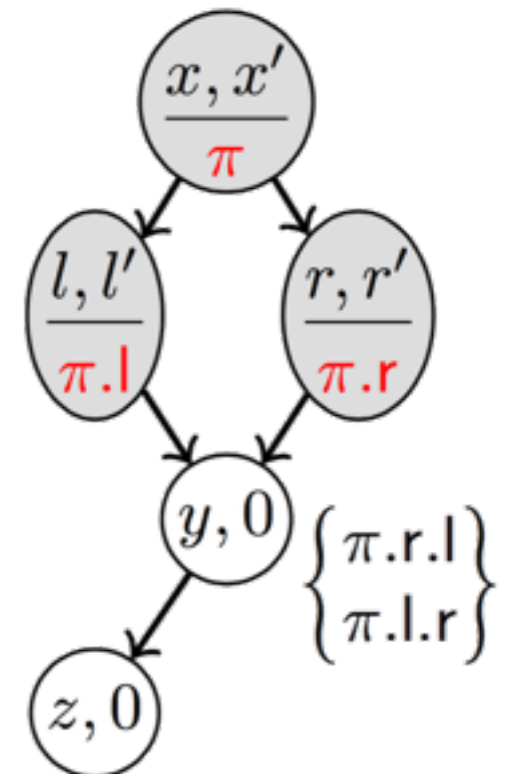
$$\bullet.r = \widehat{\circ \bullet}$$

$$(\widehat{\circ \pi}).r = \widehat{\circ \pi.r}$$

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$$\sqsubset = \{(\pi.l, \pi), (\pi.r, \pi)\}^+$$

sub-thread relation



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top-most (maximal) token

$$\pi ::= \bullet \mid \circ \widehat{\pi} \mid \widehat{\pi} \circ$$

$$\bullet.l = \bullet \circ$$

$$(\circ \widehat{\pi}).l = \circ \widehat{\pi}.l$$

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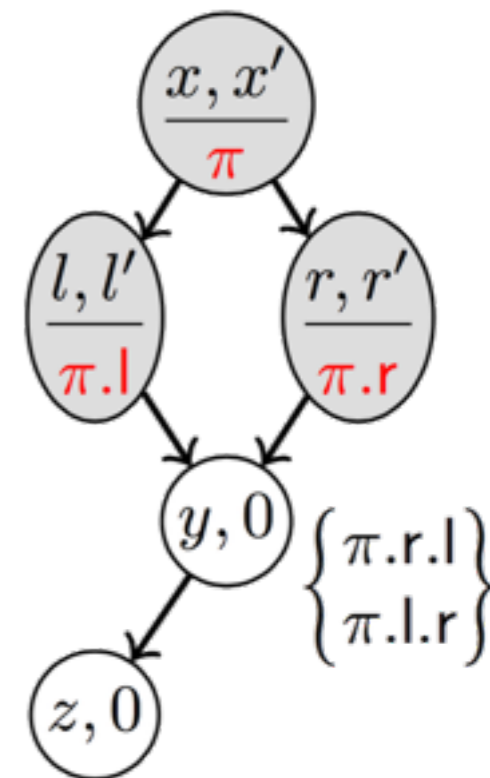
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$$(\widehat{\pi} \circ).l = \widehat{\pi}.l \circ$$

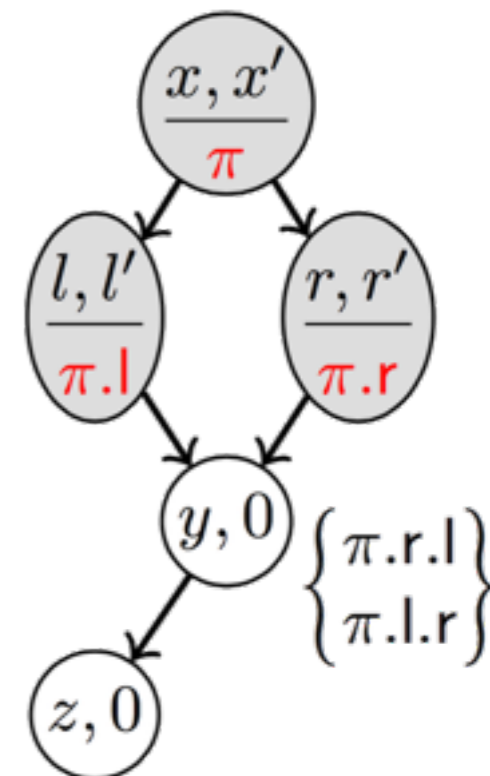
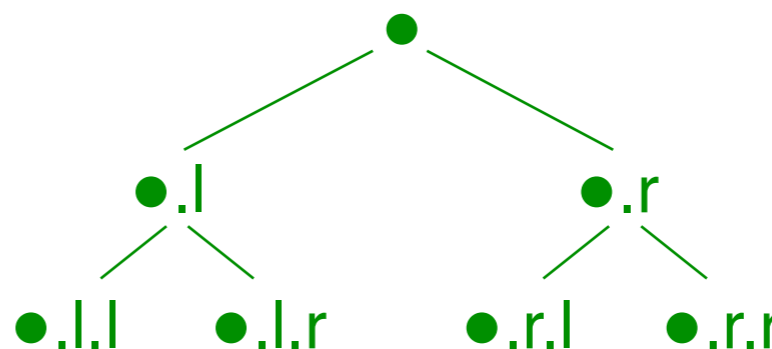
$$\bullet.r = \circ \bullet$$

$$(\circ \widehat{\pi}).r = \circ \widehat{\pi}.r$$

$$(\widehat{\pi} \circ).r = \widehat{\pi}.r \circ$$

$$\sqsubset = \{(\pi.l, \pi), (\pi.r, \pi)\}^+$$

sub-thread relation



2. Mathematical Objects

2. Mathematical Objects

- An abstract representation of the underlying data structure

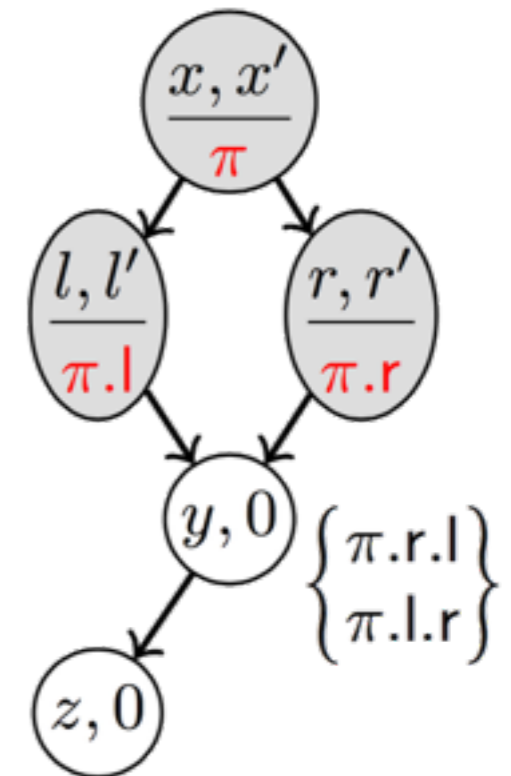
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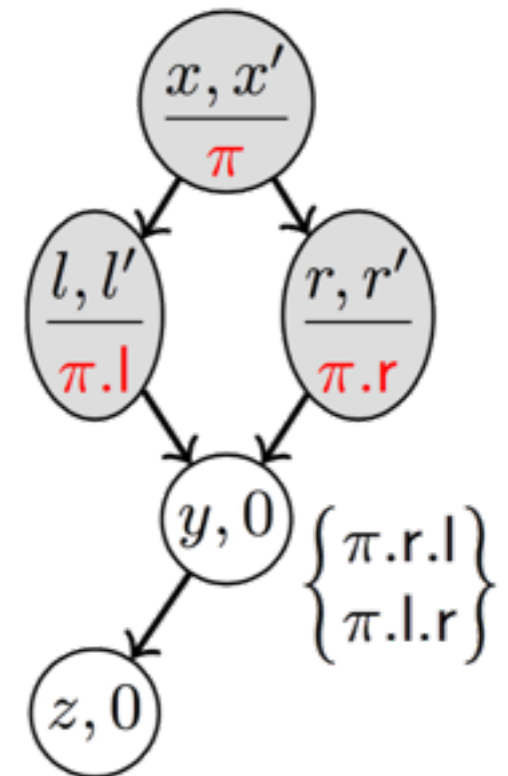


2. Mathematical Objects

- An abstract representation of the underlying data structure
 - e.g. a pair of mathematical dags (δ, δ_c)
 - each dag is a triple:

$$\delta = (\overset{\text{Vertices}}{\textcircled{V}}, E, L)$$

$$V = \{x, l, r, y, z\}$$



2. Mathematical Objects

- An abstract representation of the underlying data structure
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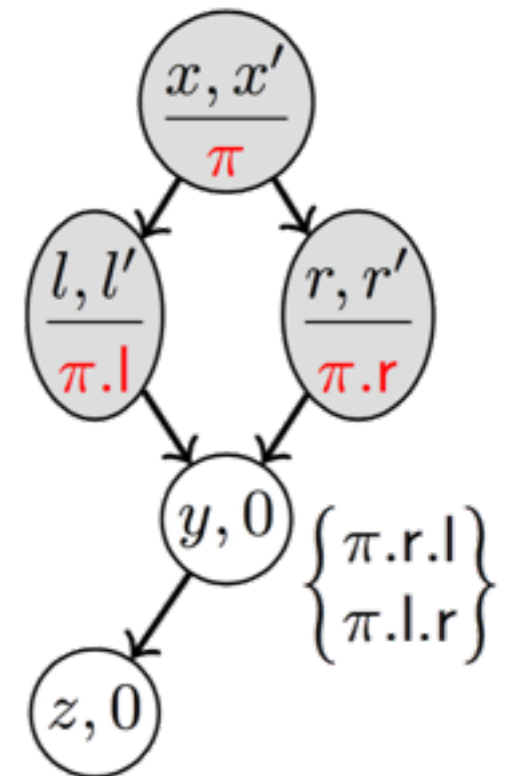
$$E(x) = l, r$$

$$E(l) = 0, y$$

$$E(r) = y, 0$$

$$E(y) = z, 0$$

$$E(z) = 0, 0$$



2. Mathematical Objects

- An abstract representation of the underlying data structure
 - e.g. a pair of mathematical dags (δ, δ_c)
 - each dag is a triple:

$$\delta = (V, E, L)$$

Labels

copy

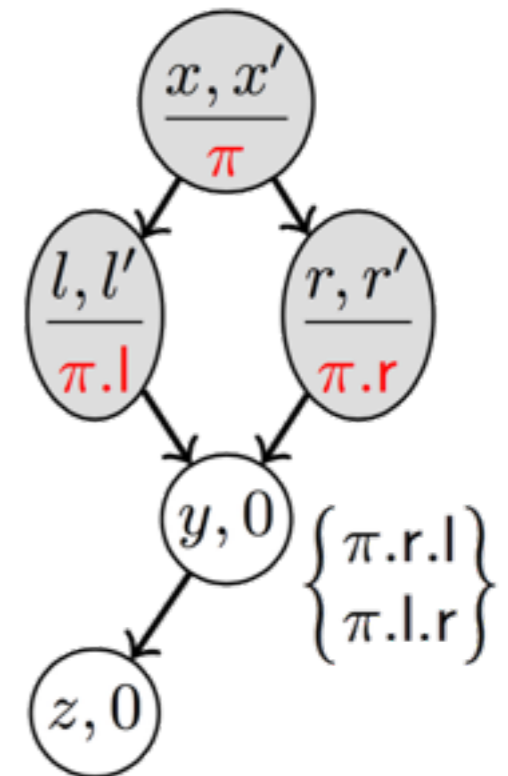
$$L(x) = x', \pi, \{\}$$

$$L(l) = l', \pi.l, \{\}$$

$$L(r) = r', \pi.r, \{\}$$

$$L(y) = 0, 0, \{\pi.r.l, \pi.l.r\}$$

$$L(z) = 0, 0, \{\}$$



2. Mathematical Objects

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Labels

copy copying thread

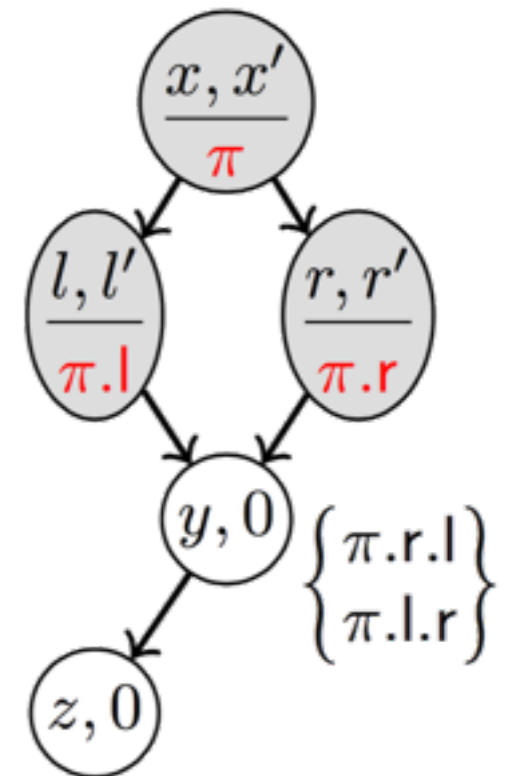
$$L(x) = x', \pi, \{\}$$

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 - e.g. a pair of mathematical dags (δ, δ_c)
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Labels

copy copying thread promise set

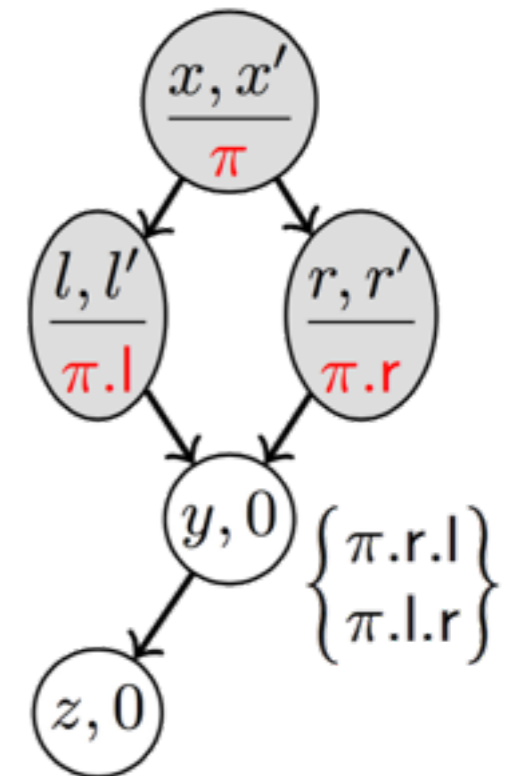
$$L(x) = x', \pi, \{\}$$

$$L(l) = l', \pi.l, \{\}$$

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Labels

copy copying thread promise set

$$L(x) = (x', \pi, \{\})$$

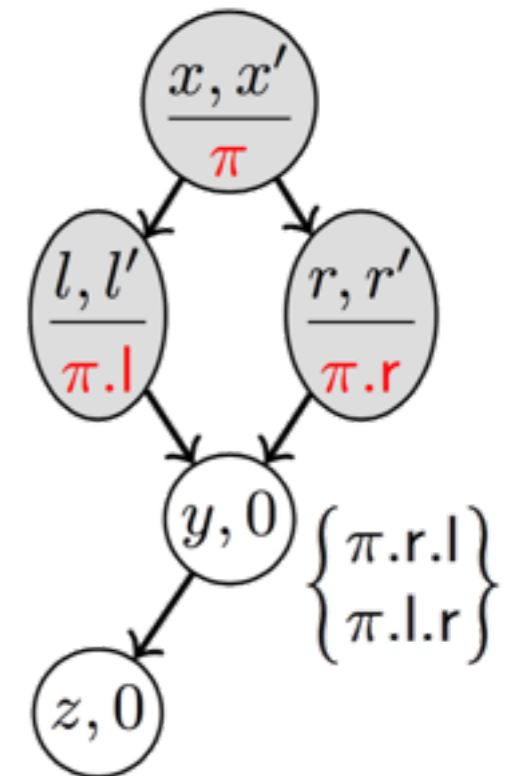
ghost components

$$L(l) = (l', \pi.l, \{\})$$

$$L(r) = (r', \pi.r, \{\})$$

$$L(y) = (0, 0, \{\pi.r.l, \pi.l.r\})$$

$$L(z) = (0, 0, \{\})$$



2. Mathematical Objects

- An abstract representation of the underlying data structure
 - e.g. a pair of mathematical dags (δ, δ_c)
 - each dag is a triple:

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Labels

copy(x) thread(x) promise(x)

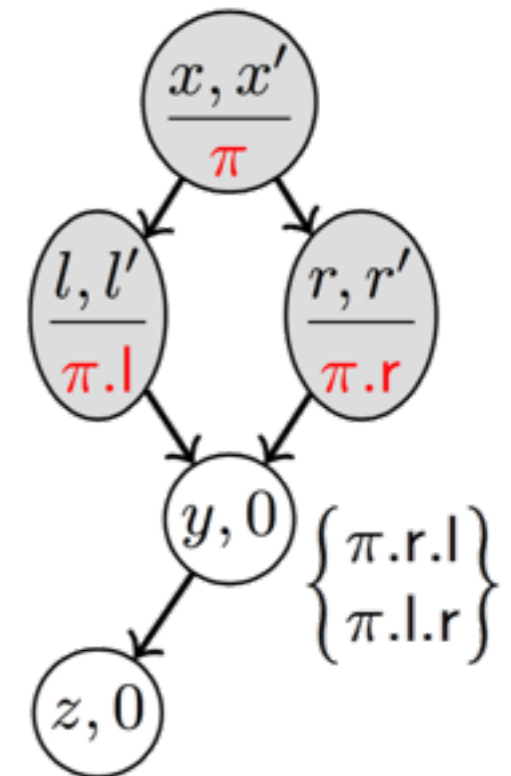
$$L(x) = (x', \pi, \{\})$$

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3. Mathematical Actions

- An abstraction of thread actions (on mathematical objects)
 - atomic blocks as well as ghost actions
 - A^π denotes the actions of thread π

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```
struct node {struct node *c, *l, *r}
copy_dag(struct node *x) {
    struct node *l, *r, *ll, *rr, *x';    bool b;
    if (!x) {return 0;}
    x' = malloc(sizeof(struct node));
    b = <CAS(x->c, 0, x')>;
    if (b) {
        l = x->l; r = x->r;
        ll = copy_dag(l)    ||    rr = copy_dag(r)
        <x'->l = ll>; <x'->r = rr>;
        return x';
    } else {
        free(x', sizeof(struct node));    return x->c;
    }
}
```

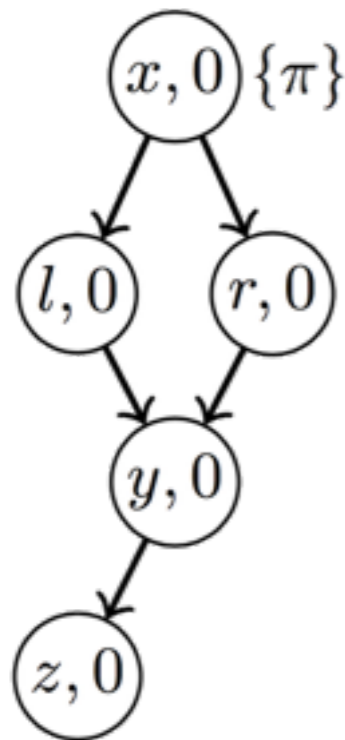
3. Mathematical Actions

- An abstraction of thread actions (on mathematical objects)
 - atomic blocks as well as ghost actions
 - A^π denotes the actions of thread π

```
struct
copy_dag(
  struct
  if
  x' =
  b = AS <CAS(x->c, 0, x')>;
  if
    l = x->l; r = x->r;
    ll = copy_dag(l) || rr = copy_dag(r)
    <x'->l = ll>; <x'->r = rr>
  } else {
  }
}
```

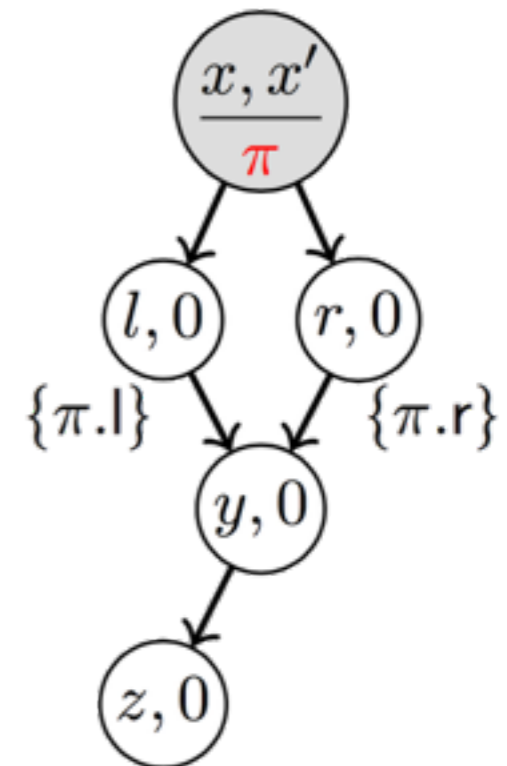
3. Mathematical Actions

- An abstraction of thread actions (on mathematical objects)
 - atomic blocks as well as ghost actions
 - A^π denotes the actions of thread π



```

struct
copy_dag(
  struct
  if
  x' =
  b = <CAS(x->c, 0, x')>;
  if
  l = x->l; r = x->r;
  ll = copy_dag(l) || rr = copy_dag(r)
  <x'->l = ll; <x'->r = rr>
} else {
}
}
    
```



$$(\delta, \delta_c) = ((V, E, L), (V_c, E_c, L_c))$$



$$(\delta', \delta'_c) = ((V, E, L'), (V'_c, E'_c, L'_c))$$

$$L(x) = 0, 0, \{\pi\}$$

$$L(l) = 0, 0, \{\}$$

$$L(r) = 0, 0, \{\}$$

$$L' = L[x \mapsto x', \pi, \{\}][l \mapsto 0, 0, \{\pi.l\}][r \mapsto 0, 0, \{\pi.r\}]$$

$$V'_c = V_c \uplus \{x'\} \quad E'_c = E_c \uplus [x' \mapsto \dots] \quad L'_c = L_c \uplus [x' \mapsto \dots]$$

4. Mathematical Specification

$\text{Inv}(\delta, \delta_c) \triangleq \delta$ and δ_c are both acyclic;
every node x' in the copy δ_c corresponds to a unique node x in the original δ ;
every node x in the original δ has some copy value x'

4. Mathematical Specification

$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge$
every node x' in the copy δ_c corresponds to a unique node x in the original δ ;
every node x in the original δ has some copy value x'

4. Mathematical Specification

$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \wedge$
every node x in the original δ has some copy value x'

4. Mathematical Specification

$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

4. Mathematical Specification

$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

$\text{ic}(x, x', \delta, \delta_c) \triangleq$ if x' is 0 (x is not copied yet), then x will eventually be copied:

4. Mathematical Specification

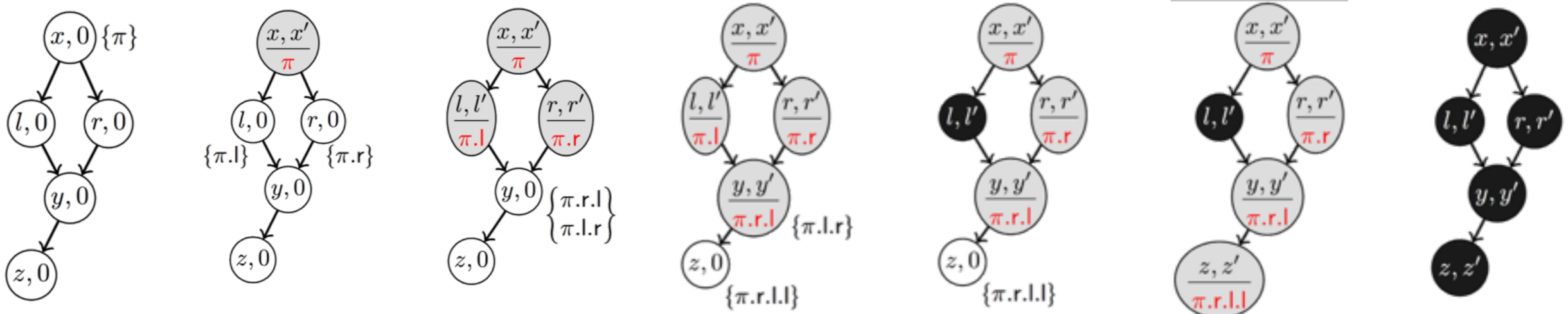
$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

$\text{ic}(x, x', \delta, \delta_c) \triangleq$ if x' is 0 (x is not copied yet), then x will eventually be copied:

there exists some y in δ s.t.

- 1) the promise set of y is non-empty;
- 2) y can reach x along a path p ;
- and 3) every node along the path p is not copied

\Rightarrow when y is eventually copied, it'll visit x along p and copy it too



4. Mathematical Specification

$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

$\text{ic}(x, x', \delta, \delta_c) \triangleq$ if x' is 0 (x is not copied yet), then x will eventually be copied:

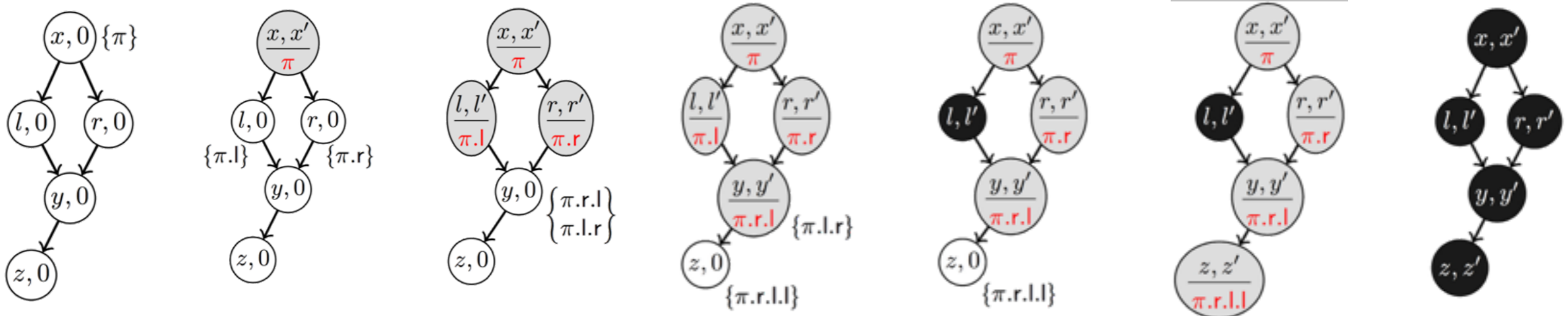
there exists some y in δ s.t.

- 1) the promise set of y is non-empty;
- 2) y can reach x along a path p ;
- and 3) every node along the path p is not copied

otherwise, x' is a node in δ_c and

the children of x , (l, r) , are also copied to some (l', r') :

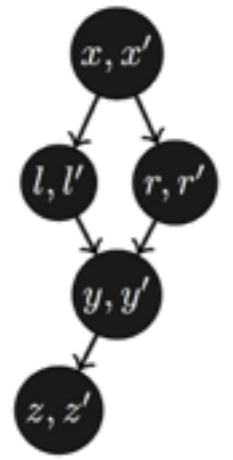
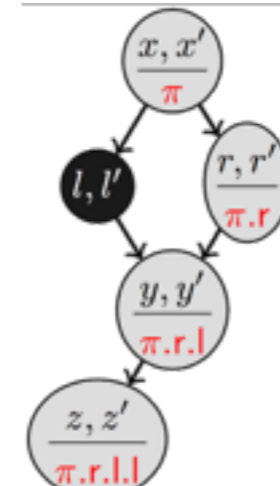
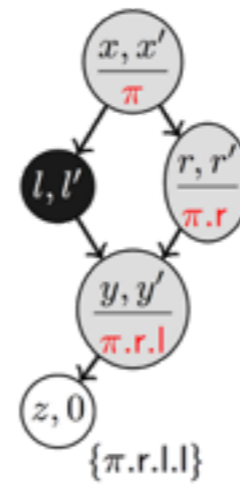
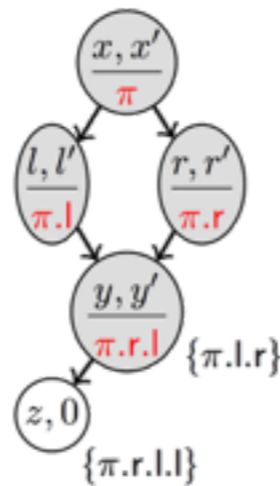
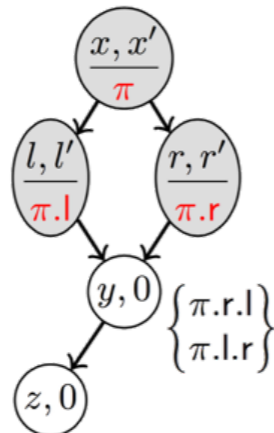
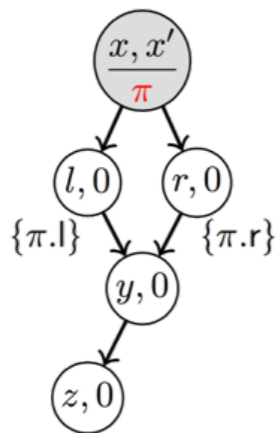
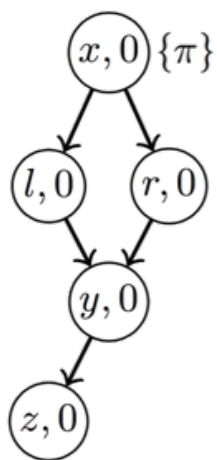
$\text{ic}(l, l', \delta, \delta_c)$ and $\text{ic}(r, r', \delta, \delta_c)$



4. Mathematical Specification

$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

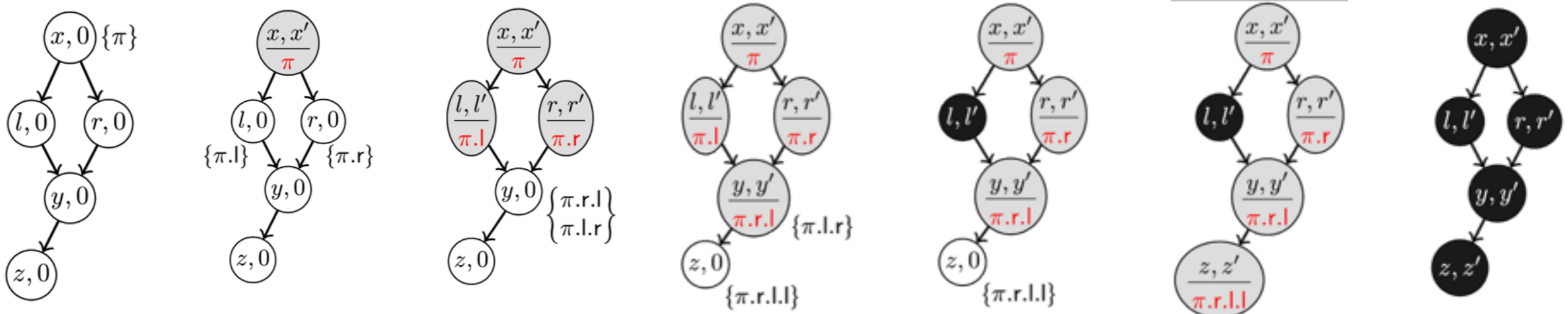
$$\text{ic}(x, x', \delta, \delta_c) \triangleq (x=0 \wedge x'=0) \vee \\ \left(x \neq 0 \wedge \left[(x'=0 \wedge \delta^c(x) = x' \wedge \exists y. \delta^p(y) \neq \emptyset \wedge y \xrightarrow{\delta}_0^* x) \right. \right. \\ \vee (x' \neq 0 \wedge x' \in \delta' \wedge \exists \pi, l, r, l', r'. \delta(x) = ((x', \pi, -), l, r) \wedge \delta'(x') = (-, l', r') \\ \wedge (l' \neq 0 \Rightarrow \text{ic}(l, l', \delta, \delta')) \wedge (r' \neq 0 \Rightarrow \text{ic}(r, r', \delta, \delta'))) \\ \left. \vee (x' \neq 0 \wedge x' \in \delta' \wedge \exists l, r, l', r'. \delta(x) = ((x', 0, -), l, r) \wedge \delta'(x') = (-, l', r') \right. \\ \left. \wedge \text{ic}(l, l', \delta, \delta') \wedge \text{ic}(r, r', \delta, \delta') \right) \left. \right]$$



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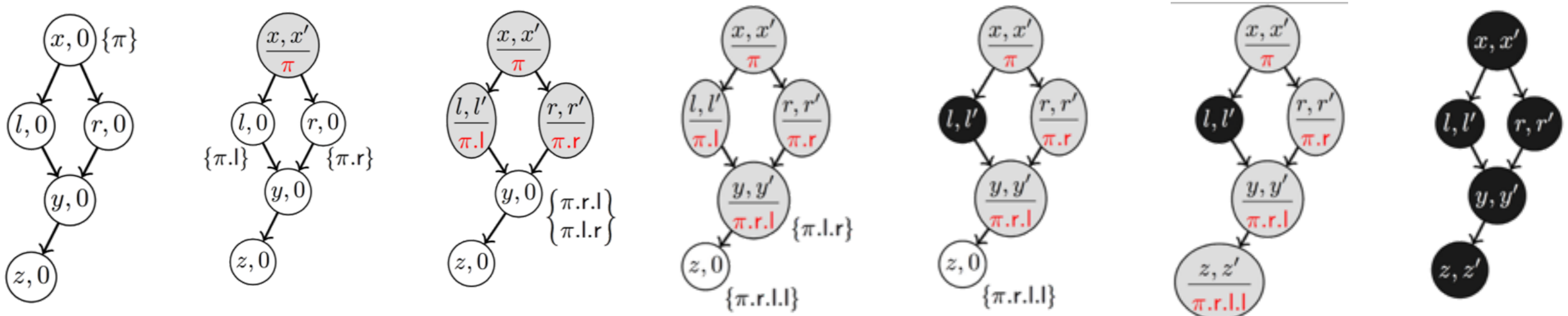
$P^\pi(x, \delta) \triangleq \pi$ has made a promise to visit x ; π has made a promise to x only; and π has not spawned any threads yet:
 its subthreads are not in the graph (in promise sets or as copying thread)



4. Mathematical Specification

$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

$P^\pi(x, \delta) \triangleq x \neq 0 \Rightarrow \pi \in \text{promise}(x) \wedge \pi$ has made a promise to x only; and
 π has not spawned any threads yet;
 its subthreads are not in the graph (in promise sets or as copying thread)



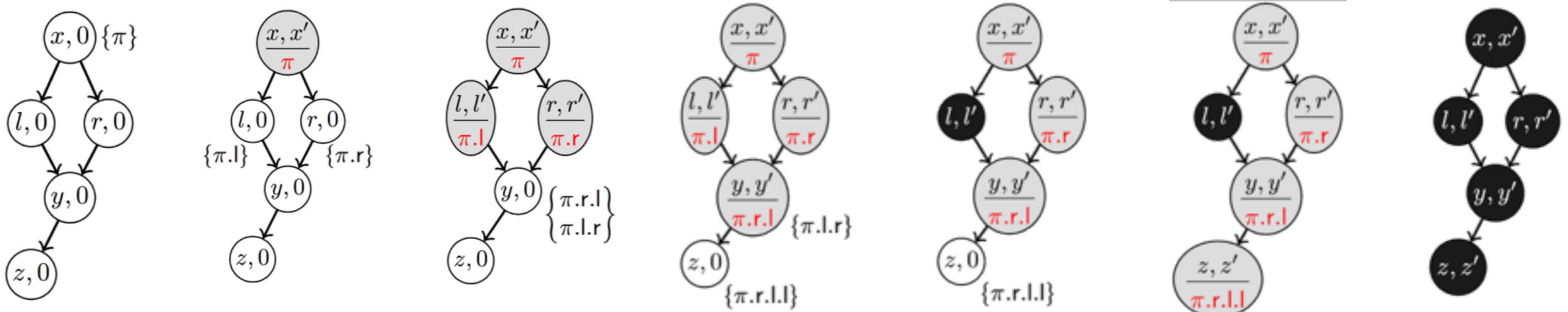
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$$P^\pi(x, \delta) \triangleq x \neq 0 \Rightarrow \pi \in \text{promise}(x) \wedge \forall z \in \delta. \pi \in \text{promise}(z) \Rightarrow x = z$$

π has not spawned any threads yet:

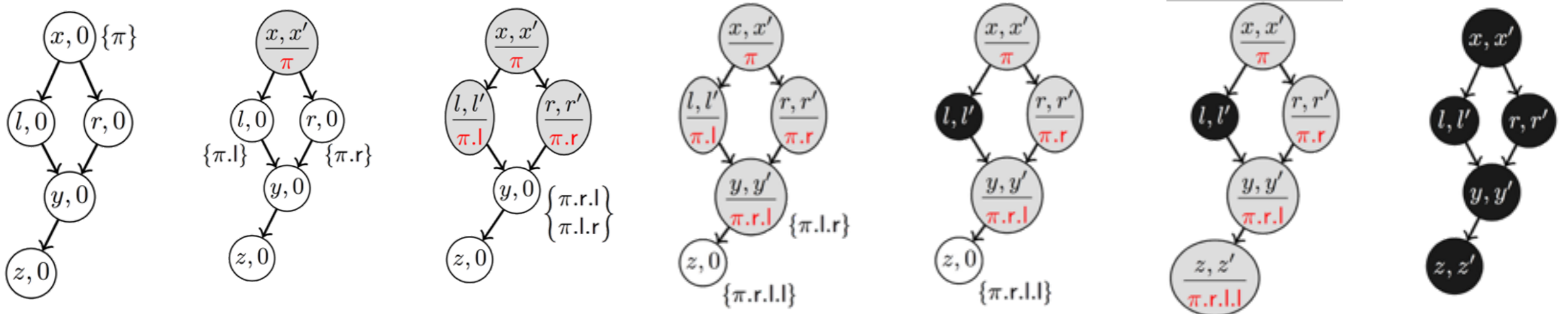
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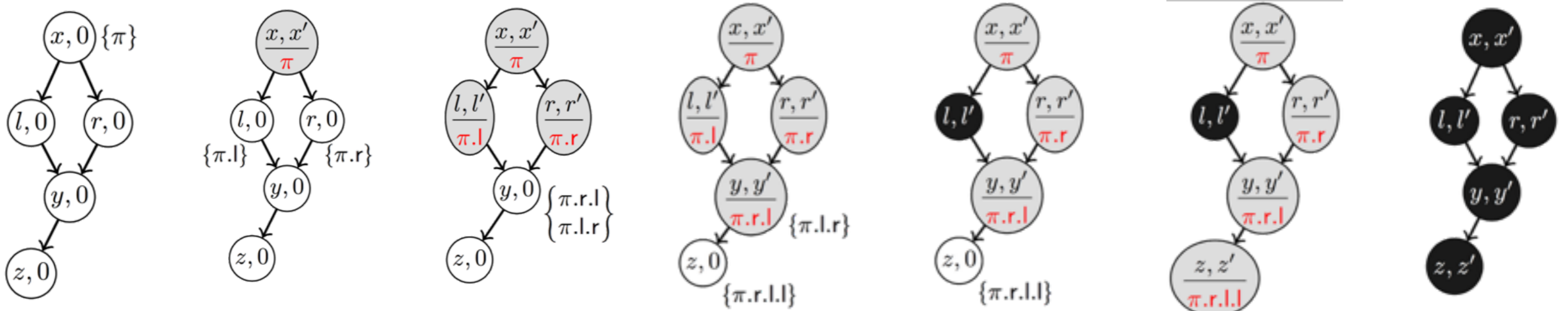
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$Q^\pi(x, x', \delta, \delta_c) \triangleq x$ is copied to x' in δ_c ; and

π and all its subthreads have finished executing (have joined):
they are not in the graph (in promise sets or as copying thread)



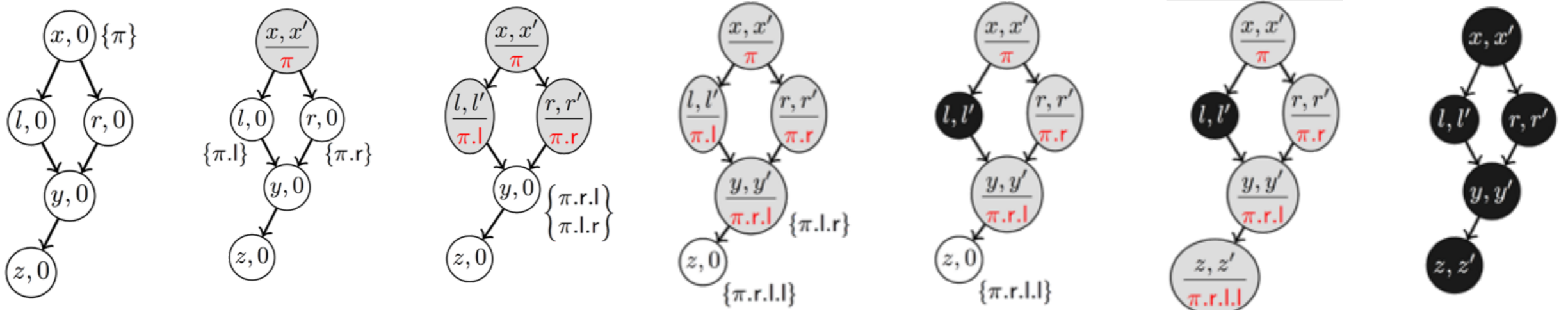
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$$Q^\pi(x, x', \delta, \delta_c) \triangleq \text{copy}(x) = x' \wedge x' \in \delta_c \wedge$$

π and all its subthreads have finished executing (have joined):
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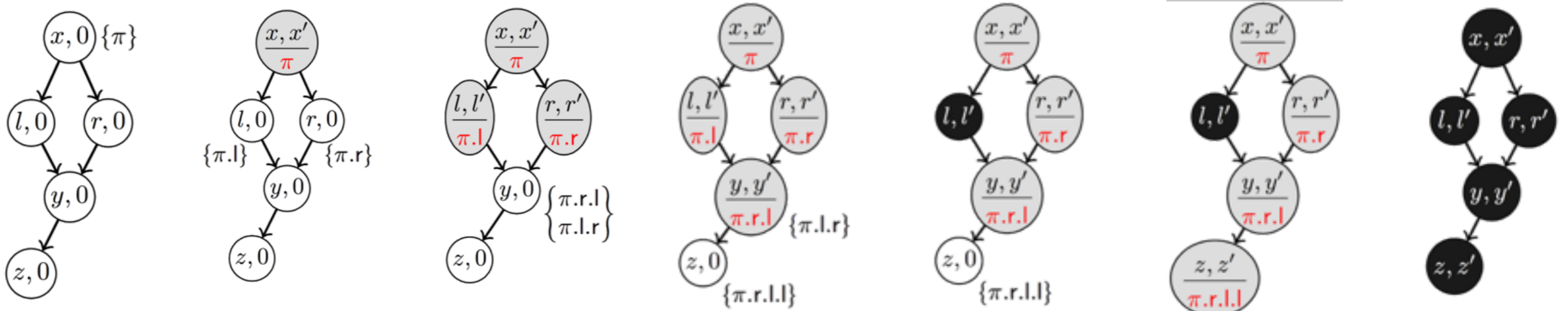


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$$Q^\bullet(x, x', \delta, \delta_c) \triangleq \text{copy}(x) = x' \wedge x' \in \delta_c \wedge \\ \forall z \in \delta. \forall \pi' \sqsubseteq \bullet. \pi' \notin \text{promise}(z) \wedge \pi' \neq \text{thread}(x)$$

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$$Q^\bullet(x, x', \delta, \delta_c) \triangleq \text{copy}(x) = x' \wedge x' \in \delta_c \wedge \\ \forall z \in \delta. \forall \pi' \sqsubseteq \bullet. \pi' \notin \text{promise}(z) \wedge \pi' \neq \text{thread}(x)$$

$$\forall \pi. \pi \sqsubseteq \bullet$$

4. Mathematical Specification

$$\text{Inv}(\delta, \delta_c) \triangleq \text{acyc}(\delta) \wedge \text{acyc}(\delta_c) \wedge \forall x' \in \delta_c. \exists! x \in \delta. \text{copy}(x) = x' \\ \wedge \forall x \in \delta. \exists x'. \text{copy}(x) = x' \wedge \text{ic}(x, x', \delta, \delta_c)$$

$$Q^\bullet(x, x', \delta, \delta_c) \triangleq \text{copy}(x) = x' \wedge x' \in \delta_c \wedge \\ \forall z \in \delta. \forall \pi'. \quad \pi' \notin \text{promise}(z) \wedge \pi' \neq \text{thread}(x)$$

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$\text{ic}(x, x', \delta, \delta_c) \triangleq$ if x' is 0 (x is not copied yet), then x will eventually be copied:

there exists some y in δ s.t.

- 1) the promise set of y is non-empty; 2) y can reach x along a path p ;
- and 3) every node along the path p is not copied

otherwise, x' is a node in δ_c and the children of x are also copied

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$\text{ic}(x, x', \delta, \delta_c) \triangleq x'$ is a node in δ_c and the children of x are also copied

$Q^\bullet(x, x', \delta, \delta_c) \wedge \text{ic}(x, x', \delta, \delta_c) \Rightarrow$ all nodes in δ are copied to nodes in δ_c

5. Spatial Objects

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- A concrete implementation of the data structures in the heap

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recorded in the *ghost heap*; not “*baked in*” to model

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- each $\text{dag}(\delta)$ implemented as a collection of nodes:

$$\text{dag}(\delta) \triangleq *_{x \in \delta} \text{node}(x, \delta)$$

$$\text{node}(x, (V, E, L)) \triangleq \exists l, r, x', P, \pi. E(x) = l, r \wedge L(x) = x', \pi, P \wedge x \mapsto x', l, r * x \Rightarrow \pi, P$$

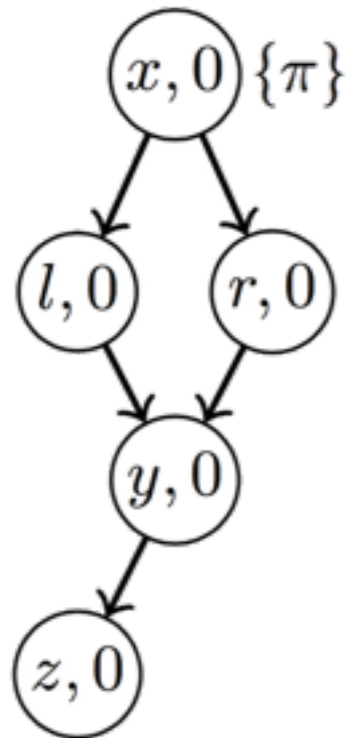
6. Spatial Actions

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 - atomic blocks as well as ghost actions

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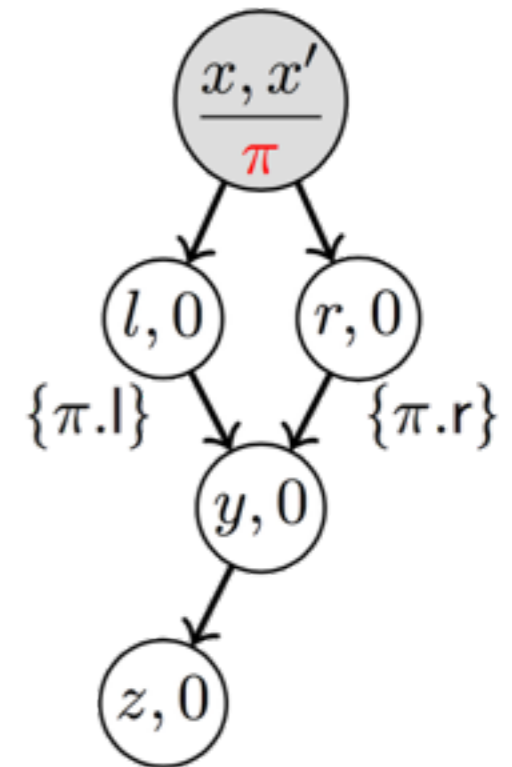
- An implementation of thread actions (on spatial objects)
 - atomic blocks as well as ghost actions



(δ, δ_c)

```

struct
copy_dag(
  struct
  if
  x' =
  b = <CAS(x->c, 0, x')>;
  if
  l = x->l; r = x->r;
  ll = copy_dag(l) || rr = copy_dag(r)
  <x'->l = ll; <x'->r = rr
} else {
}
}
    
```

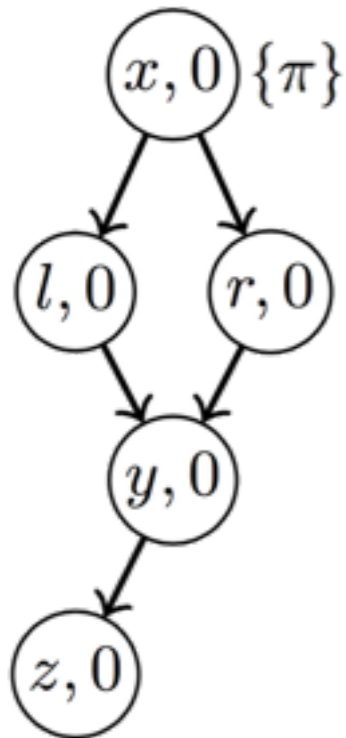


(δ', δ'_c)



6. Spatial Actions

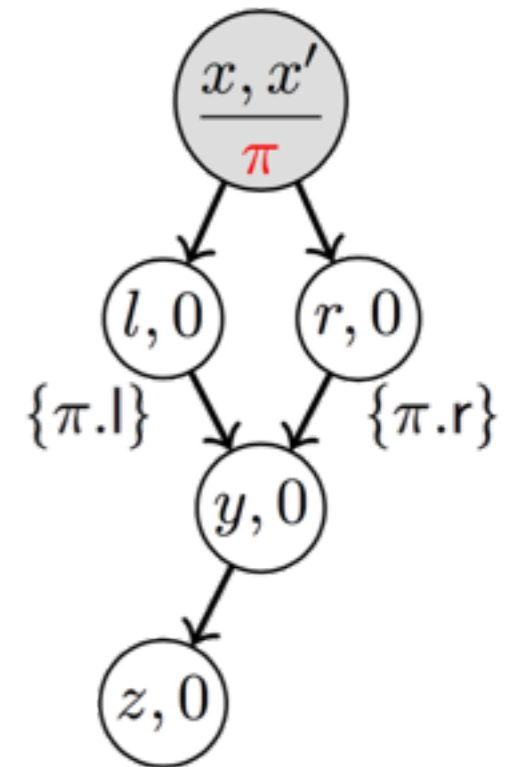
- An implementation of thread actions (on spatial objects)
 - atomic blocks as well as ghost actions
 - Lifting of mathematical actions A^π to spatial ones $[A^\pi]$



(δ, δ_c)

```

struct
copy_dag(
  struct
  if
  x' =
  b = <CAS(x->c, 0, x')>;
  if
  l = x->l; r = x->r;
  ll = copy_dag(l) || rr = copy_dag(r)
  <x'->l = ll; <x'->r = rr
} else {
}
}
    
```

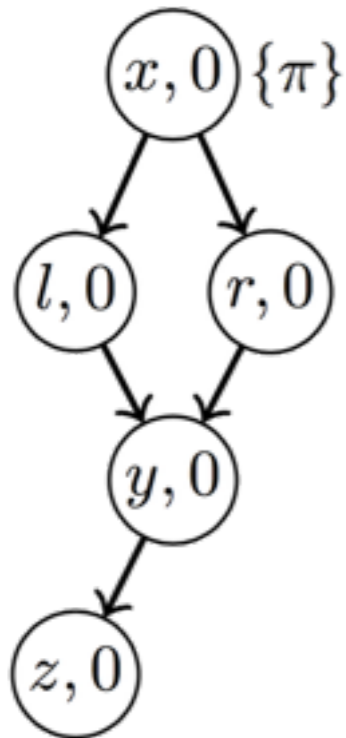


(δ', δ'_c)



6. Spatial Actions

- An implementation of thread actions (on spatial objects)
 - atomic blocks as well as ghost actions
 - Lifting of mathematical actions A^π to spatial ones $[A^\pi]$

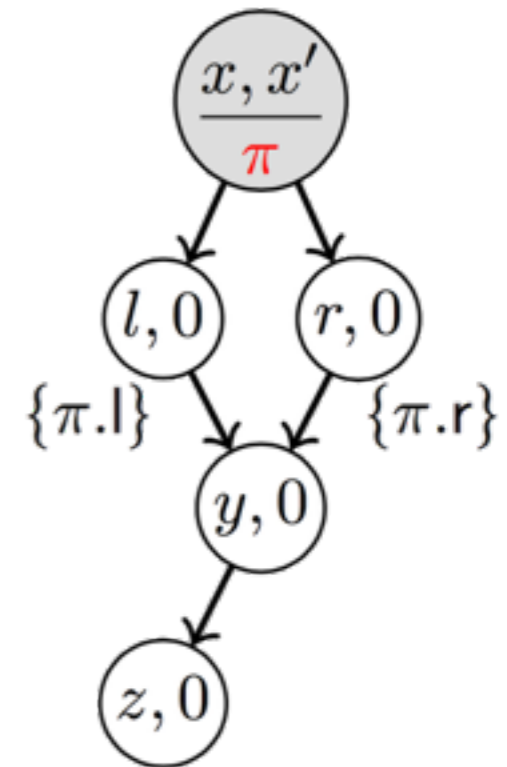


(δ, δ_c)

$\text{icdag}(\delta, \delta_c)$

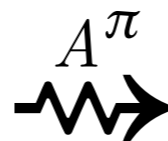
```

struct
copy_dag(
  struct
  if
  x' =
  b = <CAS(x->c, 0, x')>;
  if
  l = x->l; r = x->r;
  ll = copy_dag(l) || rr = copy_dag(r)
  <x'->l = ll; <x'->r = rr
} else {
}
}
    
```



(δ', δ'_c)

$\text{icdag}(\delta', \delta'_c)$



Verifying copy_dag(x)

```

struct node {struct node *c, *l, *r};
{Pre(x, π, δ)}
copy_dag(struct node *x) {struct node *l, *r, *ll, *rr, *y; bool b;
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Px(x, δ1)) }
  if(!x){ return 0; }
{π* ret=0 * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, ret, δ1, δ2)) }
  y = malloc(sizeof(struct node));
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Px(x, δ1)) * y ↦ 0, 0, 0 * y ⇒ π, ∅ }
  <if(x->c){ b = false; //Perform the action Aπ5
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, δ1c(x), δ1, δ2) ∧ δ1c(x) ≠ 0) }
* y ↦ 0, -, - * y ⇒ π, ∅ * b=0
} else{ x->c = y; b = true; //Perform the action Aπ1
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ ∃l, r. δ1(x) = (y, π, -, l, r) ∧ y ∈ δ2 ∧ Pπ, l(l, δ1) ∧ Pπ, r(r, δ1)) * b=1 }
}>
  if(b){ l = x->l; r = x->r;
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, π, -, l, r) ∧ y ∈ δ2 ∧ Pπ, l(l, δ1) ∧ Pπ, r(r, δ1)) }
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2) }
* π.l * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Pπ, l(l, δ1)) }
* π.r * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Pπ, r(r, δ1)) }
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y)
∧ (x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2) * {Pre(l, π.l, δ)
* Pre(r, π.r, δ)}
ll = copy_dag(l) || rr = copy_dag(r)
{Post(l, ll, π.l, δ) || {Post(r, rr, π.r, δ)}
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y)
∧ (x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2) * {Post(l, ll, π.l, δ)
* Post(r, rr, π.r, δ)}
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2 ∧ Qπ, l(l, ll, δ1, δ2) ∧ Qπ, r(r, rr, δ1, δ2)) }
<y->l = ll; <y->r = rr; //Perform Aπ2, Aπ3 and Aπ4 in order.
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, y, δ1, δ2)) }
  return y; {π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, ret, δ1, δ2)) }
} else{
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, δ1c(x), δ1, δ2) ∧ δ1c(x) ≠ 0) * y ↦ 0, -, - }
* y ⇒ π, ∅ }
  free(y, sizeof(struct node)); return x->c;
{π* ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, ret, δ1, δ2)) }
} } {Post(x, ret, π, δ)}

```

Changes reflected in the pure (mathematical) part as highlighted

Verifying copy_dag(x)

```

struct node {struct node *c, *l, *r};
{Pre(x, π, δ)}
copy_dag(struct node *x) {struct node *l, *r, *ll, *rr, *y; bool b;
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Px(x, δ1)) }
  if(!x){ return 0; }
{π* ret=0 • ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, ret, δ1, δ2)) }
  y = malloc(sizeof(struct node));
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Px(x, δ1)) • y ↦ 0, 0, 0 • y ⇒ π, ∅ }
  <if(x->c){ b = false; //Perform the action Aπ5
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, δ1c(x), δ1, δ2) ∧ δ1c(x) ≠ 0) }
  • y ↦ 0, -, - • y ⇒ π, ∅ • b=0
  }else{ x->c = y; b = true; //Perform the action Aπ1
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ ∃l, r. δ1(x) = (y, π, -, l, r) ∧ y ∈ δ2 ∧ Pl(l, δ1) ∧ Pr(r, δ1)) • b=1 }
  }>
  if(b){ l = x->l; r = x->r;
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, π, -, l, r) ∧ y ∈ δ2 ∧ Pl(l, δ1) ∧ Pr(r, δ1)) }
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
(x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2) }
  • π.l • ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Pl(l, δ1)) }
  • π.r • ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Pr(r, δ1)) }
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y)
  ∧ (x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2) • Pre(l, π.l, δ)
  • Pre(r, π.r, δ) }
  ll = copy_dag(l) || rr = copy_dag(r)
  {Post(l, ll, π.l, δ) || Post(r, rr, π.r, δ)}
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y)
  ∧ (x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2) • Post(l, ll, π.l, δ)
  • Post(r, rr, π.r, δ) }
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∉ δ1p(y) ∧
  (x ≠ y ⇒ π ≠ δ1c(y)) ∧ δ1(x) = (y, -, π, l, r) ∧ y ∈ δ2 ∧ Ql(l, ll, δ1, δ2) ∧ Qr(r, rr, δ1, δ2)) }
  <y->l = ll; <y->r = rr; //Perform Aπ2, Aπ3 and Aπ4 in order.
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, y, δ1, δ2)) }
  return y; {π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, ret, δ1, δ2)) }
  }else{
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, δ1c(x), δ1, δ2) ∧ δ1c(x) ≠ 0) • y ↦ 0, -, - }
  • y ⇒ π, ∅ }
  free(y, sizeof(struct node)); return x->c;
{π* ∃δ1, δ2. icdag(δ1, δ2) • (δ ≐ δ1 ∧ Inv(δ1, δ2) ∧ Qx(x, ret, δ1, δ2)) }
  } } {Post(x, ret, π, δ)}

```

Changes reflected in the pure (mathematical) part as highlighted

The spatial part appears unchanged as highlighted

Verifying `copy_dag(x)`

```

struct node {struct node *c, *l, *r};
{Pre(x, π, δ)}
copy_dag(struct node *x) {struct node *l, *r, *ll, *rr, *y; bool b;
  icdag(δ1, δ2) }
  if(!x) return 0;
  {π * ret=0 * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qc(x, ret, δ1, δ2)) }
  y = malloc(sizeof(struct node));
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Pc(x, δ1)) * y ↦ 0, 0, 0 * y ↦ π, ∅ }
  <if(x->c){ b = false; //Perform the action Aπ5
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qc(x, δ1c(x), δ1, δ2) ∧ δ1c(x) ≠ 0) }
  * y ↦ 0, -, - * y ↦ π, ∅ * b=0
  }else{ x->c = y; b = true; //Perform the action Aπ1
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∈ δ1c(y) ∧
  (x+y ↦ π+δ1c(y)) ∧ ∃l, r. δ1c(x)=(y, π, -, l, r) ∧ y ∈ δ2 ∧ Pc,l(l, δ1) ∧ Pc,r(r, δ1)) * b=1 }
  }>
  if(b){ l = x->l; r = x->r;
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∈ δ1c(y) ∧
  (x+y ↦ π+δ1c(y)) ∧ δ1c(x)=(y, π, -, l, r) ∧ y ∈ δ2 ∧ Pc,l(l, δ1) ∧ Pc,r(r, δ1)) }
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∈ δ1c(y) ∧
  (x+y ↦ π+δ1c(y)) ∧ δ1c(x)=(y, -, π, l, r) ∧ y ∈ δ2) }
  * π.l * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Pc,l(l, δ1)) }
  * π.r * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Pc,r(r, δ1)) }
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∈ δ1c(y)
  ∧ (x+y ↦ π+δ1c(y)) ∧ δ1c(x)=(y, -, π, l, r) ∧ y ∈ δ2) * Pre(l, π.l, δ)
  * Pre(r, π.r, δ) }
  ll = copy_dag(l) | rr = copy_dag(r)
  {Post(l, ll, π.l, δ) | Post(r, rr, π.r, δ)}
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∈ δ1c(y)
  ∧ (x+y ↦ π+δ1c(y)) ∧ δ1c(x)=(y, -, π, l, r) ∧ y ∈ δ2) * Post(l, ll, π.l, δ)
  * Post(r, rr, π.r, δ) }
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ ∀y ∈ δ1. π ∈ δ1c(y) ∧
  (x+y ↦ π+δ1c(y)) ∧ δ1c(x)=(y, -, π, l, r) ∧ y ∈ δ2 ∧ Qc,l(l, ll, δ1, δ2) ∧ Qc,r(r, rr, δ1, δ2)) }
  <y->l = ll; <y->r = rr; //Perform Aπ2, Aπ3 and Aπ4 in order.
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qc(x, y, δ1, δ2)) }
  return y; {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qc(x, ret, δ1, δ2)) }
  }else{
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qc(x, δ1c(x), δ1, δ2) ∧ δ1c(x) ≠ 0) * y ↦ 0, -, - }
  free(y, sizeof(struct node)); return x->c;
  {π * ∃δ1, δ2. icdag(δ1, δ2) * (δ ≃ δ1 ∧ Inv(δ1, δ2) ∧ Qc(x, ret, δ1, δ2)) }
  } } {Post(x, ret, π, δ)}

```

Changes reflected in the pure (mathematical) part as highlighted

The spatial part appears unchanged as highlighted

Conclusions

- ☑ Verified 4 concurrent fine-grained graph algorithms
 - ☑ Copying dags (directed acyclic graphs)
 - ☑ Speculative variant of Dijkstra's shortest path
 - ☑ Computing the spanning tree of a graph
 - ☑ Marking a graph

- ☑ Presented a common proof pattern for graph algorithms
 - ☑ *Abstract mathematical* graphs for Functional correctness
 - ☑ *Concrete Spatial (heap-represented)* graphs for memory safety
 - ☑ Combined reasoning for full proof
 - ☑ Inspired by existing logics where this pattern is “baked-in” to the model
 - ☑ “Baking-in” is unnecessary; demonstrated by CoLoSL reasoning

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Thank you for listening!

Speculative Concurrent Shortest Path

```
parallel_dijkstra((int[][] a, int[] c, int size, src) {
    bitarray work[size], done[size];
    for (i=0; i<size; i++){
        c[i] = a[src][i]; work[i] = 1; done[i] = 0; //initialisation
    }; c[src] = 0;
    dijkstra(a,c,size,work,done) || ... || dijkstra(a,c,size,work,done)
}

dijkstra(int[][] a, int[] c, int size, bitarray work, done){
    i = 0;
    while(done != 2^size-1){
        b = <CAS(work[i], 1, 0)>;
        if(b){ cost = c[i];
            for(j=0; j<size; j++){ newcost = cost + a[i][j]; b = true;
                do{ oldcost = c[j];
                    if(newcost < oldcost){
                        b = <CAS(work[j], 1, 0)>;
                        if(b){ b = <CAS(c[j], oldcost, newcost)>; <work[j] = 1>; }
                        else { b = <CAS(done[j], 1, 0)>;
                            if(b){ b = <CAS(c[j], oldcost, newcost)>;
                                if(b){ < work[j] = 1 > } else { < done[j] = 1 > }
                            } } }
                    } while(!b)
                } < done[i] = 1 >;
            } i = (i+1) mod size;
        } }
}
```