

# Correctness in a Weakly Consistent Setting

**Azalea Raad**

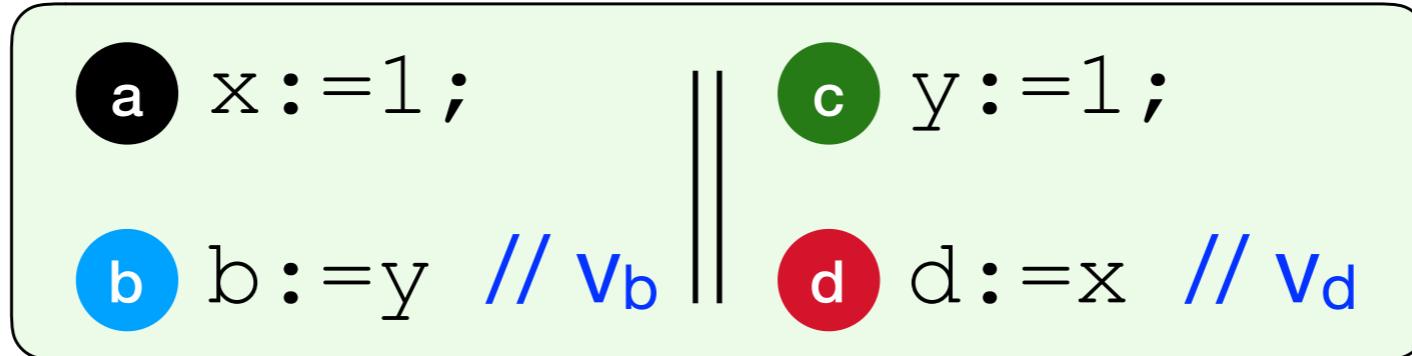
Max Planck Institute for Software Systems (MPI-SWS)

# History

Difficulty



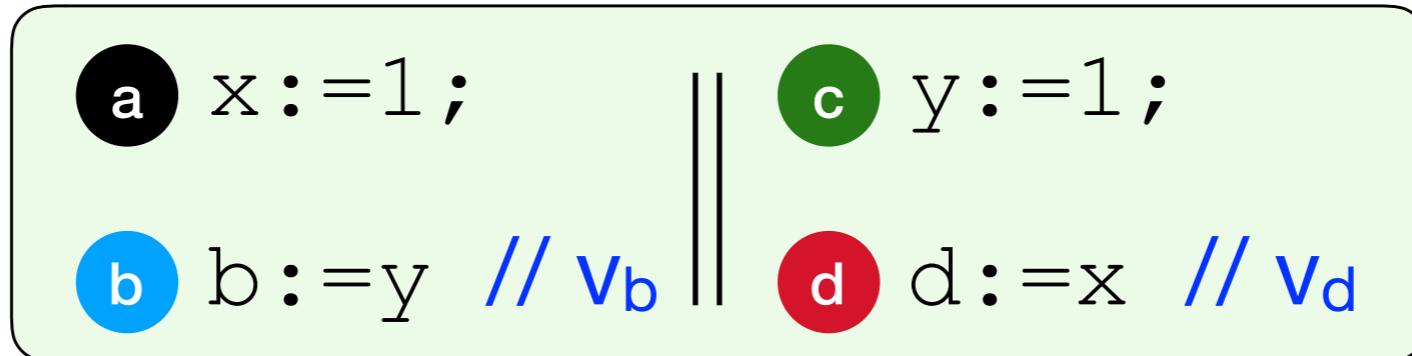
# Sequential Consistency (SC)



SC (a.k.a. interleaving concurrency):

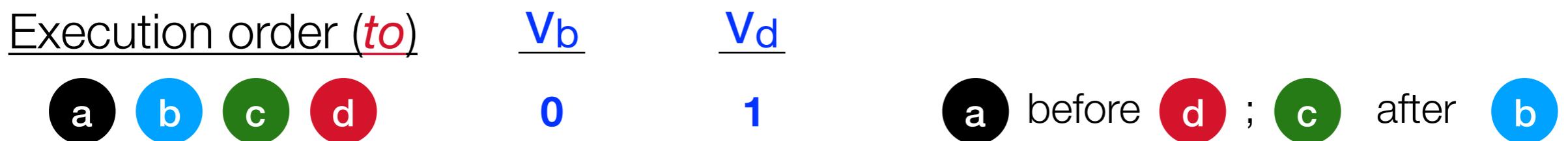
**total** execution order (**to**) that respects program order (**po**)

# Sequential Consistency (SC)

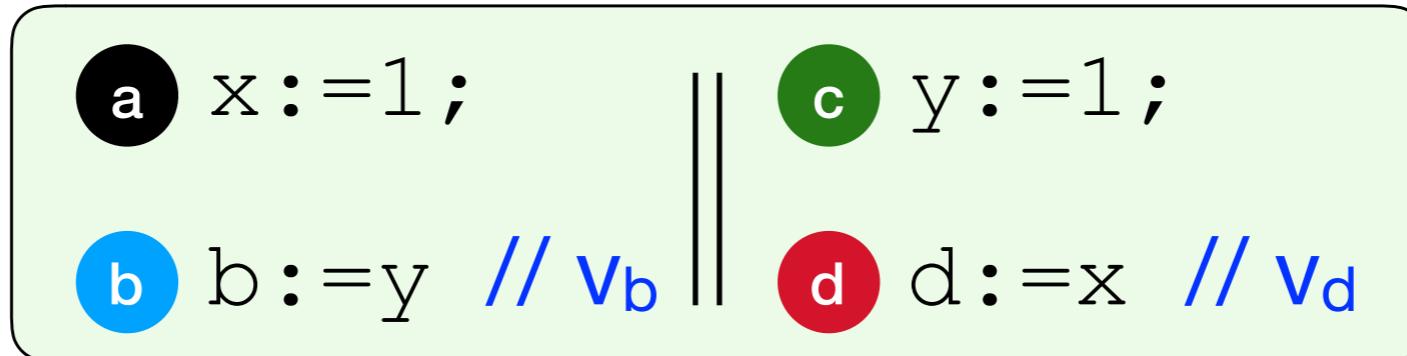


SC (a.k.a. interleaving concurrency):

**total** execution order (*to*) that respects program order (*po*)



# Sequential Consistency (SC)

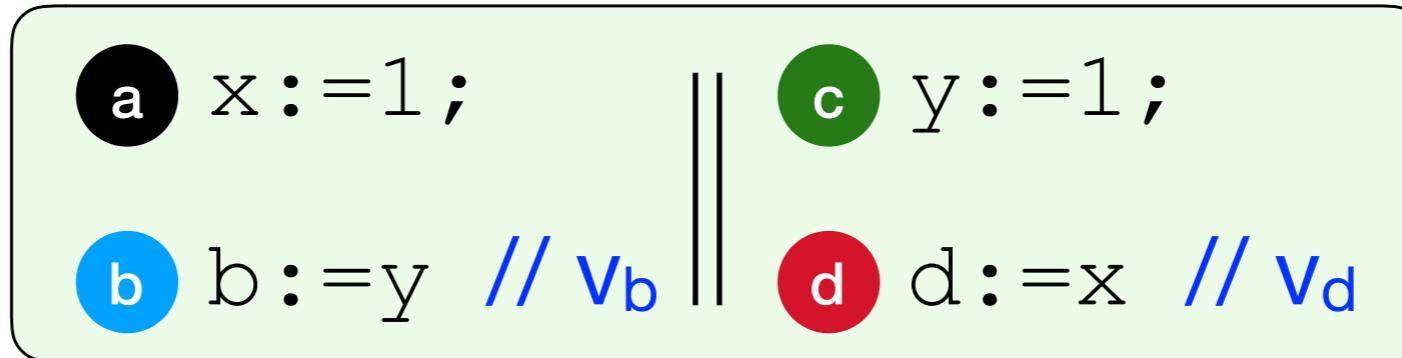


SC (a.k.a. interleaving concurrency):

**total** execution order (*to*) that respects program order (*po*)

Execution order ( <i>to</i> )	<u>v<sub>b</sub></u>	<u>v<sub>d</sub></u>	
a b c d	0	1	a before d ; c after b
c d a b	1	0	a after d ; c before b

# Sequential Consistency (SC)



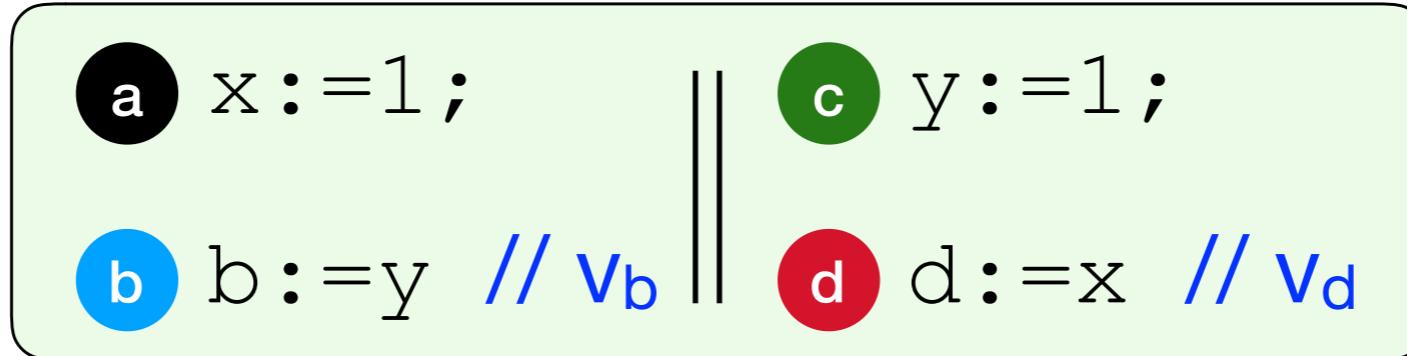
SC (a.k.a. interleaving concurrency):

**total** execution order (*to*) that respects program order (*po*)

Execution order ( <i>to</i> )	$v_b$	$v_d$	
a b c d	0	1	a before d ; c after b
c d a b	1	0	a after d ; c before b
a c b d	1	1	
a c d b	1	1	
c a b d	1	1	a before d ; c before b
c a d b	1	1	

A red bracket is drawn from the bottom of the fourth row to the bottom of the eighth row, grouping the  $v_d$  values of 1 for all six rows.

# Sequential Consistency (SC)



SC (a.k.a. interleaving concurrency):

**total** execution order (*to*) that respects program order (*po*)

<u><math>v_b</math></u>	<u><math>v_d</math></u>	
0	1	a before d ; c after b
1	0	a after d ; c before b
1	1	a before d ; c before b
0	0	not possible!

# Weak Memory Concurrency (WMC)

**No** total execution order (***to***) ⇒

**anomalies (*litmus tests*)**: behaviour absent under SC;  
caused by:

- instruction ***reordering***;
  - e.g. store buffering (SB)

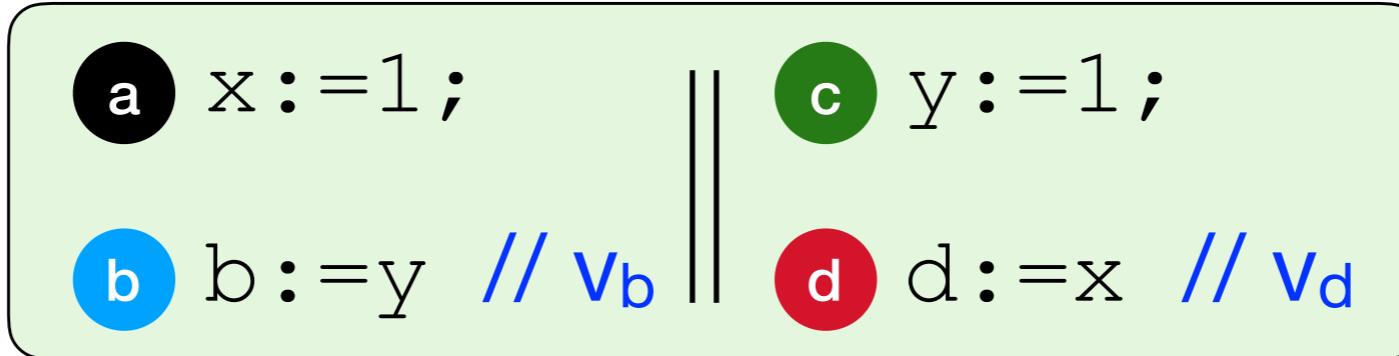
# Weak Memory Concurrency (WMC)

**No** total execution order (**to**)  $\Rightarrow$

**anomalies (litmus tests)**: behaviour absent under SC;  
caused by:

- instruction **reordering**;
  - e.g. store buffering (SB)
- **different write propagation** across cache hierarchy;
  - e.g. Independent Reads of Independent Writes (IRIW)

# WMC: Store Buffering

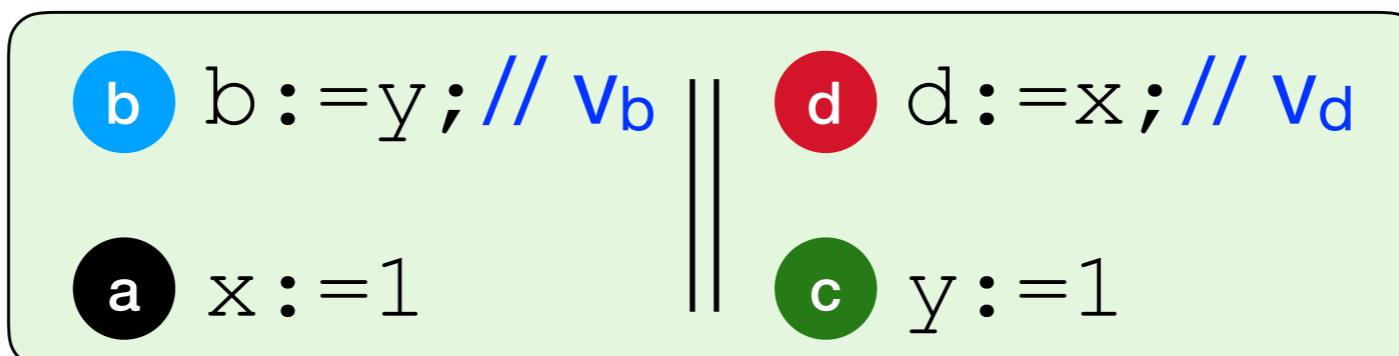


<u>v<sub>b</sub></u>	<u>v<sub>d</sub></u>
0	1
1	0
1	1
0	0

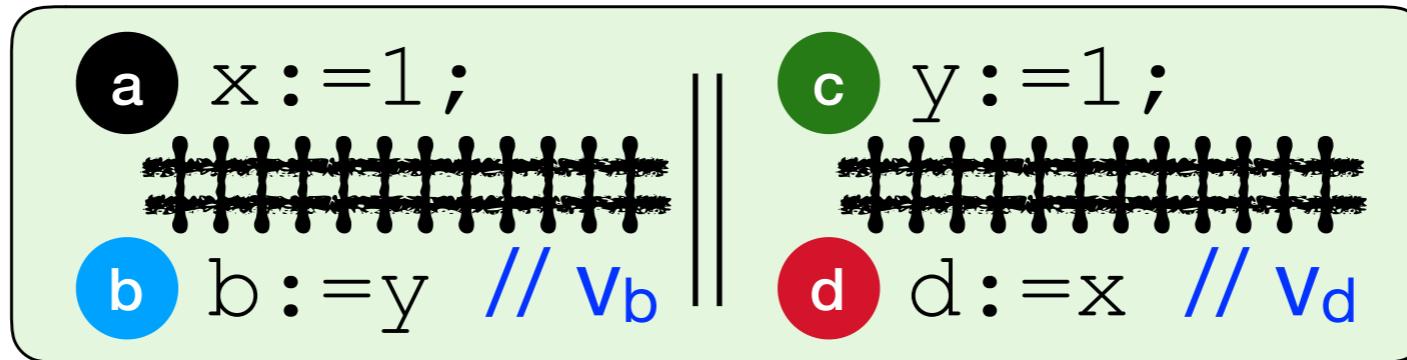
possible, due to reordering!



**store buffering(SB)**



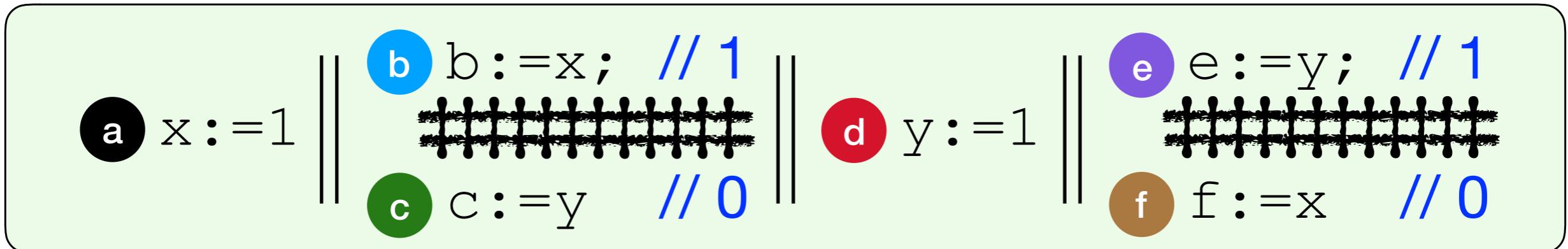
# Fences to Stop Reordering



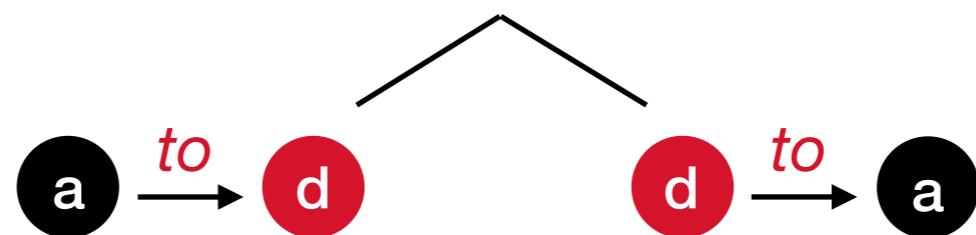
<u>V<sub>b</sub></u>	<u>V<sub>d</sub></u>
0	1
1	0
1	1
0	0

~~possible, due to reordering!~~

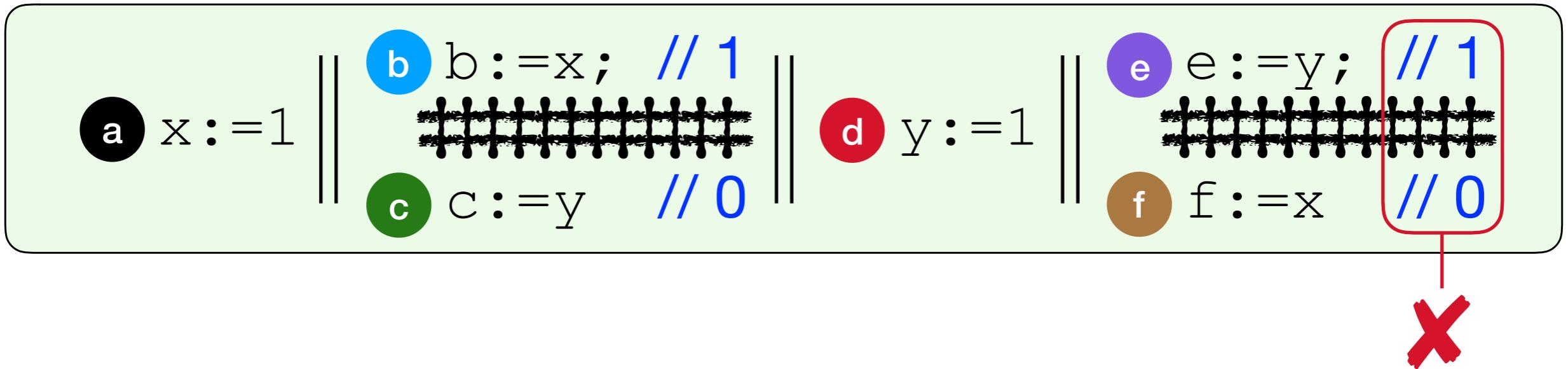
# WMC: Independent Reads of Independent Writes



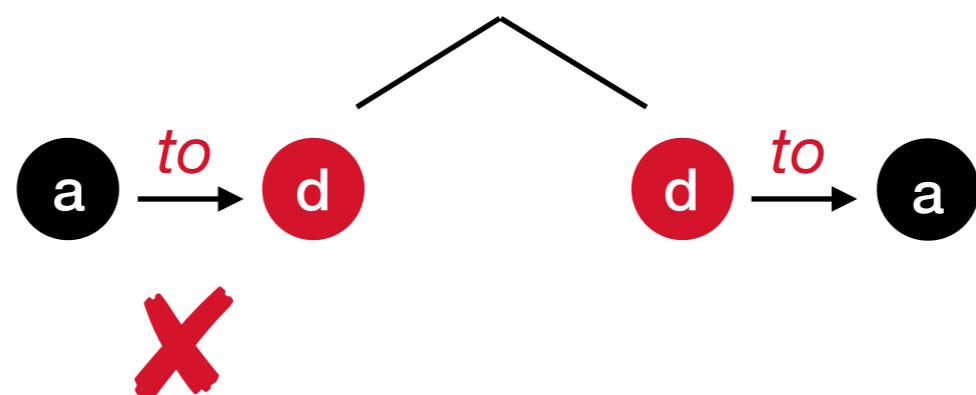
annotated behaviour **not** allowed **under SC** :



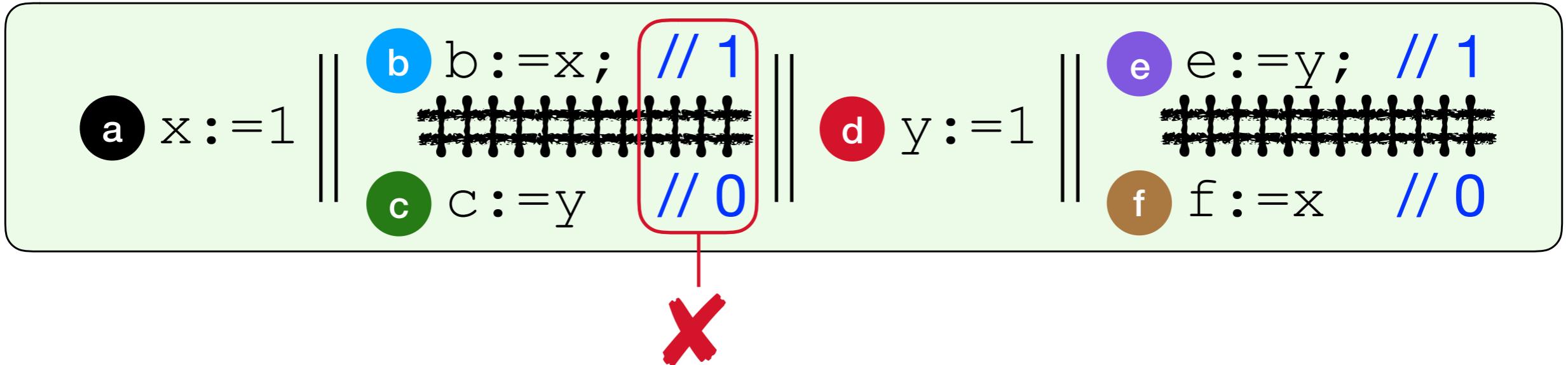
# WMC: Independent Reads of Independent Writes



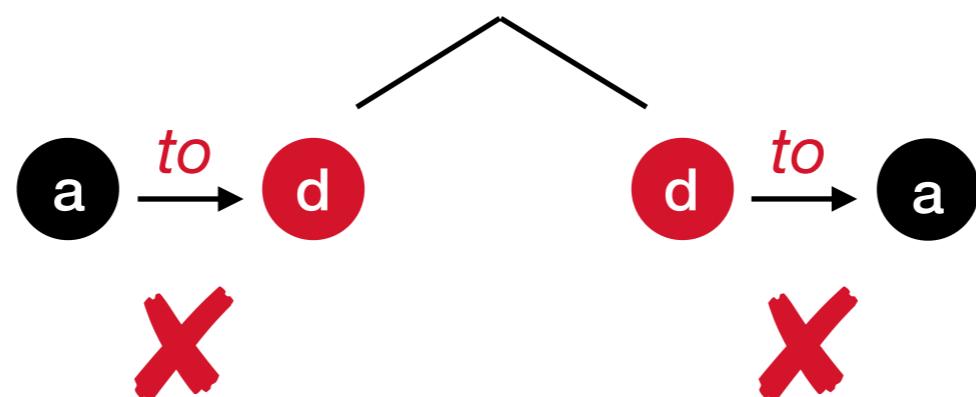
annotated behaviour ***not*** allowed ***under SC*** :



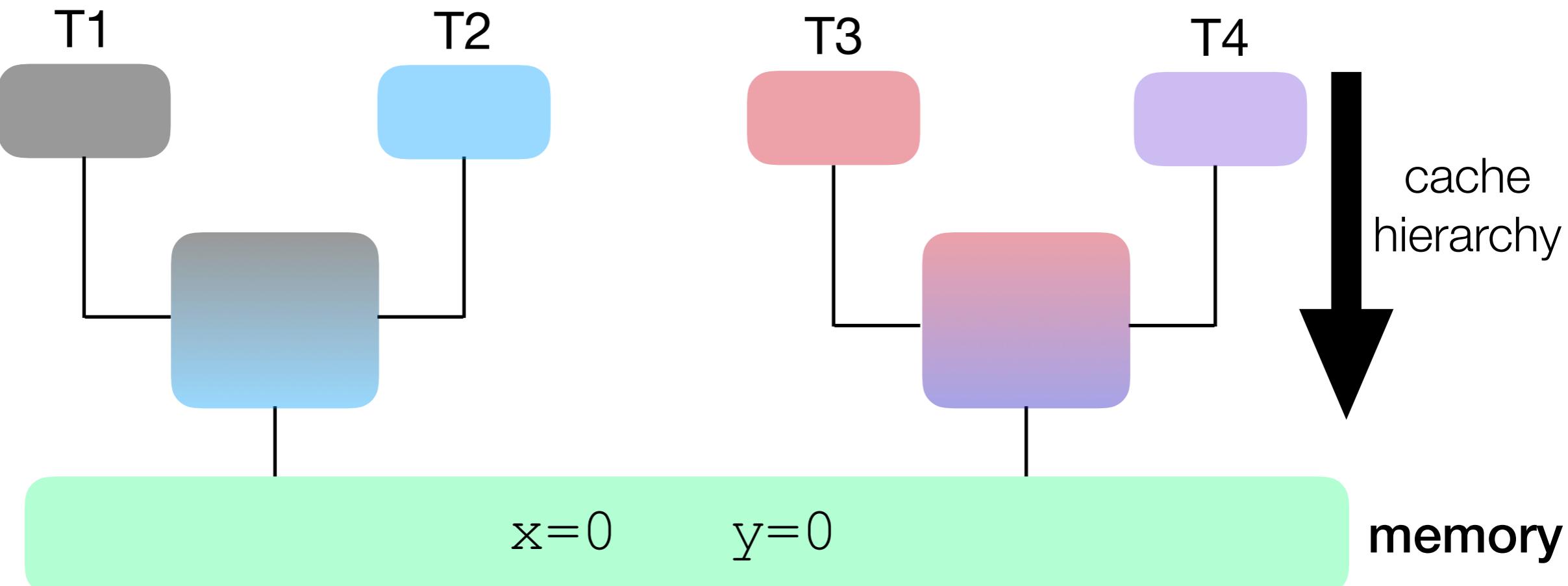
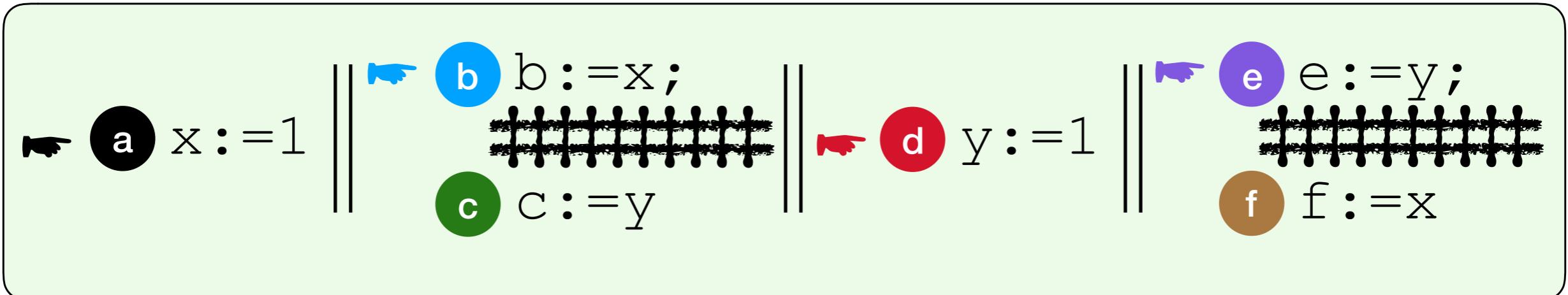
# WMC: Independent Reads of Independent Writes



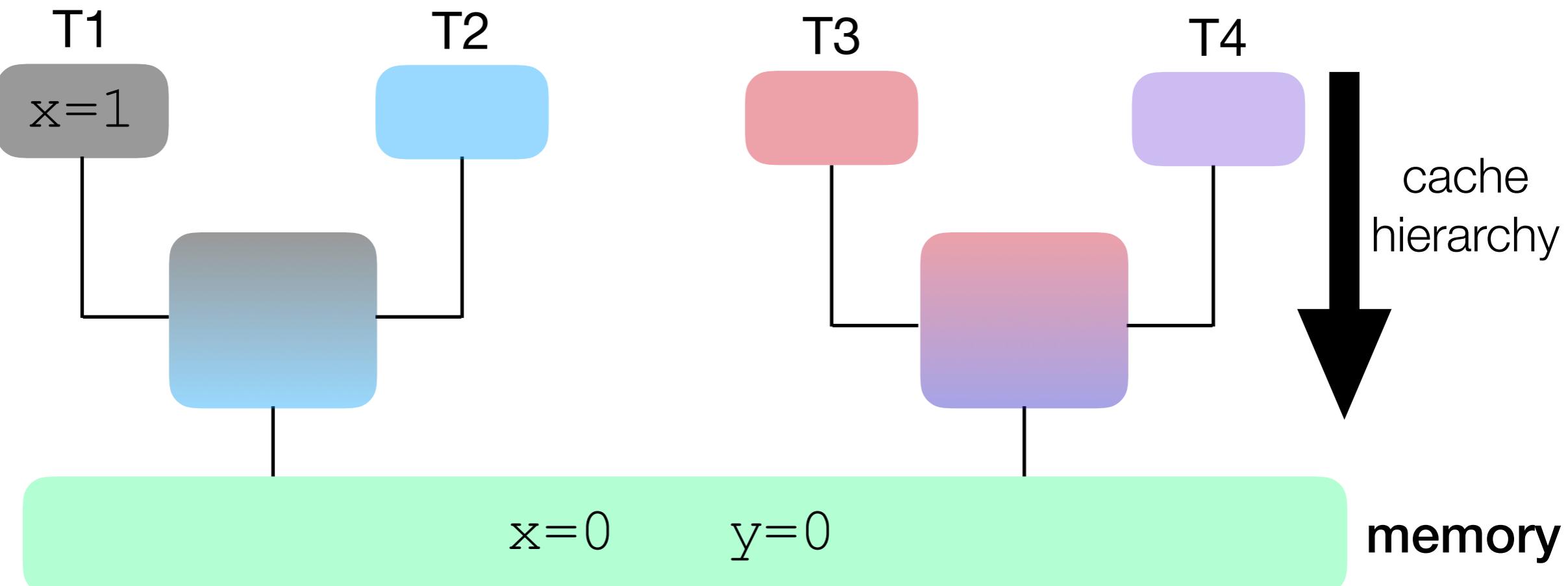
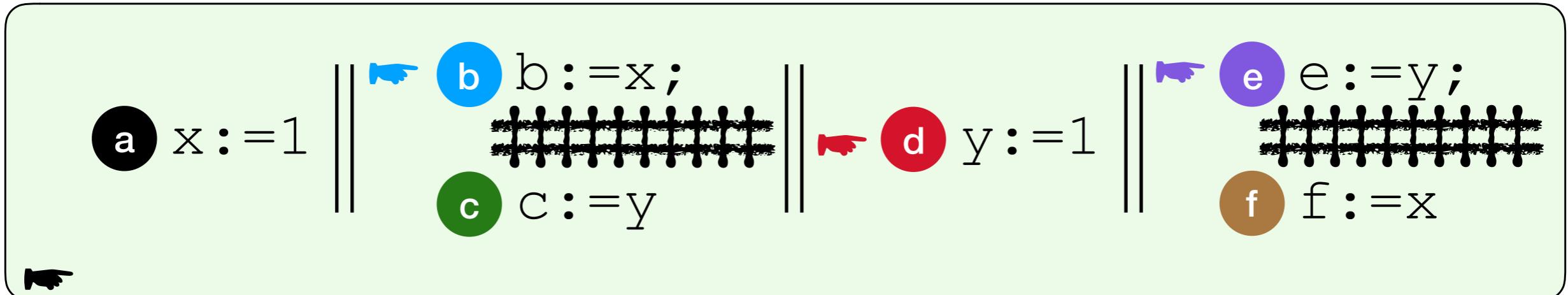
annotated behaviour ***not*** allowed ***under SC*** :



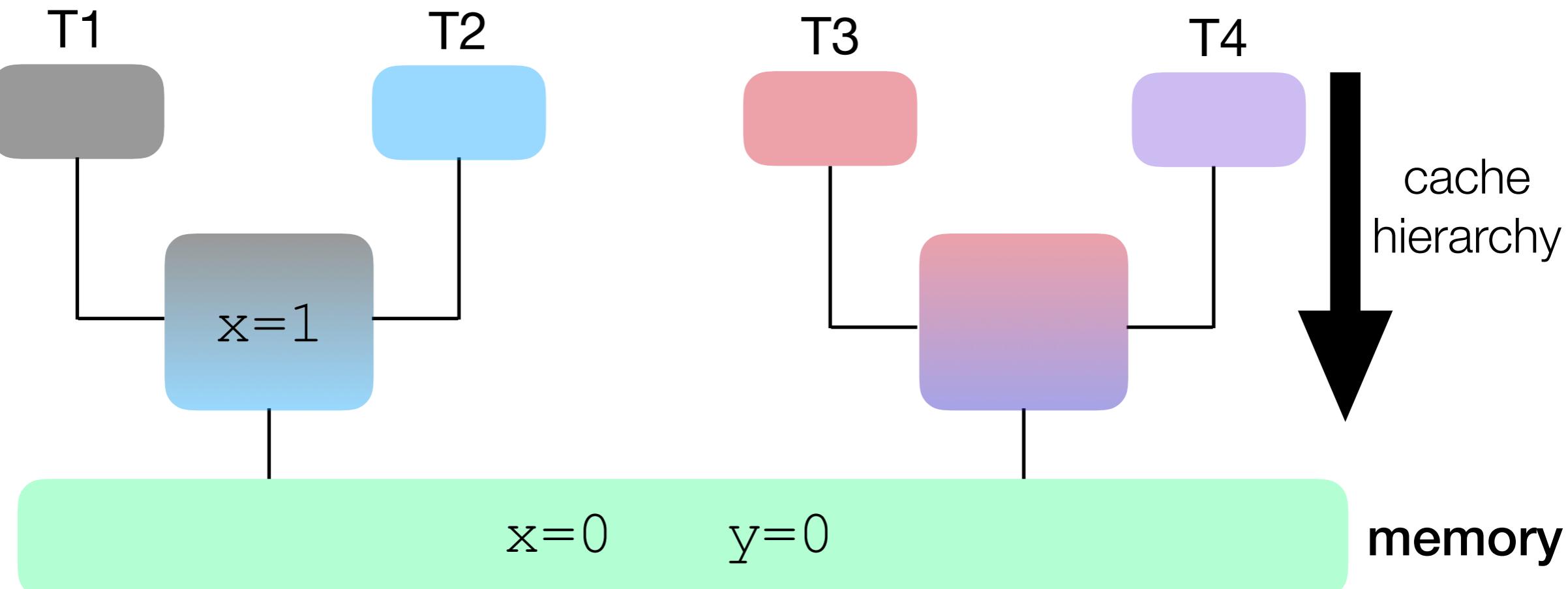
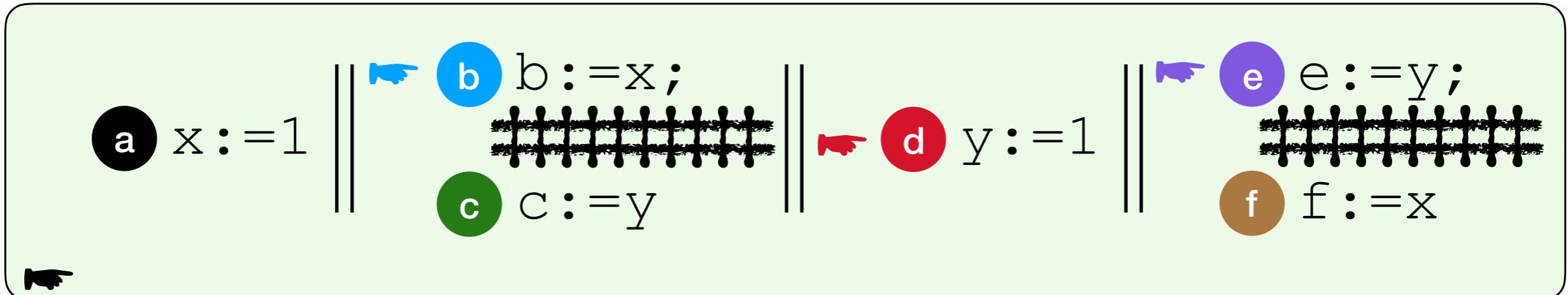
# WMC: Independent Reads of Independent Writes



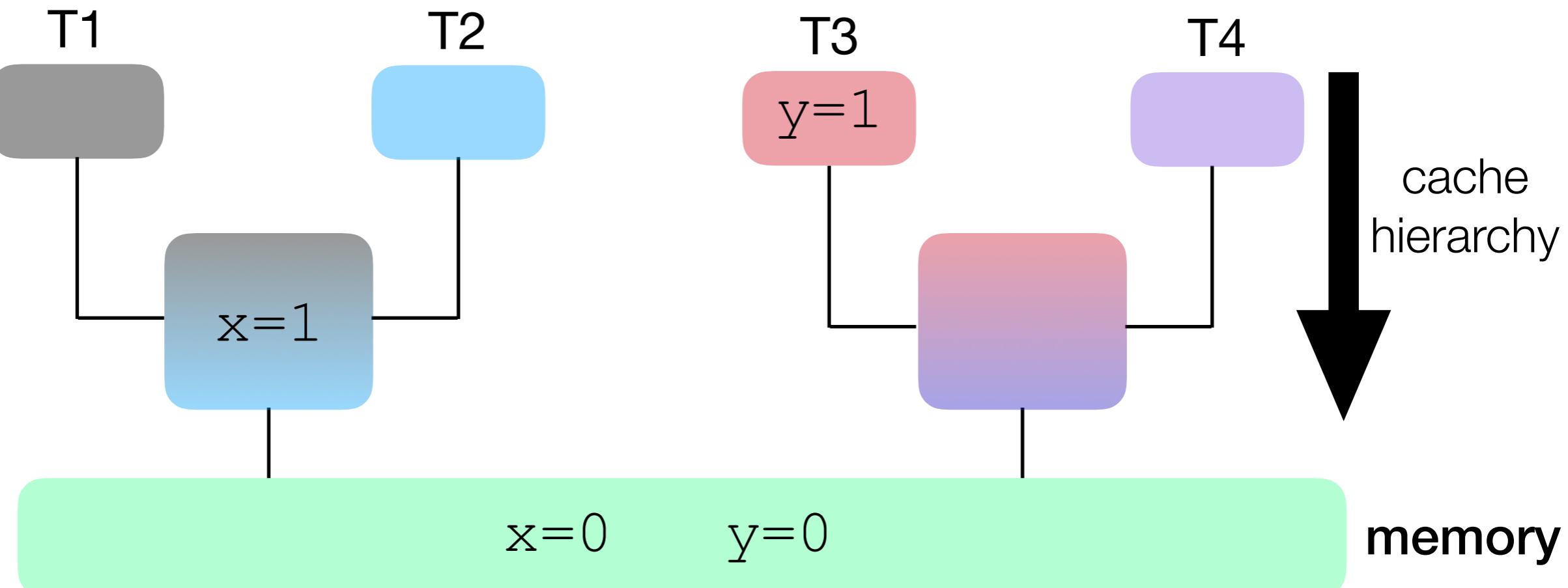
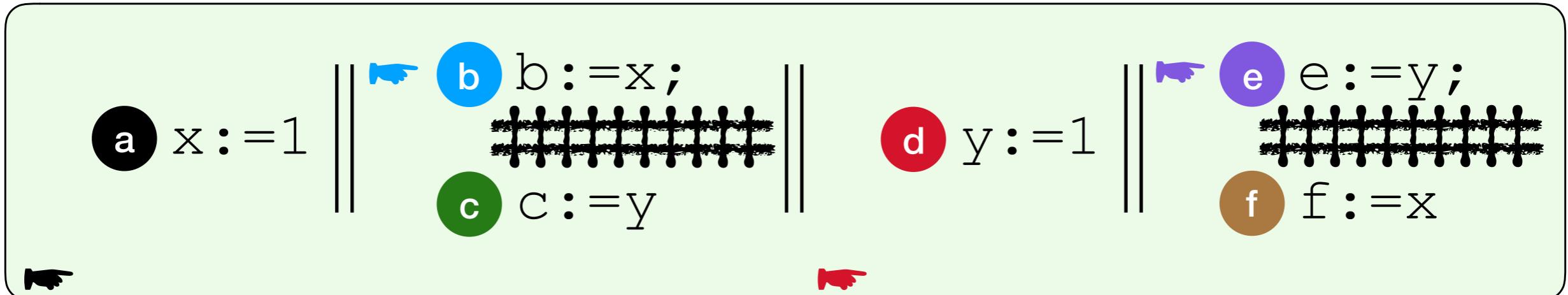
# WMC: Independent Reads of Independent Writes



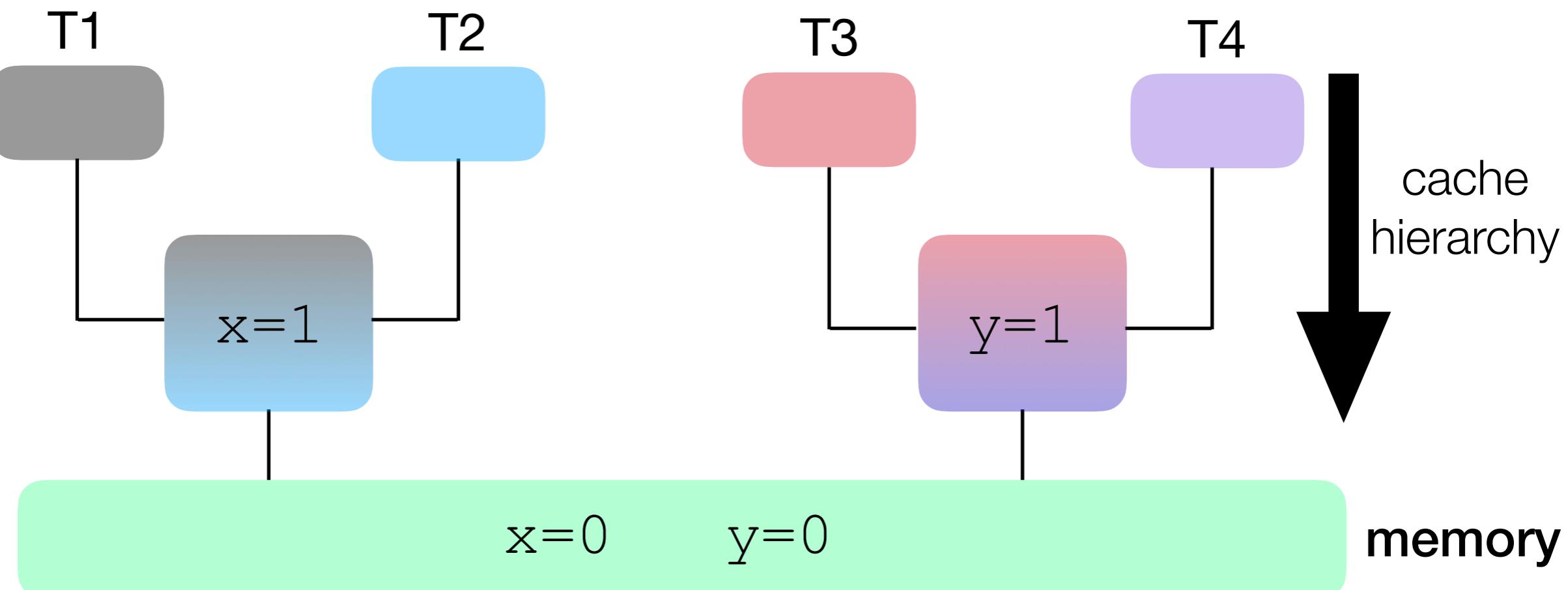
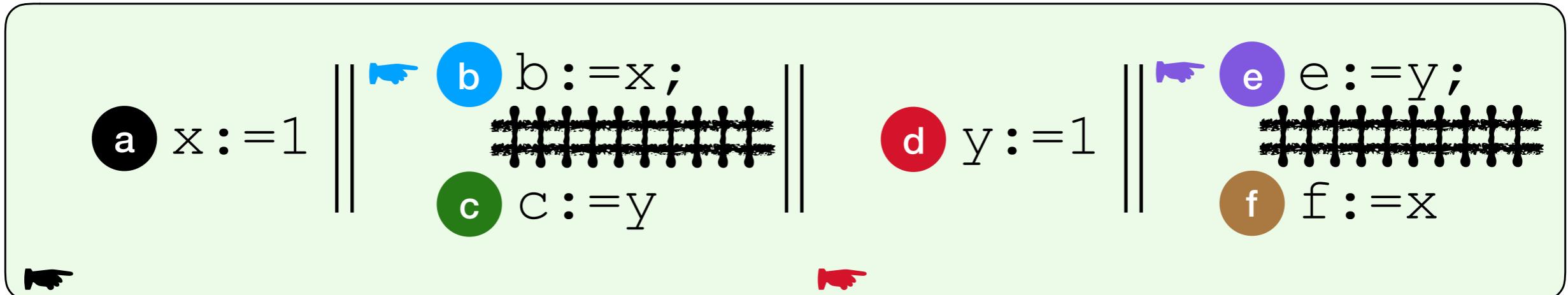
# WMC: Independent Reads of Independent Writes



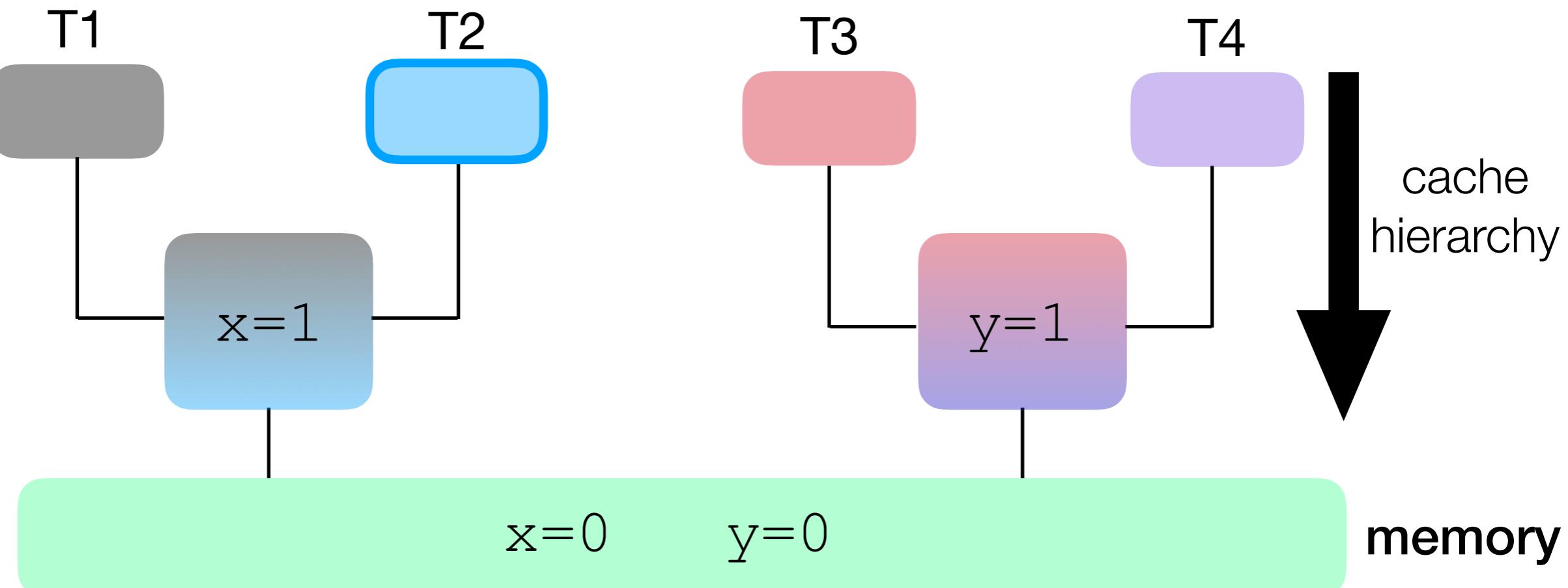
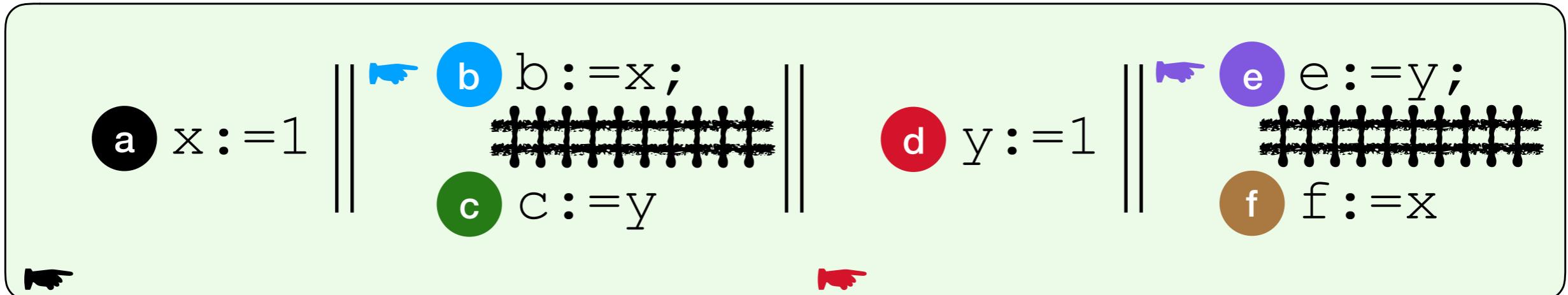
# WMC: Independent Reads of Independent Writes



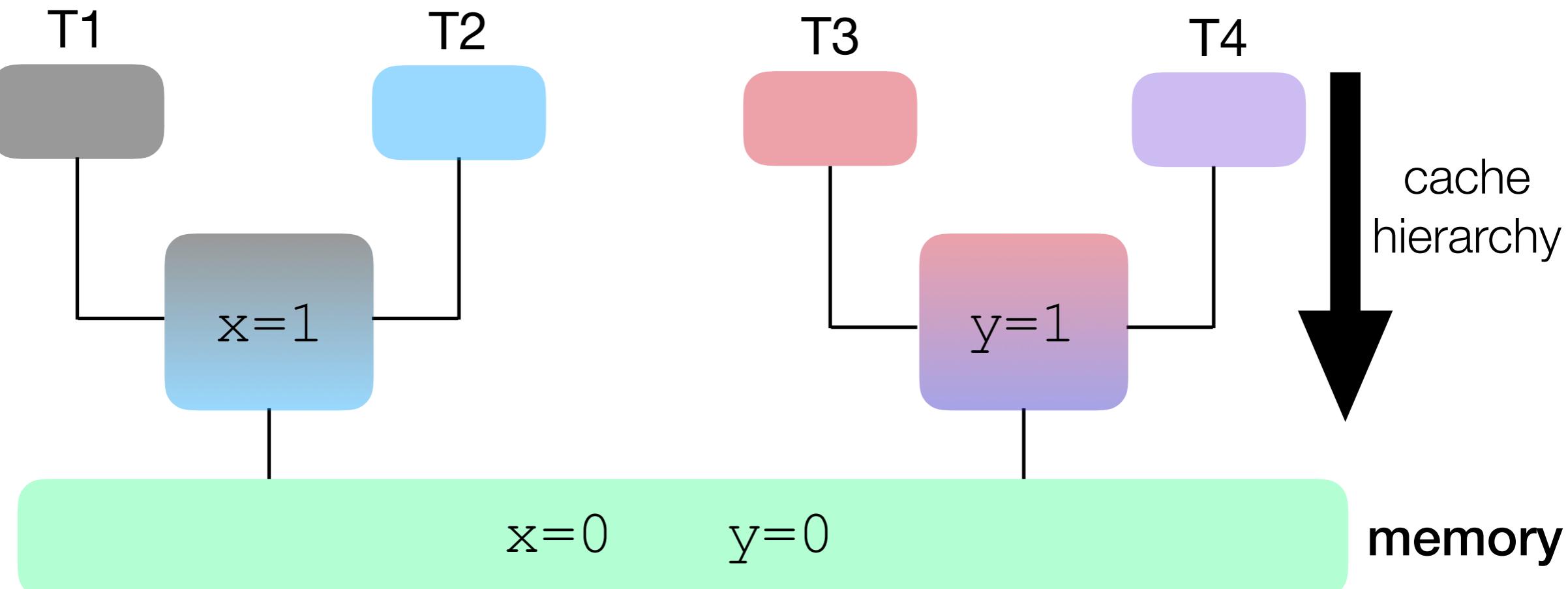
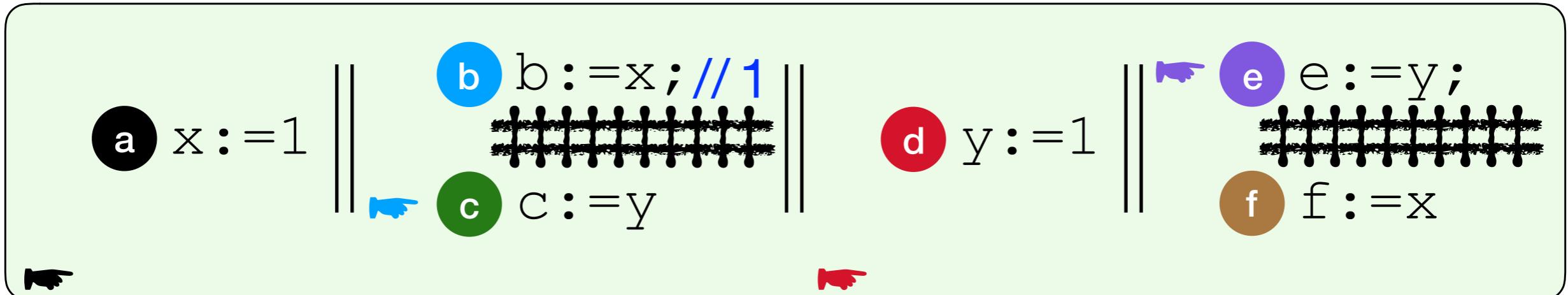
# WMC: Independent Reads of Independent Writes



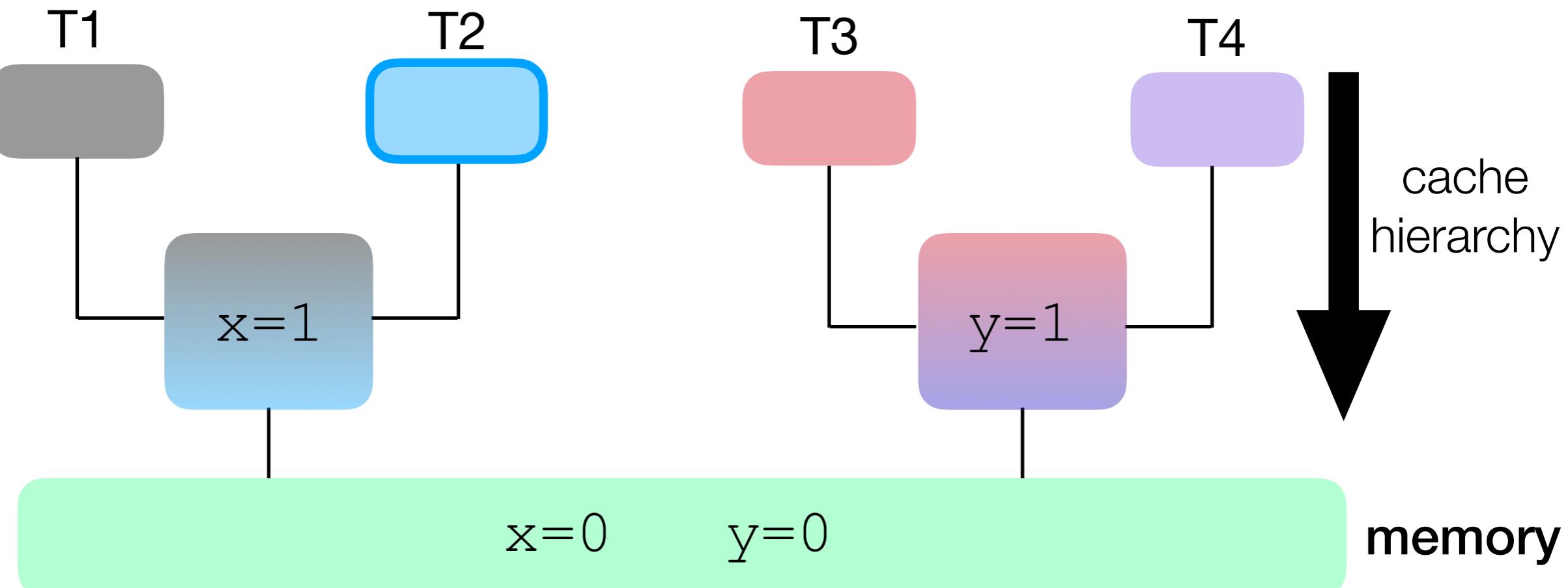
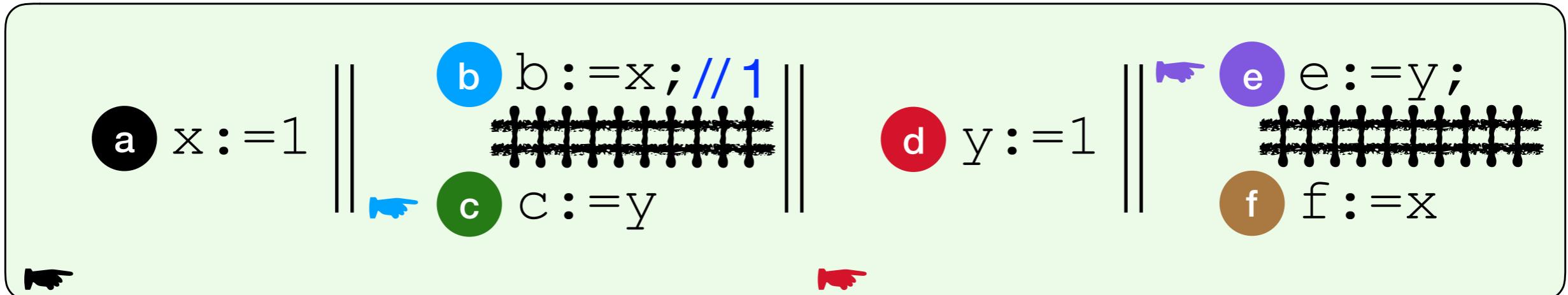
# WMC: Independent Reads of Independent Writes



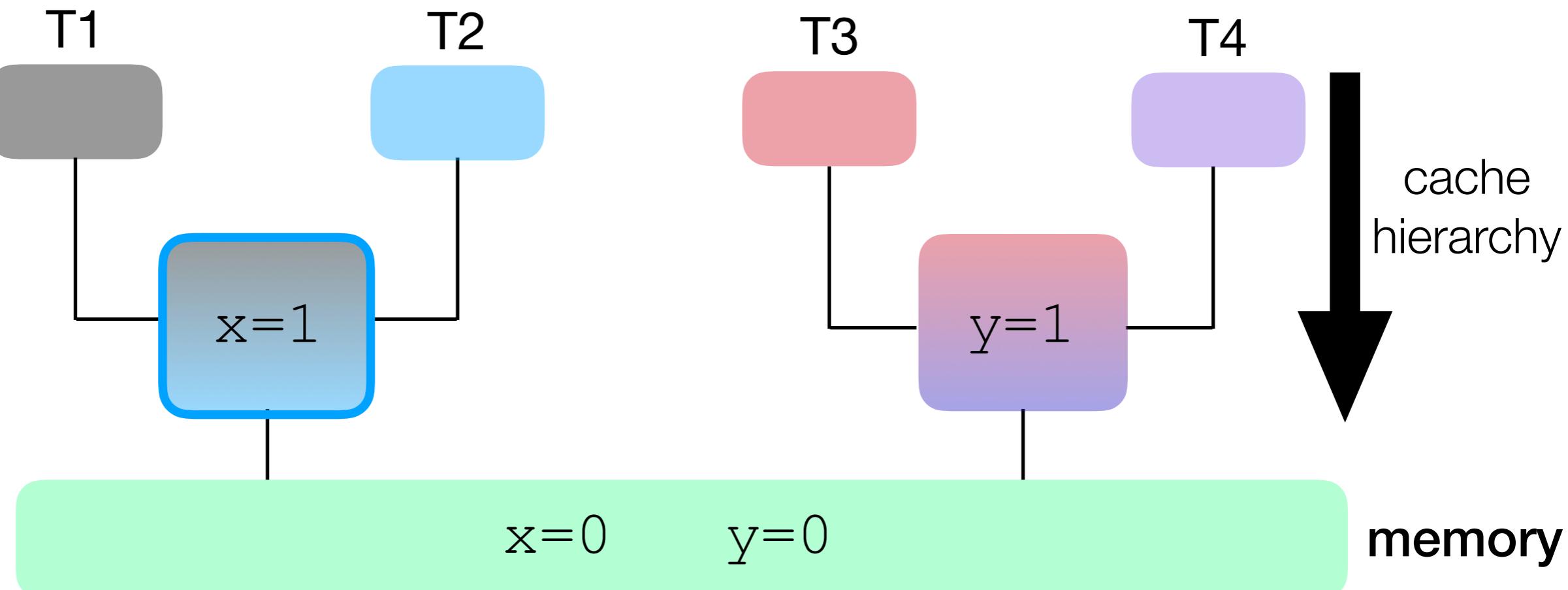
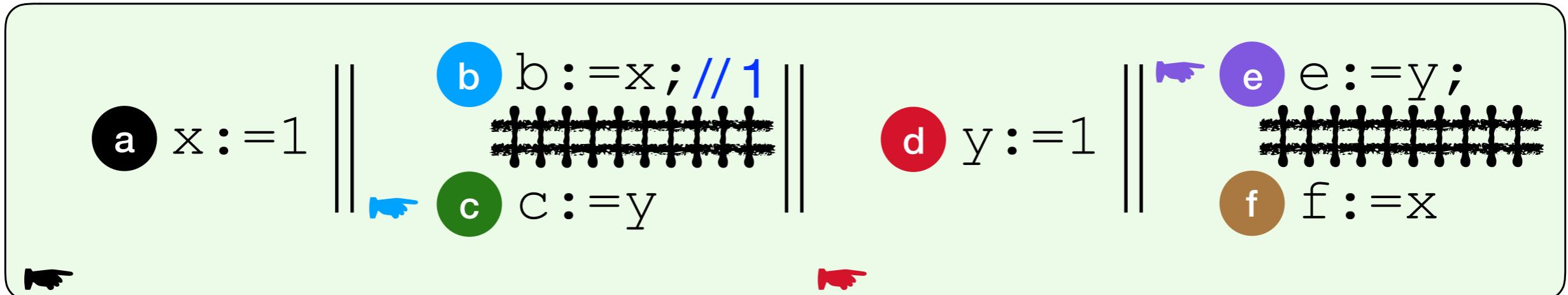
# WMC: Independent Reads of Independent Writes



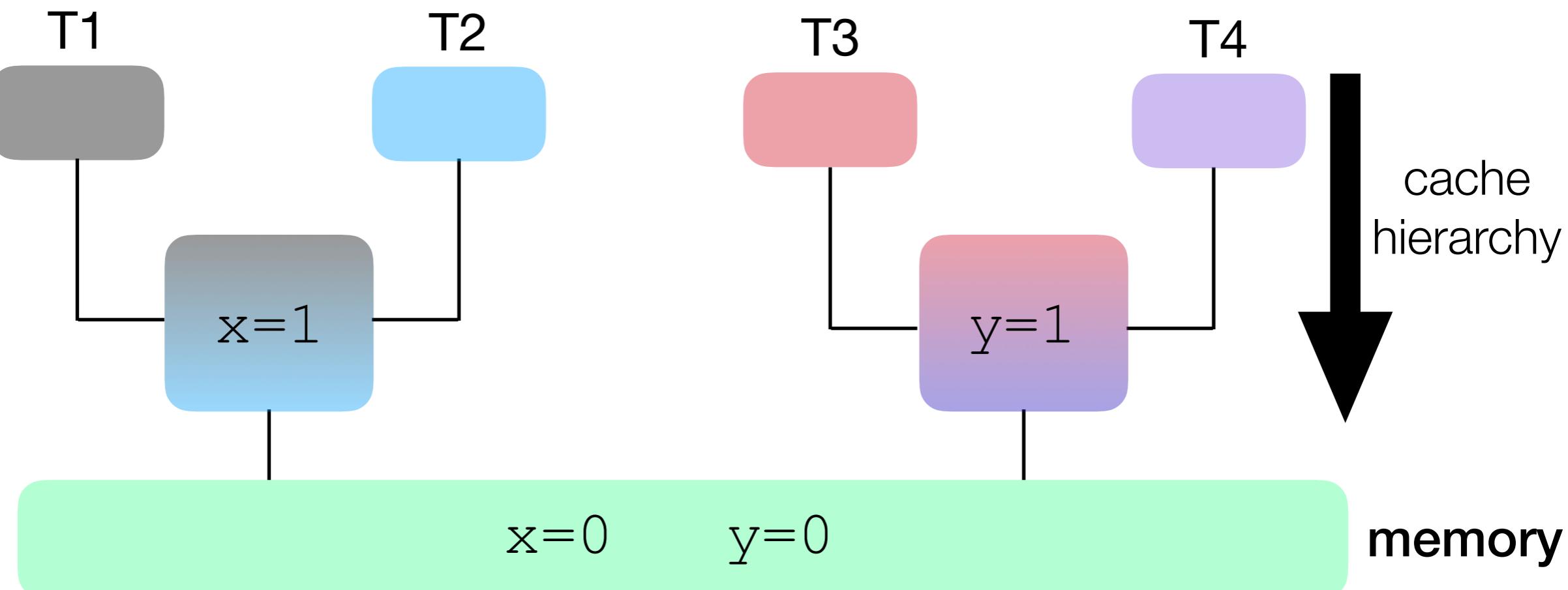
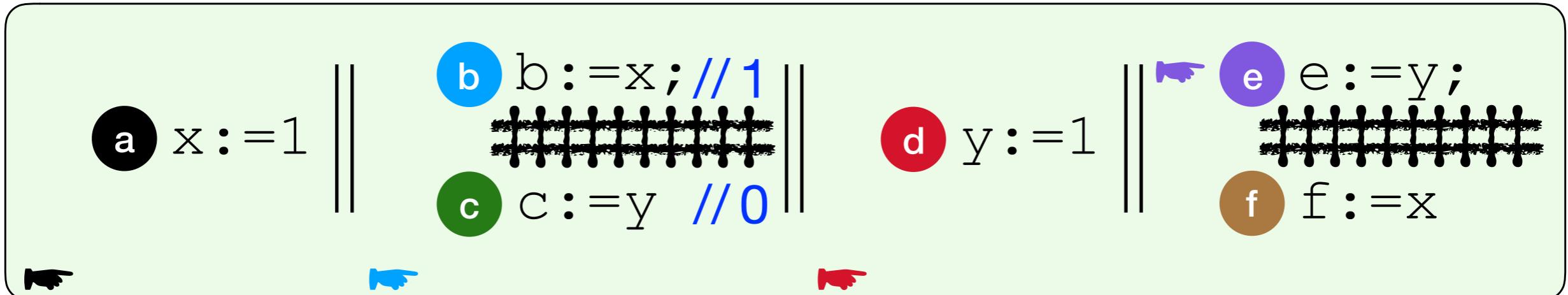
# WMC: Independent Reads of Independent Writes



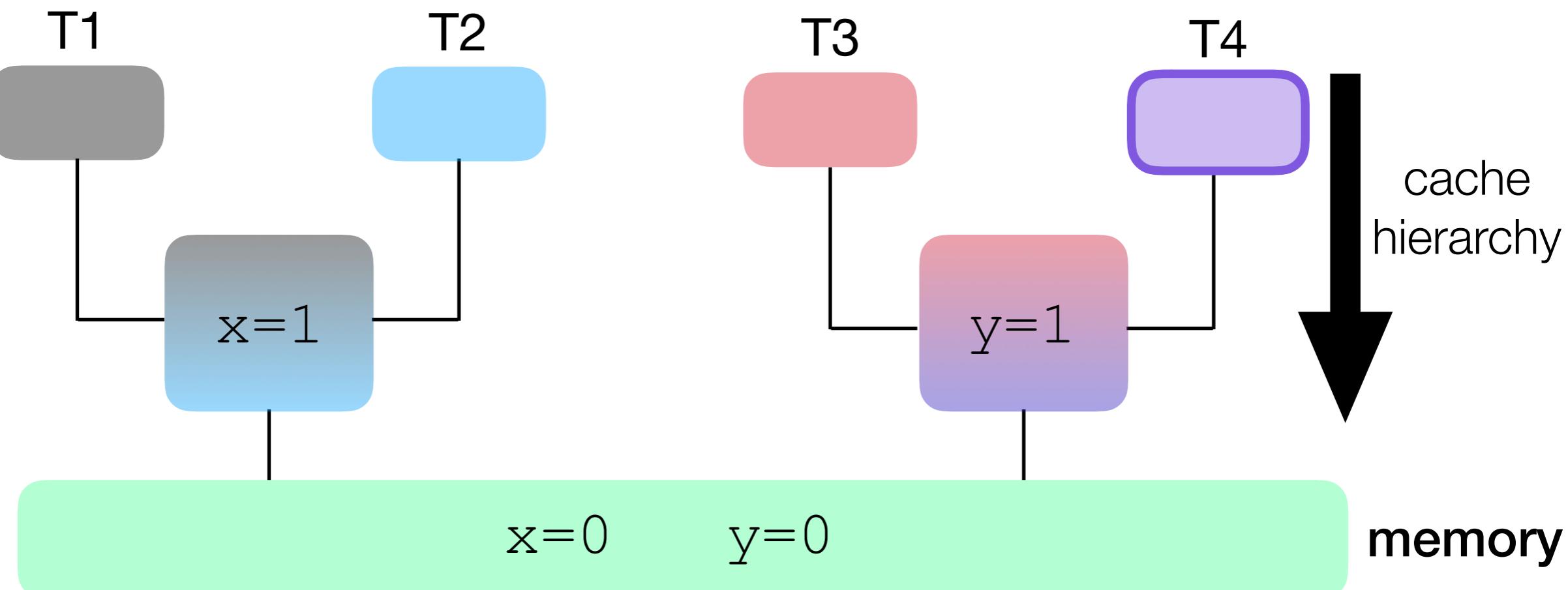
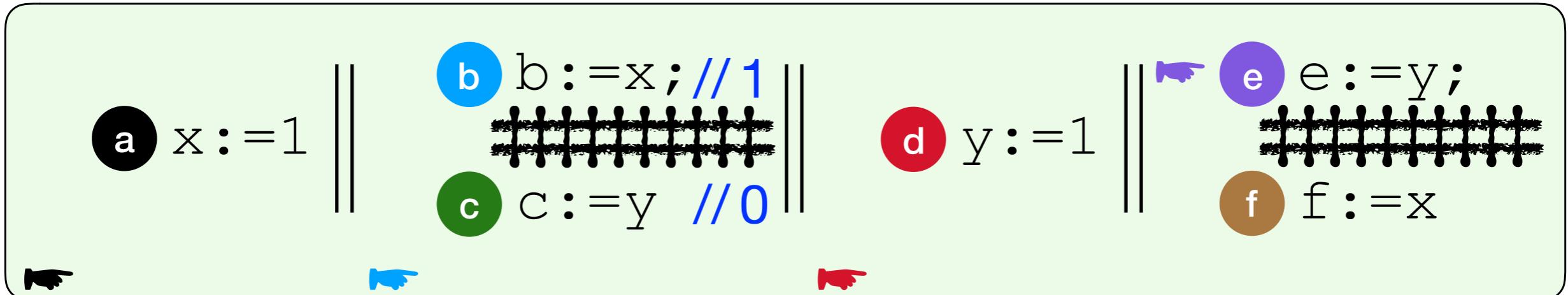
# WMC: Independent Reads of Independent Writes



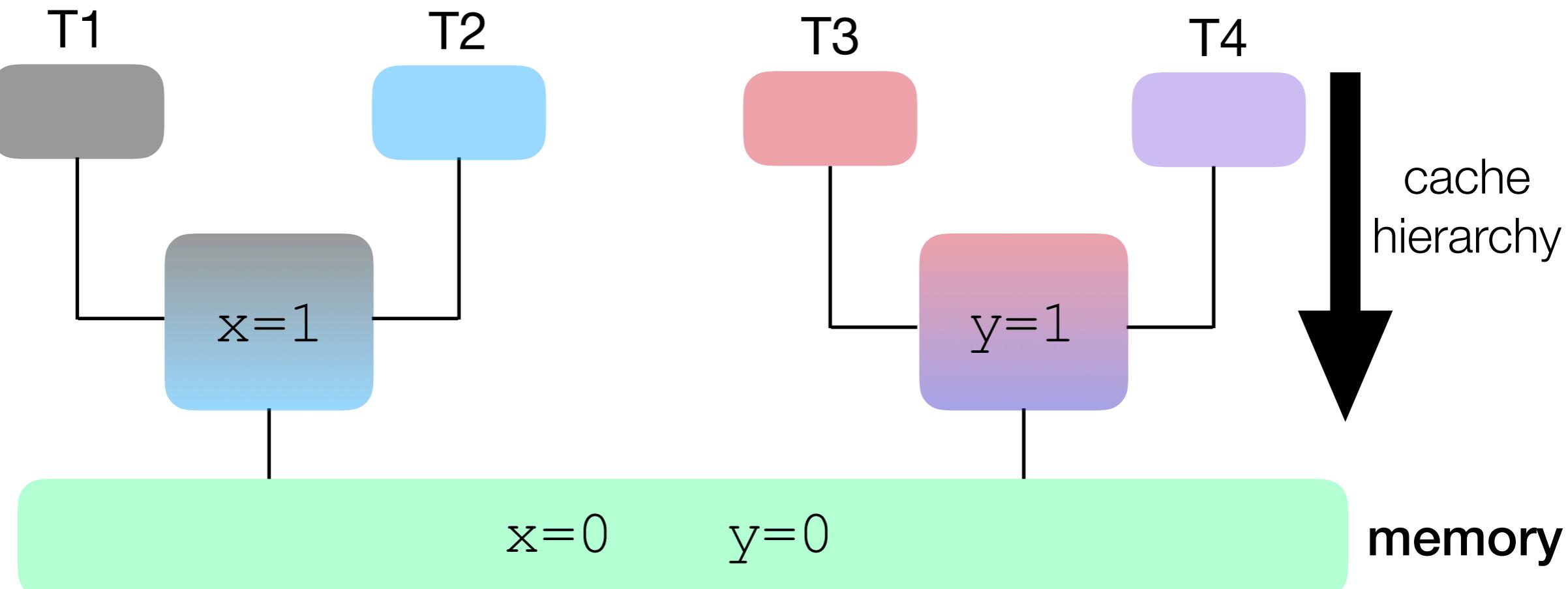
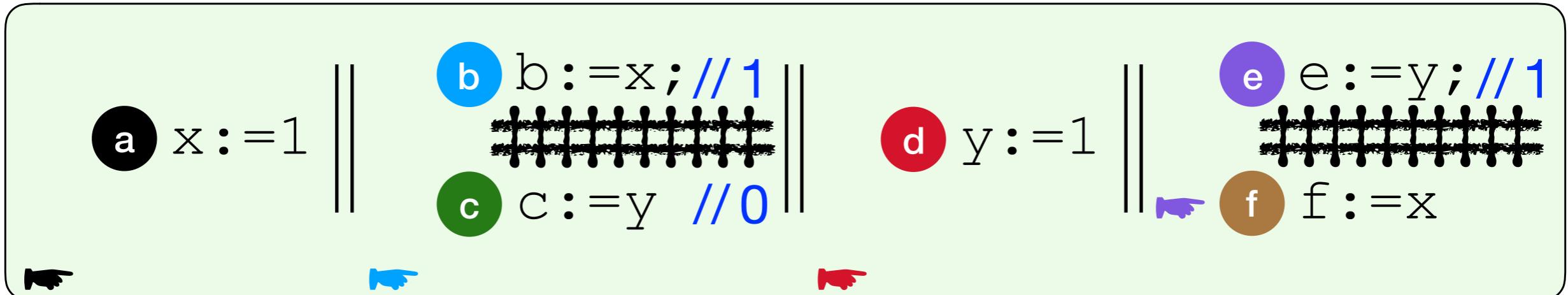
# WMC: Independent Reads of Independent Writes



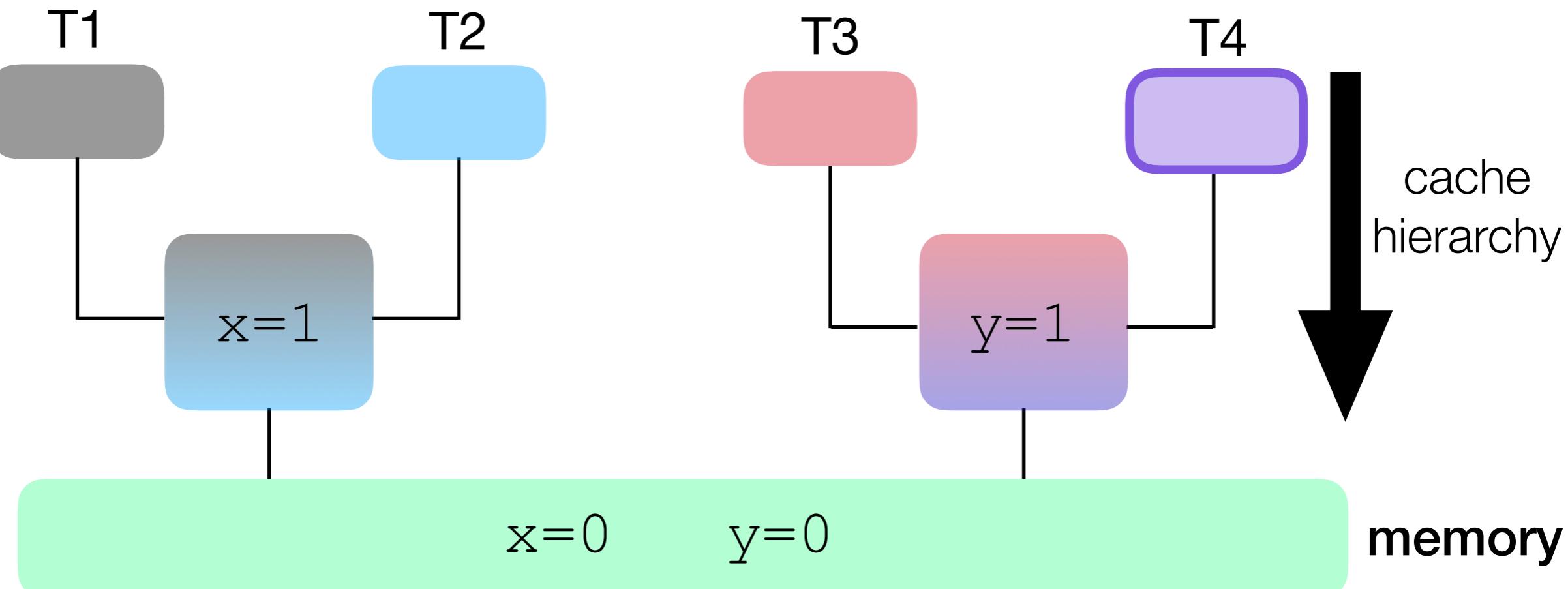
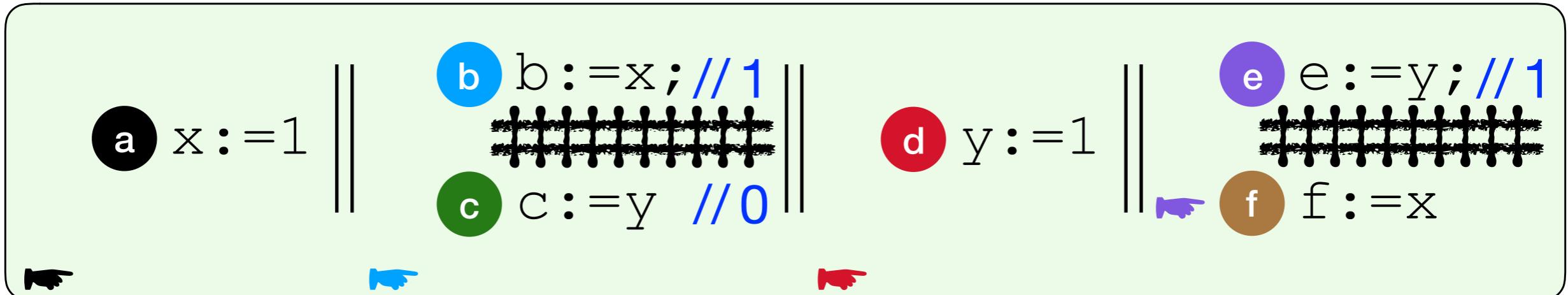
# WMC: Independent Reads of Independent Writes



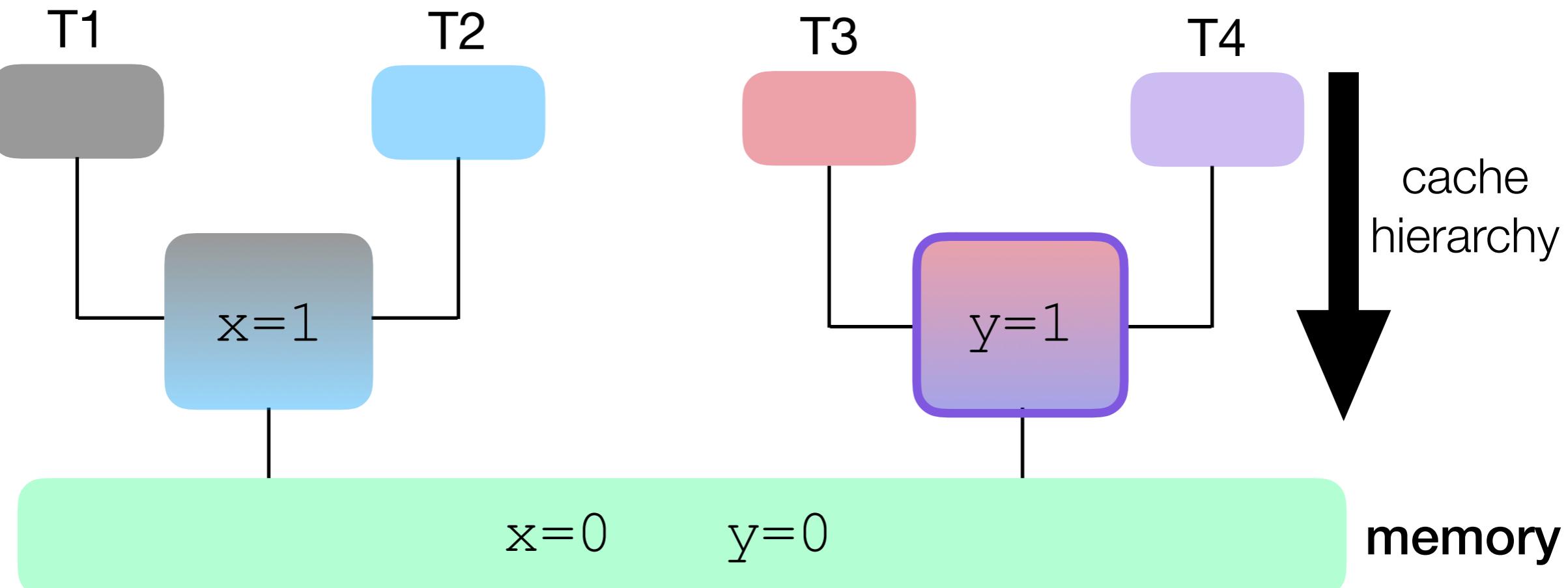
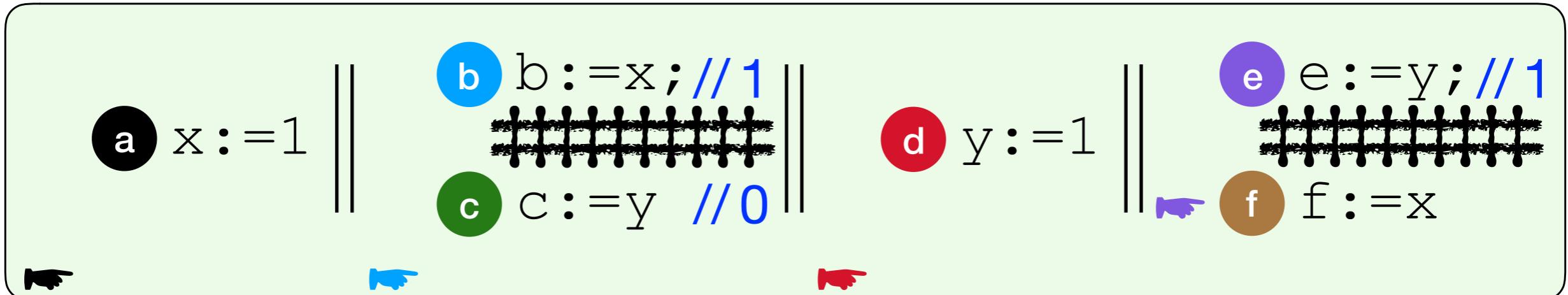
# WMC: Independent Reads of Independent Writes



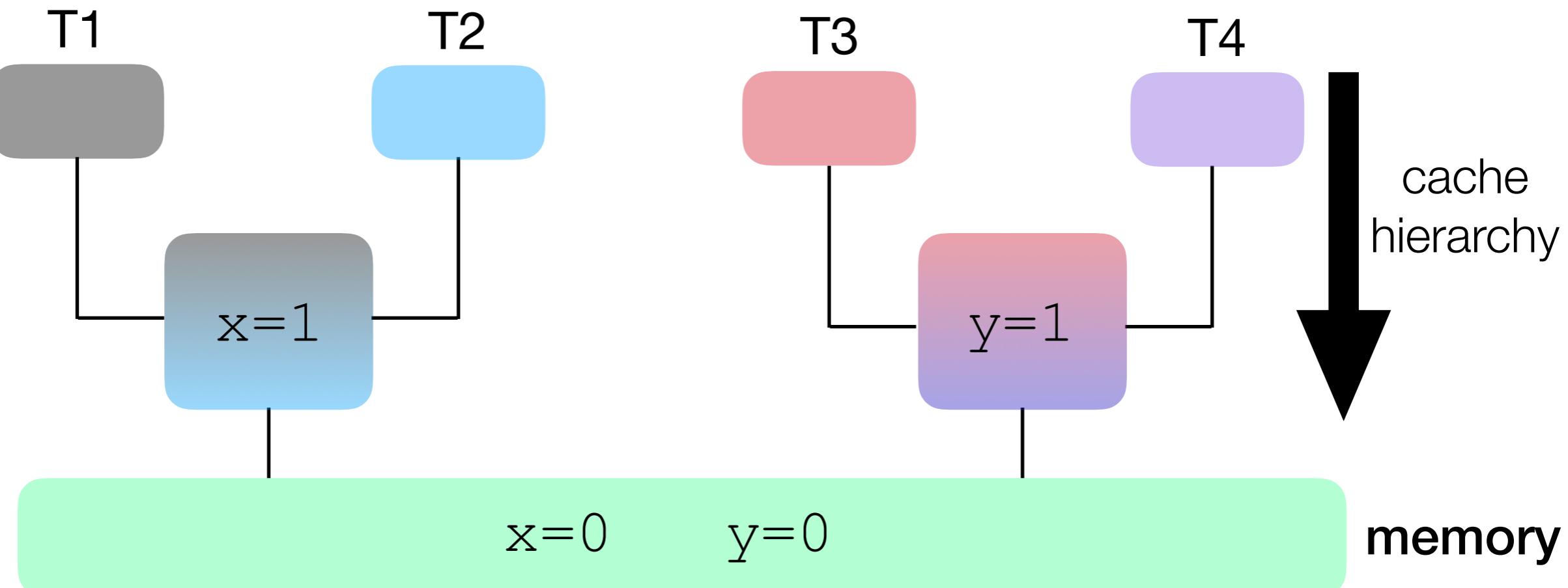
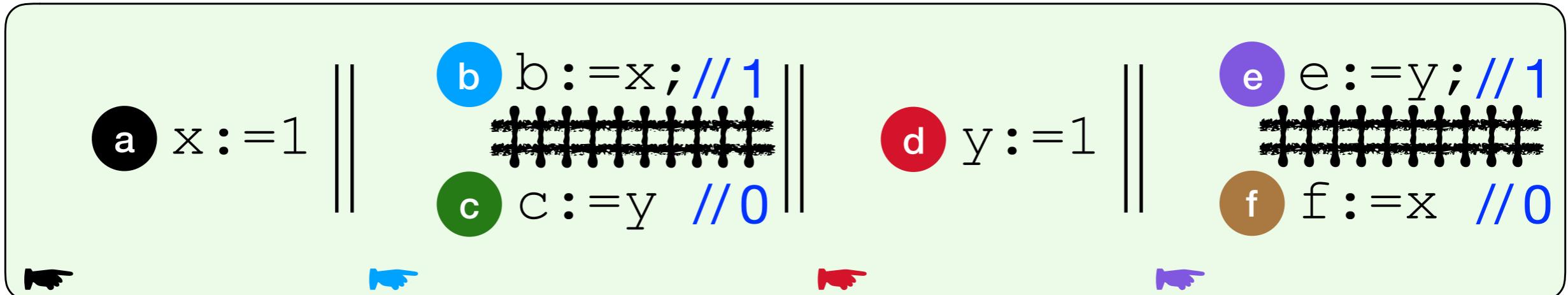
# WMC: Independent Reads of Independent Writes



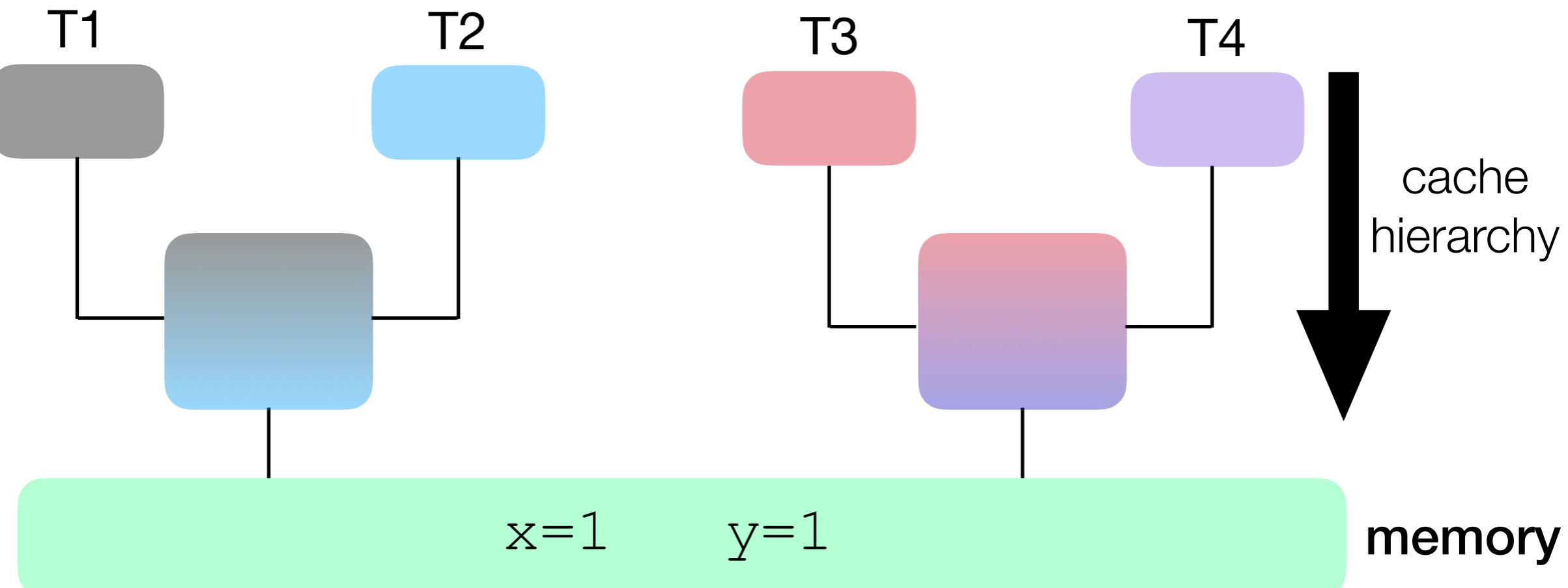
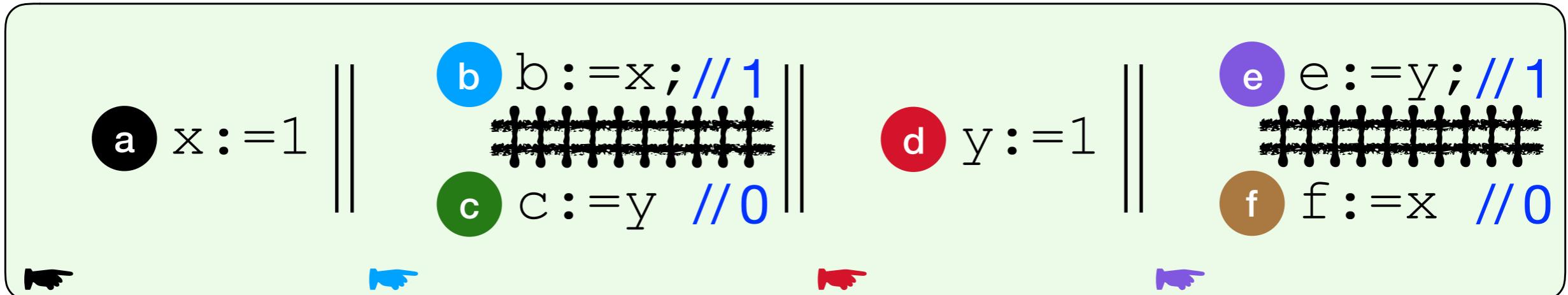
# WMC: Independent Reads of Independent Writes



# WMC: Independent Reads of Independent Writes



# WMC: Independent Reads of Independent Writes



# Weak Memory Concurrency (WMC)

**No** total execution order (**to**)  $\Rightarrow$

**anomalies (litmus tests)**: behaviour absent under SC;  
caused by:

- instruction **reordering**;
  - e.g. store buffering (SB)
- **different write propagation** across cache hierarchy;
  - e.g. Independent Reads of Independent Writes (IRIW)

programming under WMC can be unintuitive and **error-prone**

# Weak Memory Concurrency (WMC)

No total execution order (*to*)  $\Rightarrow$

*anomalies (litmus tests)*: behaviour absent under SC;  
caused by:

- instruction interleaving
  - e.g. store after load
- *different visibility* of shared memory
  - e.g. Index-based race

programmer

**Formal Methods**  
***to the rescue!***

architectural  
anomalies (IRIW)

-prone

# WMC: State of the Art

- Formal Specification
  - ▶ **Hardware** (architecture) level WMC specification
    - e.g. x86-TSO, ARMv7, ARMv8, POWER, ...
  - ▶ **Software** (language) level WMC specification
    - e.g. C/C++11, Java, ...

# WMC: State of the Art

- Formal Specification
  - ▶ **Hardware** (architecture) level WMC specification
    - e.g. x86-TSO, ARMv7, ARMv8, POWER, ...
  - ▶ **Software** (language) level WMC specification
    - e.g. C/C++11, Java, ...
- Formal Verification
  - ▶ Correctness of ***language to architecture compilation***, e.g. C11 to x86
  - ▶ Correctness of ***compiler transformations***
  - ▶ **Verified compilers**, e.g. CompCertTSO
  - ▶ Case studies
    - Linux RCU, ARC, ...

# WMC: State of the Art

- Formal Specification
  - ▶ **Hardware** (architecture) level WMC specification
    - e.g. x86-TSO, ARMv7, ARMv8, POWER, ...
  - ▶ **Software** (language) level WMC specification
    - e.g. C/C++11, Java, ...
- Formal Verification
  - ▶ Correctness of ***language to architecture compilation***, e.g. C11 to x86
  - ▶ Correctness of ***compiler transformations***
  - ▶ **Verified compilers**, e.g. CompCertTSO
  - ▶ Case studies
    - Linux RCU, ARC, ...

***low-level !***

# WMC: State of the Art

- Formal Specification

- ▶ Hard

- e.g.

- ▶ Softw

- e.g.

- Formal

- ▶ Corre

- ▶ Corre

- ▶ Verifi

- ▶ Case

- Linux RCU, ARC, ...

What about

**high-level**

**concurrent library**

specification & verification

under

WMC?

i. C11 to x86

*low-level !*

## Part I.

# Concurrent Library ***Specification*** under WMC

# Concurrent Library Specification

- Sequential Consistency (SC) -- well-explored
  - ▶ semantic-based: linearisability
  - ▶ program logics: Hoare logic, separation logic, etc.
  - ▶ **large** body of case studies

# Concurrent Library Specification

- Sequential Consistency (SC) -- well-explored
  - ▶ semantic-based: linearisability
  - ▶ program logics: Hoare logic, separation logic, etc.
  - ▶ **large** body of case studies
- Weak Memory Concurrency (WMC) -- under-explored
  - ▶ linearisability variants
  - ▶ program logic adaptations
  - ▶ **small** body of case studies

# Concurrent Library Specification

- Sequential Consistency (SC) -- well-explored
    - ▶ semantic-based: linearisability
    - ▶ program logics: Hoare logic, separation logic, etc.
    - ▶ **large** body of case studies
  - Weak Memory Concurrency (WMC) -- under-explored
    - ▶ linearisability variants
    - ▶ program logic adaptations
    - ▶ **small** body of case studies
- 
- tied to a  
**particular WMC**  
**memory model (MM) !**  
E.g. C11, TSO, ...

# Concurrent Library Specification

- Sequential Consistency (SC) -- well-explored
  - ▶ semantic-based: linearisability
  - ▶ program logics: Hoare logic, separation logic, etc.
  - ▶ *large* bodies of work
- Weak Memory Models -- less explored
  - ▶ linearisability
  - ▶ program logics
  - ▶ *small* bodies of work

**wanted**

***general***

***MM-agnostic***

***declarative***

***specification & verification***

***framework***

colored

ed to a

**circular** WMC

***model (MM)* !**

C11, TSO, ...

# Declarative Framework Desiderata

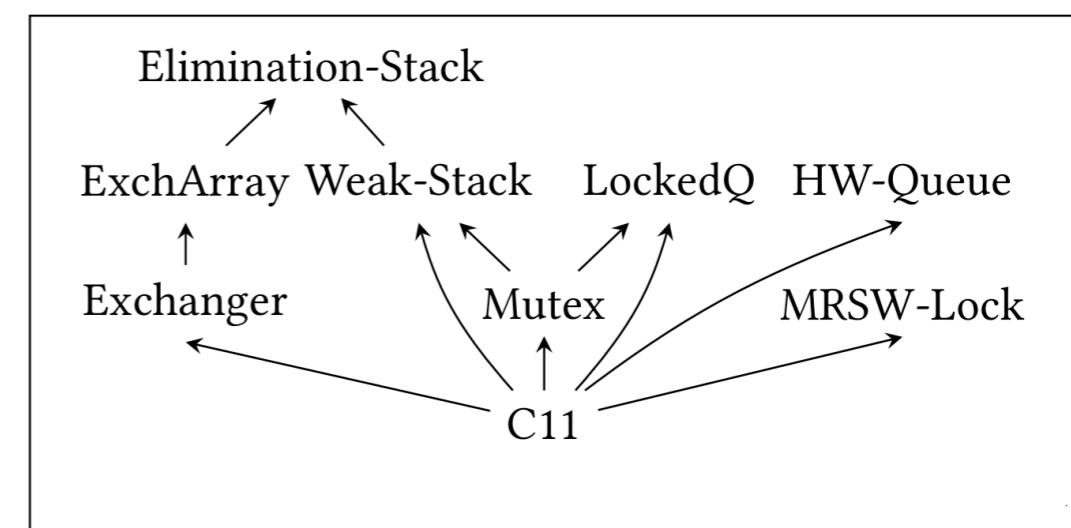
- **Agnostic** to memory model
  - ▶ support both SC and WMC specs
- **General**
  - ▶ port existing SC (linearisability) specs
  - ▶ port existing WMC specs (e.g. C11, TSO)
  - ▶ built from the ground up: assume no pre-existing libraries or specs
- **Compositional**
  - ▶ verify client programs

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1);  || a:=pop(s);  
push(s,2)   || if(a==2)  
                           b:=deq(q) // returns 1
```

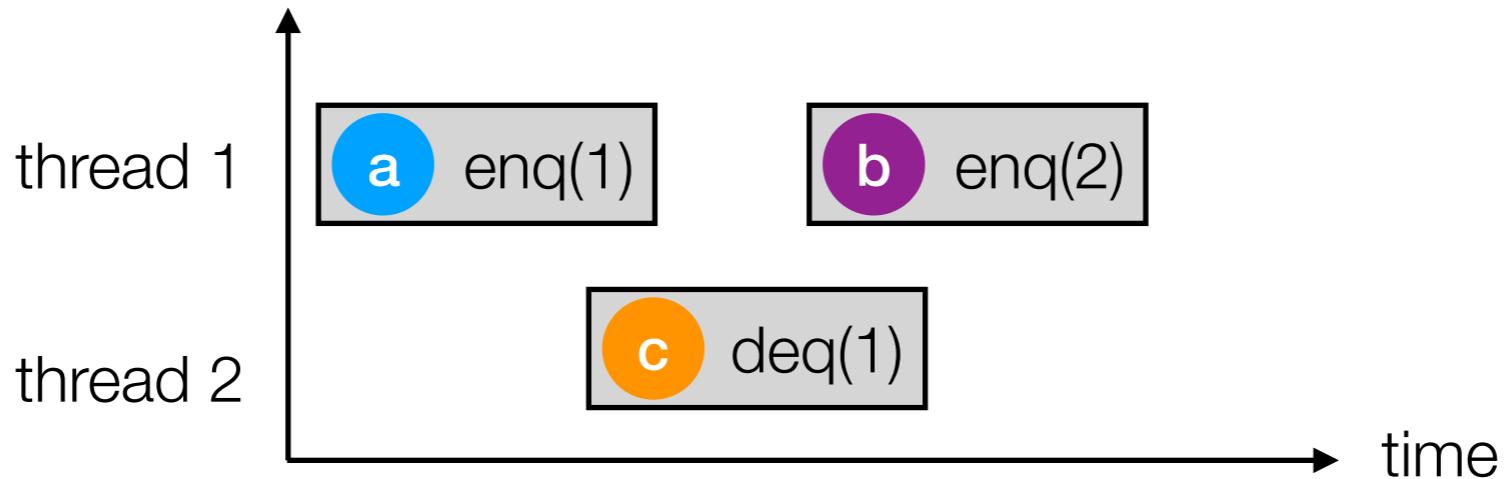
# Declarative Framework Desiderata

- **Agnostic** to memory model
  - ▶ support both SC and WMC specs
- **General**
  - ▶ port existing SC (linearisability) specs
  - ▶ port existing WMC specs (e.g. C11, TSO)
  - ▶ built from the ground up: assume no pre-existing libraries or specs
- **Compositional**
  - ▶ verify client programs
  - ▶ verify library implementations ⇒ towers of abstraction

```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1); || a:=pop(s);  
push(s, 2)    || if(a==2)  
                  b:=deq(q) // returns 1
```

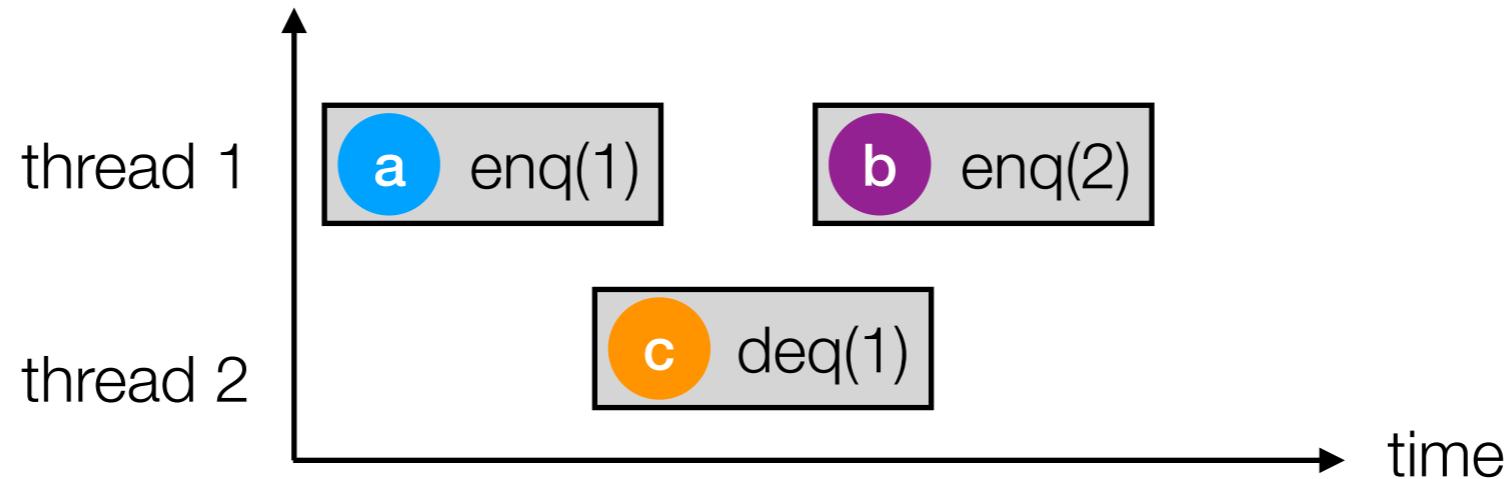


# Linearisability



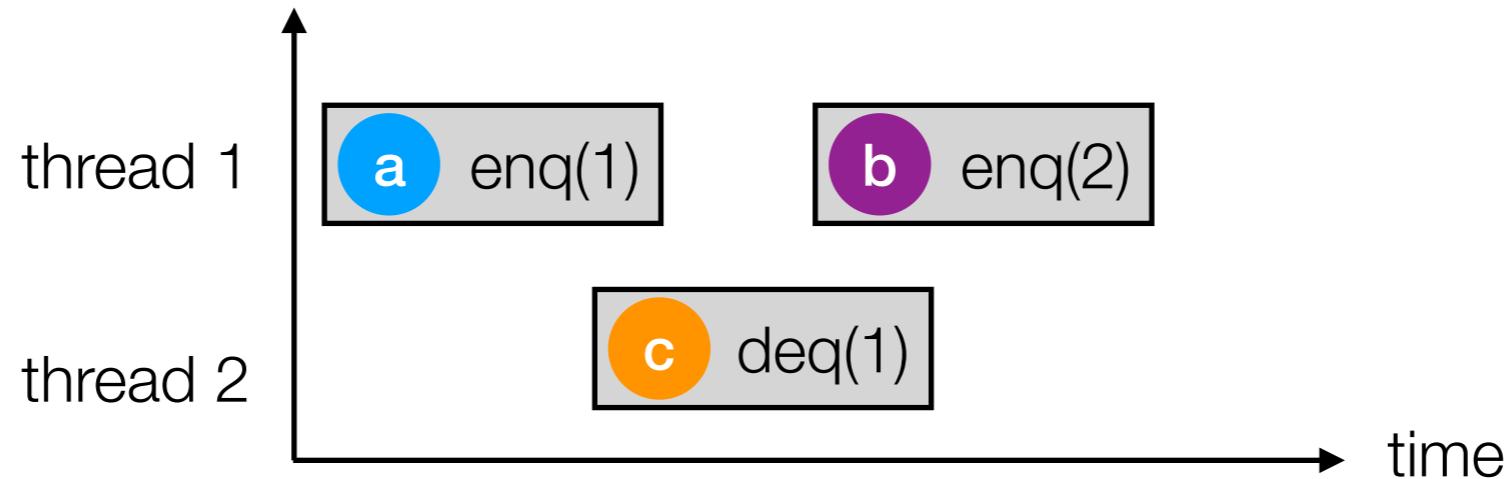
- Define a (partial) happens-before relation  $hb$  on events
  - ▶  $(e_1, e_2) \in hb \iff e_1.\text{end} <_{\text{time}} e_2.\text{begin}$ 
    - e.g.  $(a, b) \in hb$        $(a, c) \notin hb$

# Linearisability



- Define a (partial) happens-before relation  $hb$  on events
  - ▶  $(e_1, e_2) \in hb \iff e_1.\text{end} <_{\text{time}} e_2.\text{begin}$ 
    - e.g.  $(a, b) \in hb$        $(a, c) \notin hb$
- **Linearisable**  $\iff \exists \text{ to. to totally orders events}$ 
  - ▶  $hb \subseteq \text{to}$
  - ▶  $\text{to}$  is a **legal** sequence (library-specific)
    - e.g.  $\text{to}$  is a **FIFO** sequence

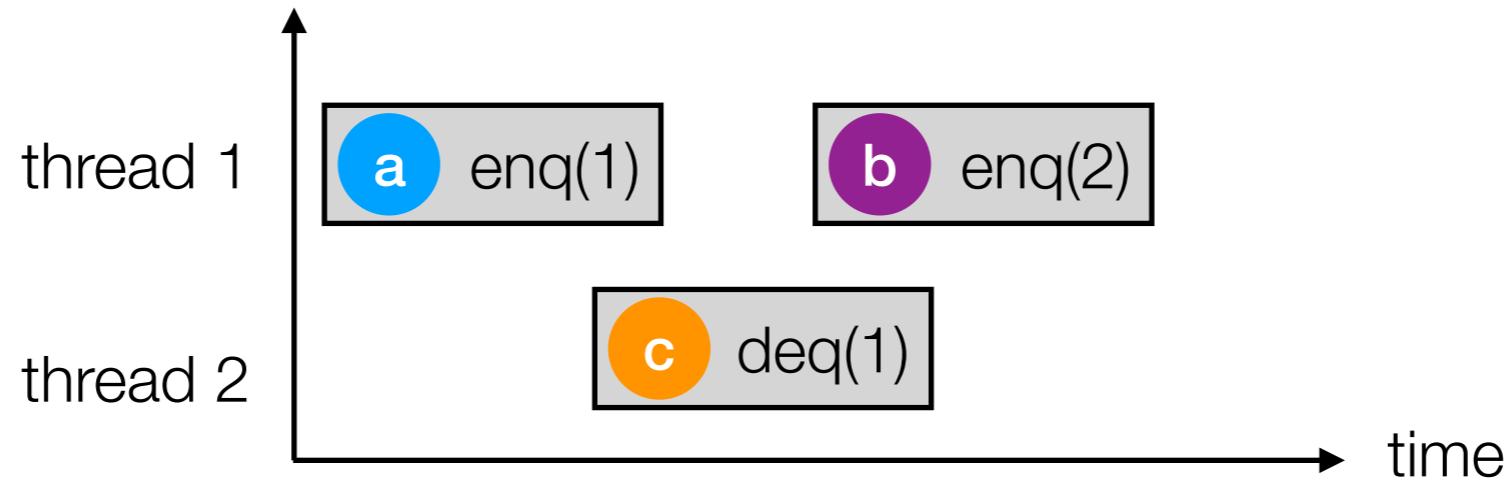
# Linearisability



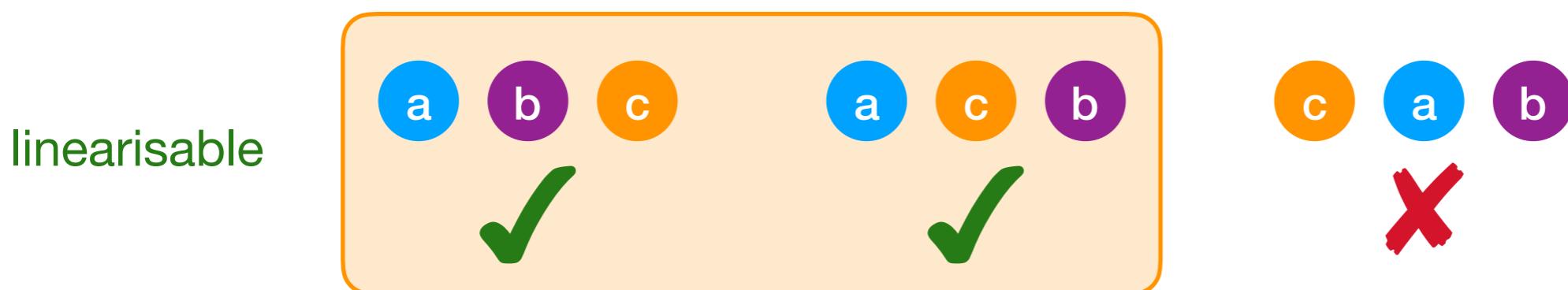
- Define a (partial) happens-before relation  $hb$  on events
  - ▶  $(e_1, e_2) \in hb \iff e_1.\text{end} <_{\text{time}} e_2.\text{begin}$ 
    - e.g.  $(\text{a}, \text{b}) \in hb$        $(\text{a}, \text{c}) \notin hb$
- **Linearisable**  $\iff \exists \text{ to. to totally orders events}$ 
  - ▶  $hb \subseteq \text{to}$
  - ▶  $\text{to}$  is a **legal** sequence (library-specific)
    - e.g.  $\text{to}$  is a **FIFO** sequence



# Linearisability

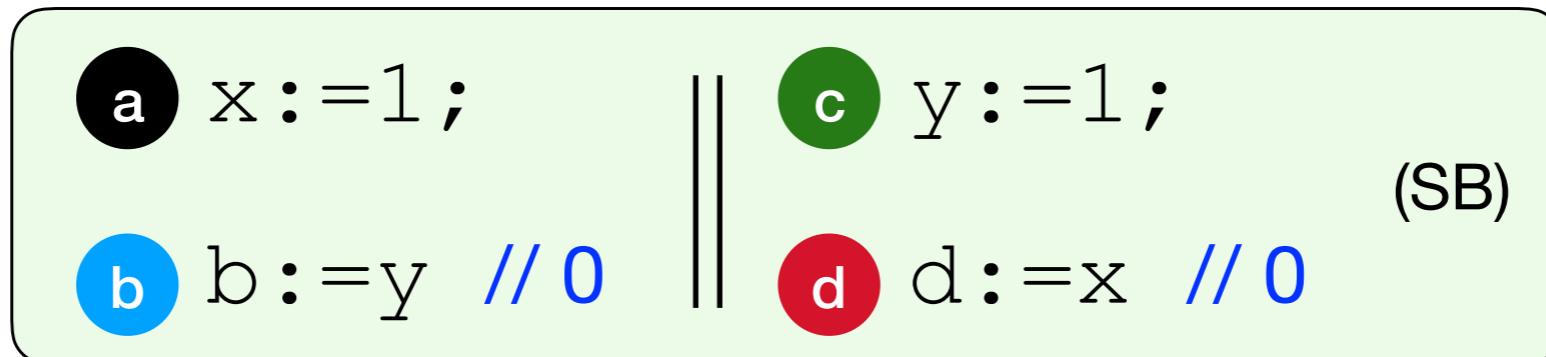


- Define a (partial) happens-before relation  $hb$  on events
  - ▶  $(e_1, e_2) \in hb \iff e_1.\text{end} <_{\text{time}} e_2.\text{begin}$ 
    - e.g.  $(\text{a}, \text{b}) \in hb$        $(\text{a}, \text{c}) \notin hb$
- **Linearisable**  $\iff \exists \text{ to. to totally orders events}$ 
  - ▶  $hb \subseteq \text{to}$
  - ▶  $\text{to}$  is a **legal** sequence (library-specific)
    - e.g.  $\text{to}$  is a **FIFO** sequence



# Why Not Linearisability?

- ✗ assumes **<time** order -- not present under WMC
- ✗ requires **total** order on **all** events -- not always possible under WMC

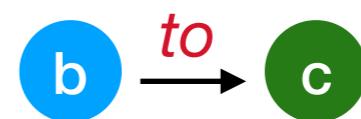
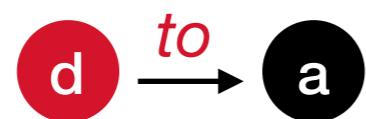
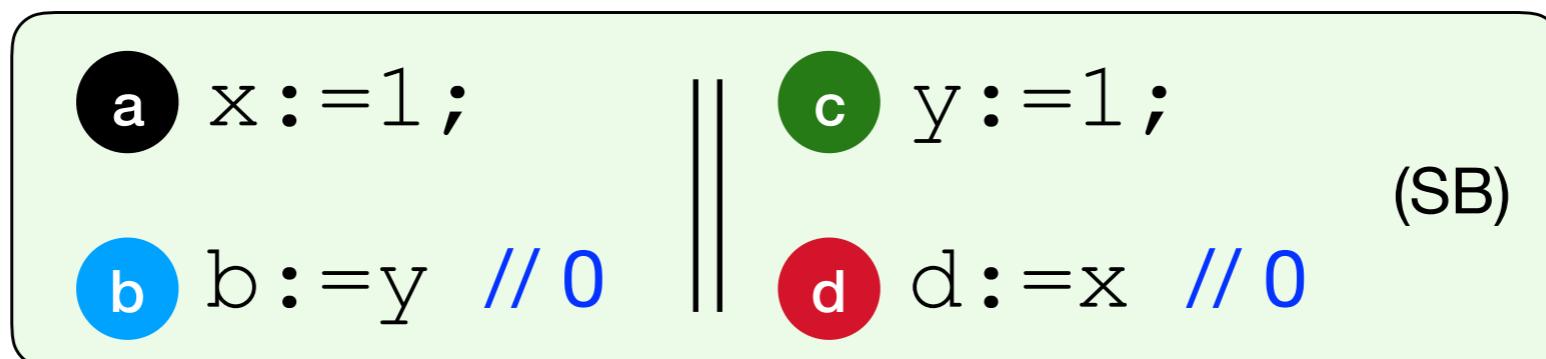


✗

not linearisable

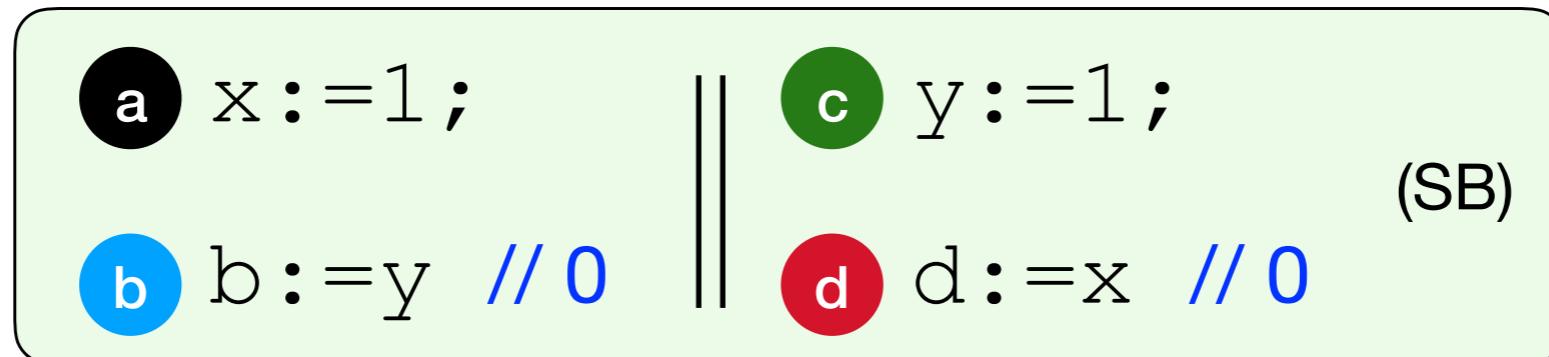
# Why Not Linearisability?

- ✗ assumes  $<\text{time}$  order -- not present under WMC
- ✗ requires **total** order on **all** events -- not always possible under WMC
- ? **per-location** linearisability?



# Why Not Linearisability?

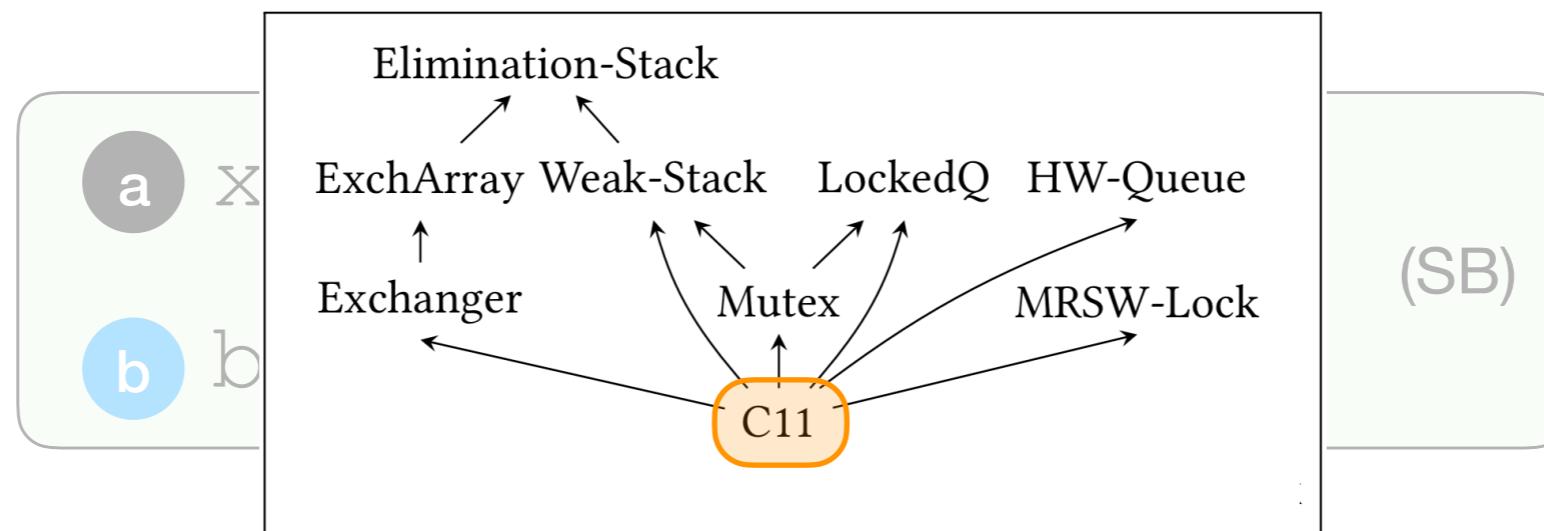
- ✗ assumes  $<_{\text{time}}$  order -- not present under WMC
- ✗ requires **total** order on **all** events -- not always possible under WMC
- ? **per-location** linearisability?
  - ✗ cannot model **weak specs**, e.g. C11, TSO



✗ not linearisable

# Why Not Linearisability?

- ✗ assumes  $<_{\text{time}}$  order -- not present under WMC
- ✗ requires **total** order on **all** events -- not always possible under WMC
- ? **per-location** linearisability?
  - ✗ cannot model **weak specs**, e.g. C11, TSO



✗ not linearisable

# Our Solution

- ✓ **no** particular *memory model*
- ✓ **no**  $<_{\text{time}}$  order
- ✓ **no total** order on events
- ✓ **per-library** specification
  - set of *library executions*

$$\{G \mid G \text{ satisfies certain } \mathbf{axioms}\}$$

|  
library execution

# Library Executions

Example: **Queue** Library

```
q:=new-queue();
s:=new-stack();

enq(q,1);    || a:=pop(s);
push(s,2)    || if(a==2)
                b:=deq(q) // may read 1 or empty ( $\perp$ )
```

$G_{queue} = \langle E, po, so, hb \rangle$

# Library Executions

Example: **Queue** Library

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1);    || a:=pop(s);  
push(s,2)    || if(a==2)  
                b:=deq(q) // may read 1 or empty ( $\perp$ )
```

$G_{queue} = \langle E, po, so, hb \rangle$

$E$   
events

**n** new-queue(q)

**e** enq(q, 1)

**d** deq(q, 1)

# Library Executions

Example: **Queue** Library

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1);    || a:=pop(s);  
push(s,2)    || if(a==2)  
                b:=deq(q) // may read 1 or empty ( $\perp$ )
```

$G_{queue} = \langle E, po, so, hb \rangle$

$E$   
events

**n** new-queue(q)

**n** new-queue(q)

**e** enq(q, 1)

**d** deq(q, 1)

**e** enq(q, 1)

**d** deq(q,  $\perp$ )

# Library Executions

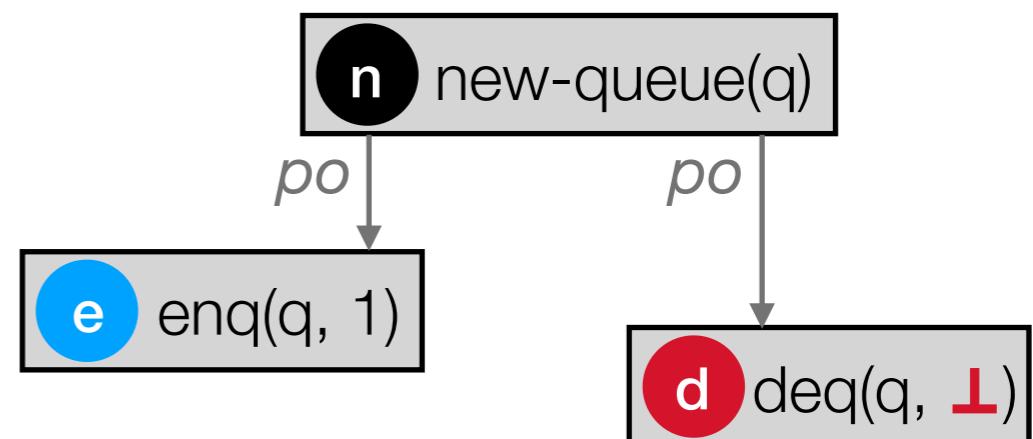
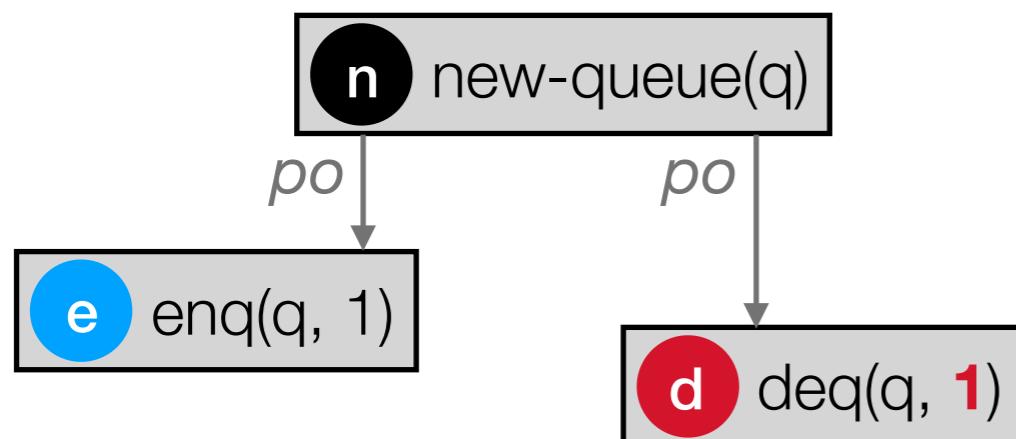
# Example: **Queue** Library

```

q:=new-queue();
s:=new-stack();

enq(q,1);    || a:=pop(s);
push(s,2)        || if(a==2)
                    || b:=deq(q) // may read 1 or empty ( $\perp$ )

```



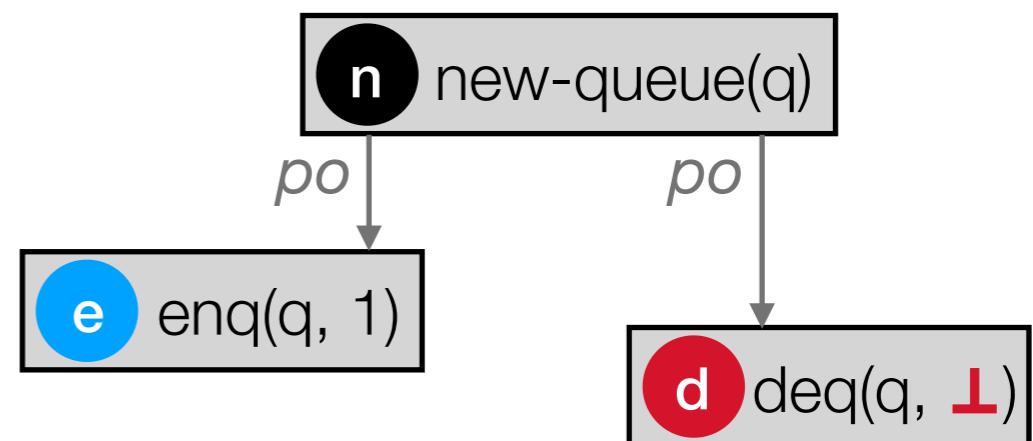
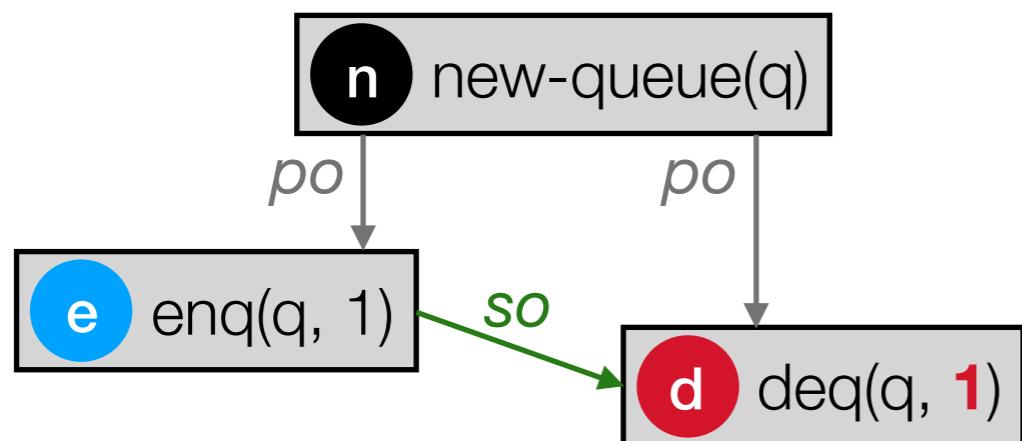
# Library Executions

Example: **Queue** Library

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1); || a:=pop(s);  
push(s,2)   || if(a==2)  
               b:=deq(q) // may read 1 or empty ( $\perp$ )
```

$G_{queue} = \langle E, po, so, hb \rangle$

events                    program-order                    synchronisation-order



# Library Executions

# Example: **Queue** Library

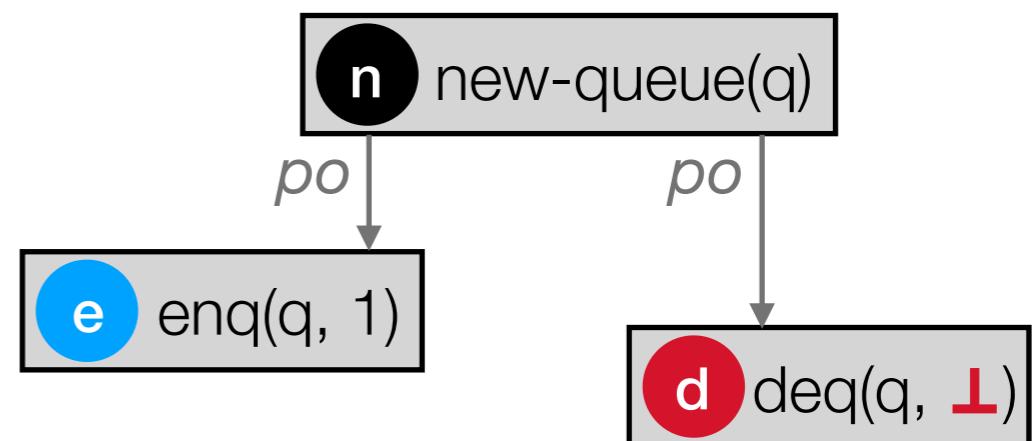
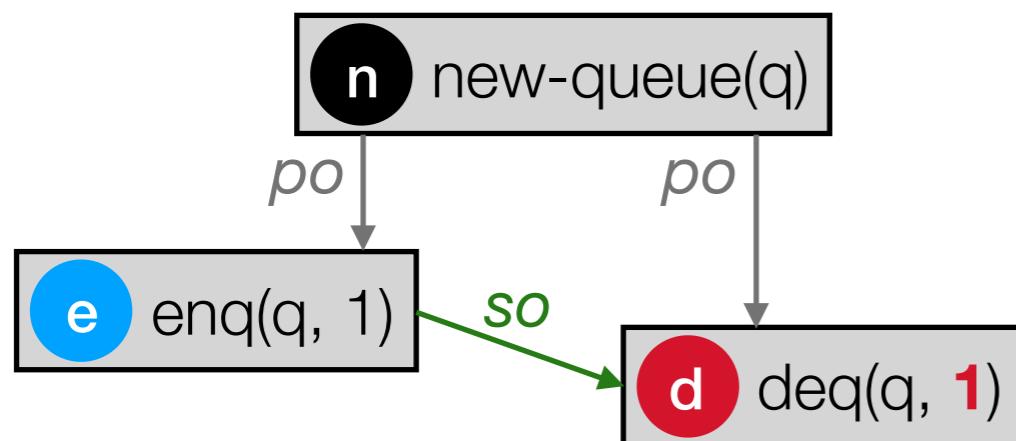
```

q:=new-queue();
s:=new-stack();

enq(q,1);    || a:=pop(s);
push(s,2)        || if(a==2)
                    || b:=deq(q) // may read 1 or empty ( $\perp$ )

```

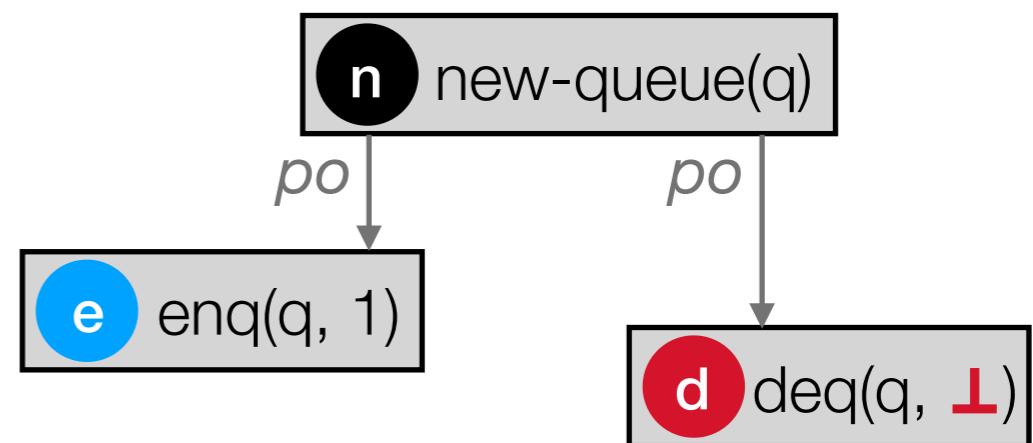
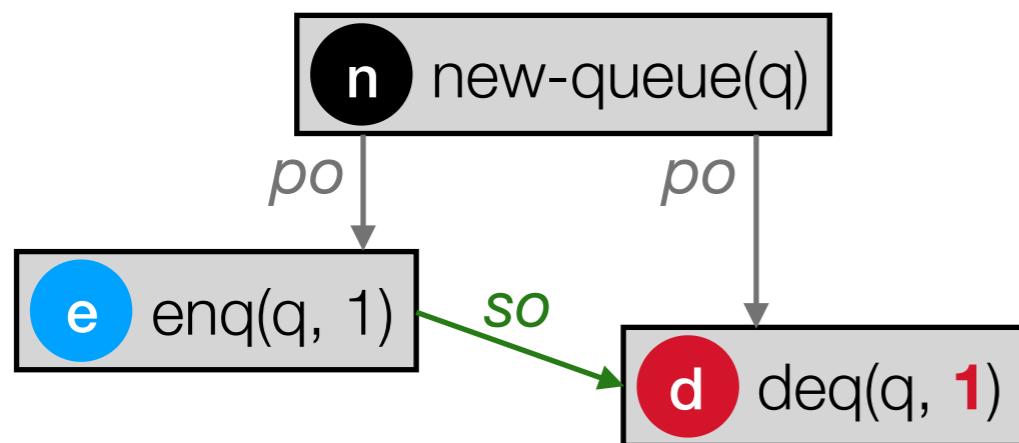
$$G_{queue} = < \underset{\text{events}}{E}, \underset{\text{program-order}}{po}, \underset{\text{synchronisation-order}}{so}, \underset{\text{happens-before order (coming soon!)}}{hb} >$$



# Library Executions

# Example: **Queue** Library

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1); || a:=pop(s);  
push(s,2) || if(a==2)  
            b:=deq(q) // may read 1 or empty(±)
```

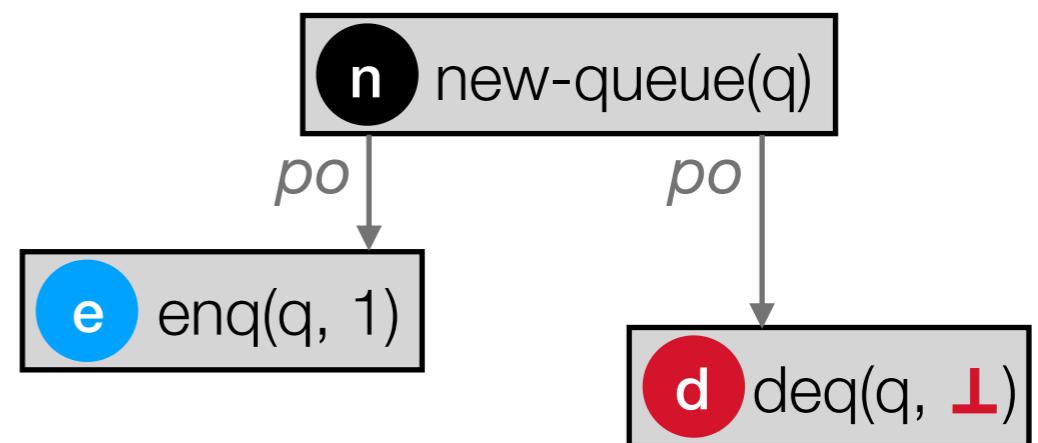
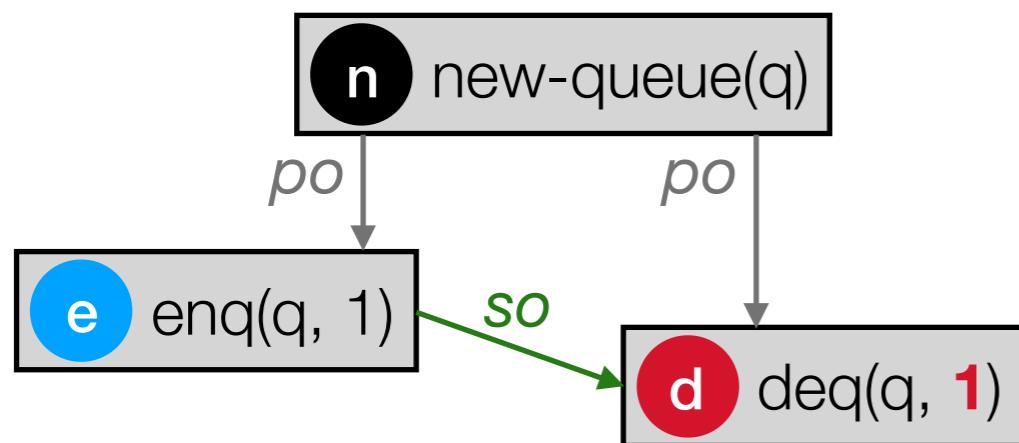


# Library Executions

Example: **Queue** Library

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1); || a:=pop(s);  
push(s,2)  || if(a==2)  
              b:=deq(q) // may read 1 or empty ( $\perp$ )
```

How to eliminate this "*incorrect*" behaviour?



X

# Program Executions

$$\llbracket P \rrbracket = \{ G_P = < \textcircled{E}, \textcircled{po}, \textcircled{so} > \mid \dots \}$$

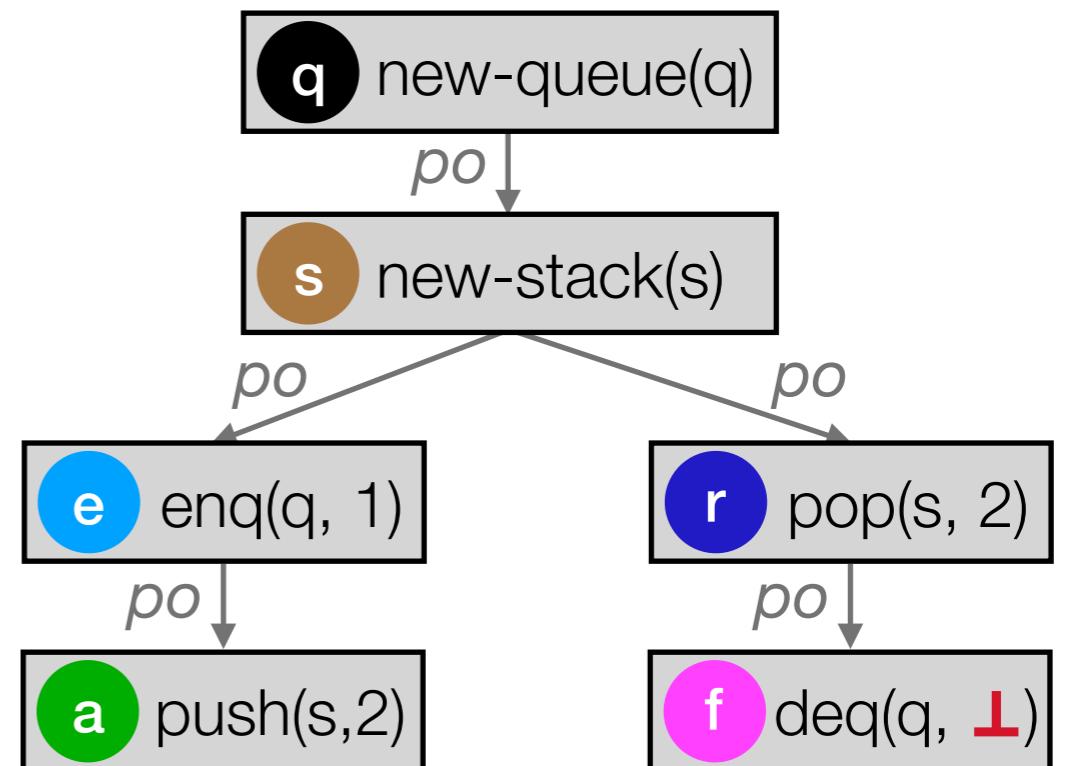
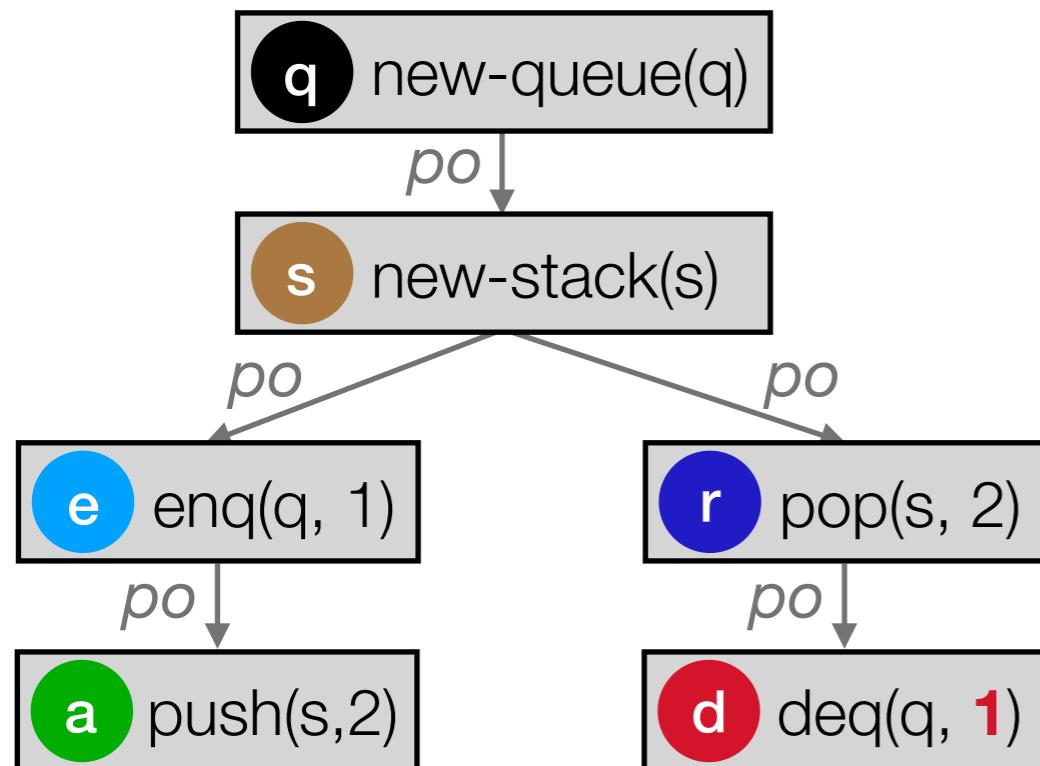
semantics of  $P$       |      execution of  $P$       events      |      program-order      |      synchronisation-order  
(library-specific)

# Program Executions

$$[\![P]\!] = \{ G_P = \langle E, po, so \rangle \mid \dots \}$$

semantics of  $P$       execution of  $P$

```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1);   || a:=pop(s);  
push(s, 2)     || if(a==2)  
                  b:=deq(q) // should return 1
```



# Program Executions

$$[\![P]\!] = \{ G_P = \langle E, po, so \rangle \mid \dots \}$$

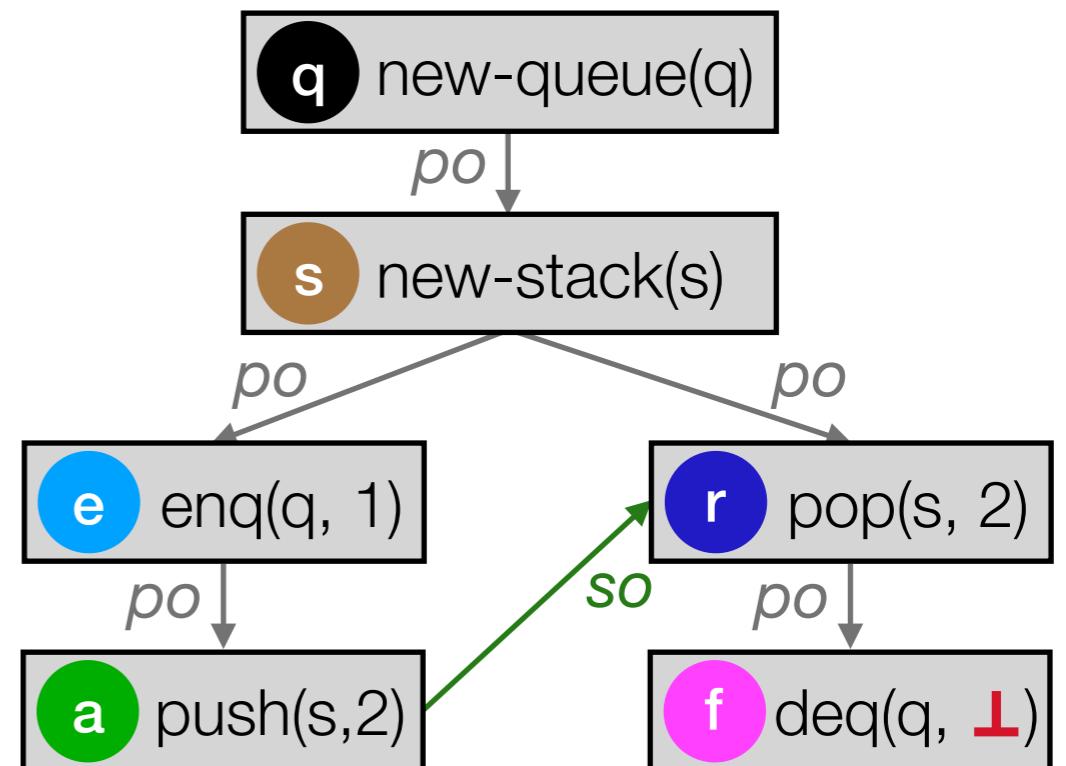
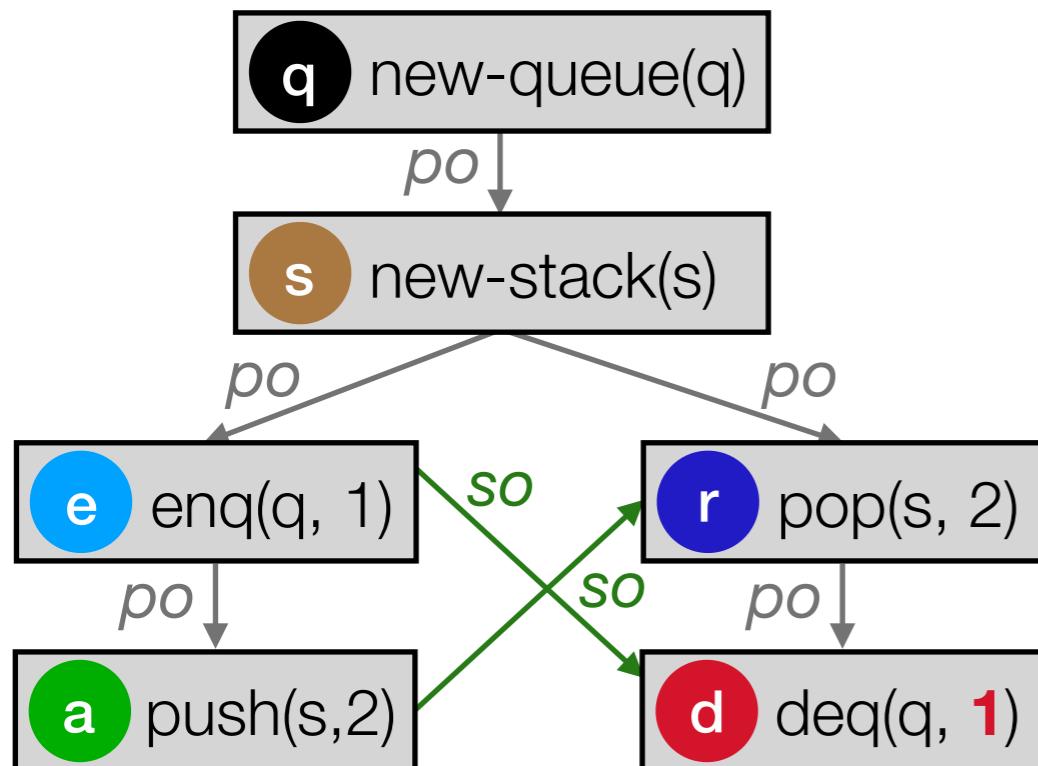
| semantics of  $P$

| execution of  $P$

```

q:=new-queue();
s:=new-stack();

enq(q,1) || a:=pop(s);
push(s,2)   if(a==2)
              b:=deq(q) // should return 1
  
```



# Program Executions

$$[\![P]\!] = \{ G_P = \langle E, po, so \rangle \mid \dots \}$$

semantics of  $P$

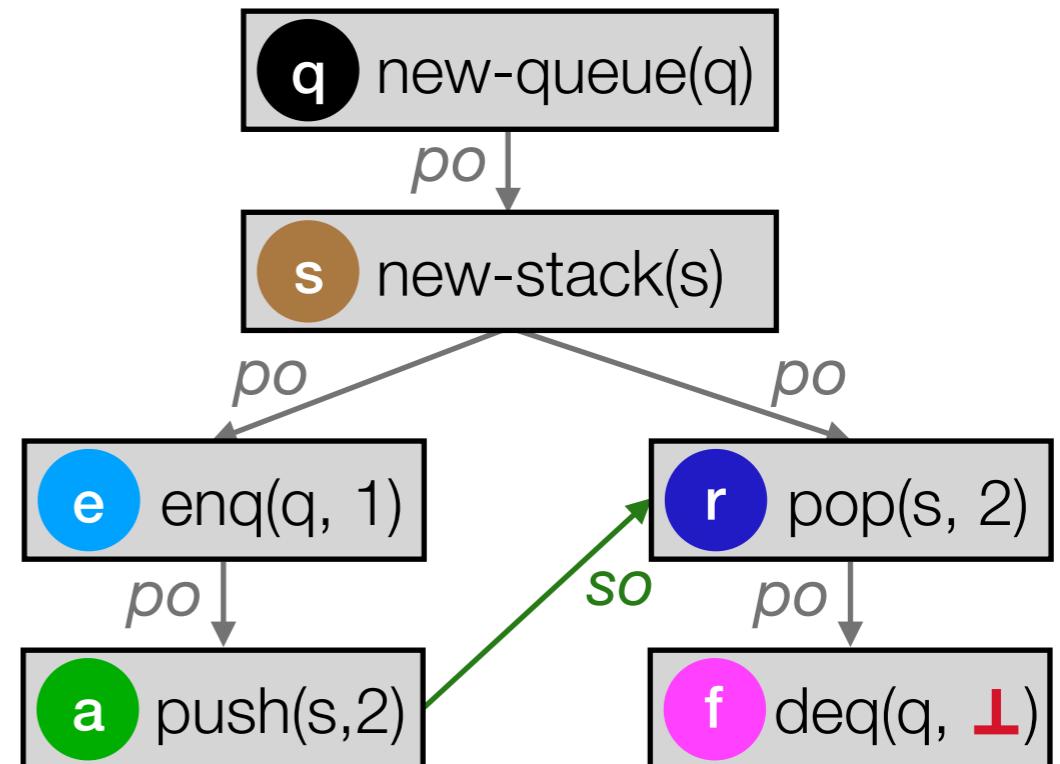
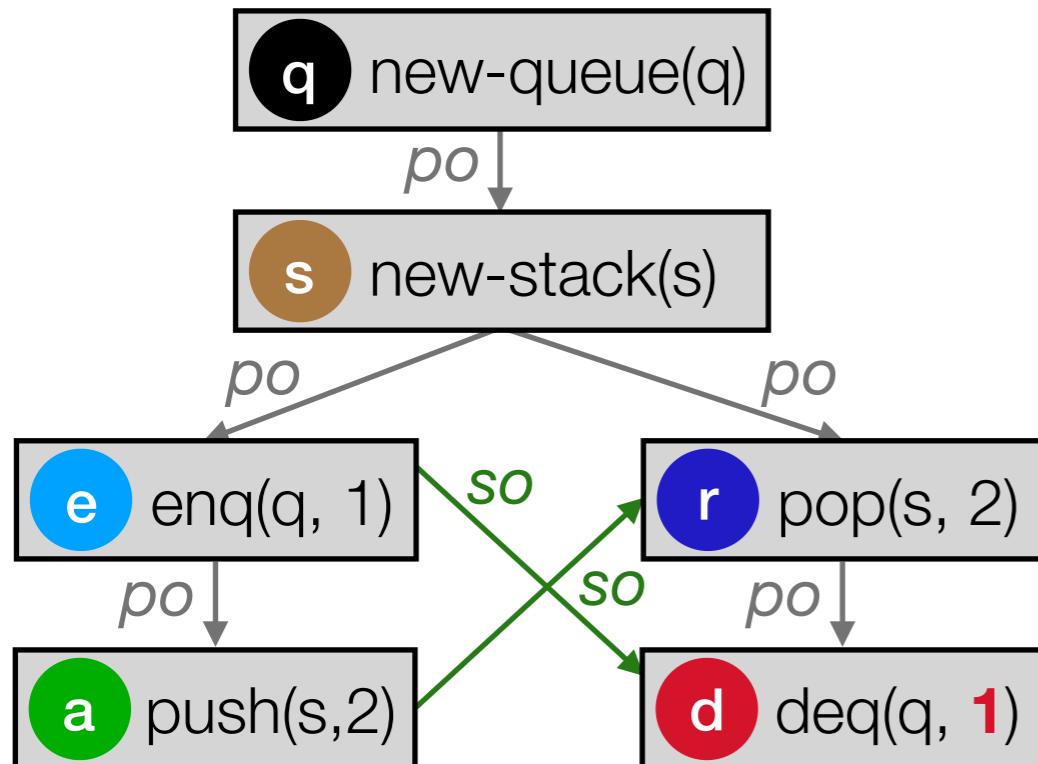
execution of  $P$

$$hb = (po \cup so)^+$$

```

q:=new-queue();
s:=new-stack();

enq(q,1) || a:=pop(s);
push(s,2)   if(a==2)
              b:=deq(q) // should return 1
  
```



# Program Executions

$$[\![P]\!] = \left\{ G_P = \langle E, \text{po} , \text{so} \rangle \mid \dots \right\}$$

semantics of  $P$

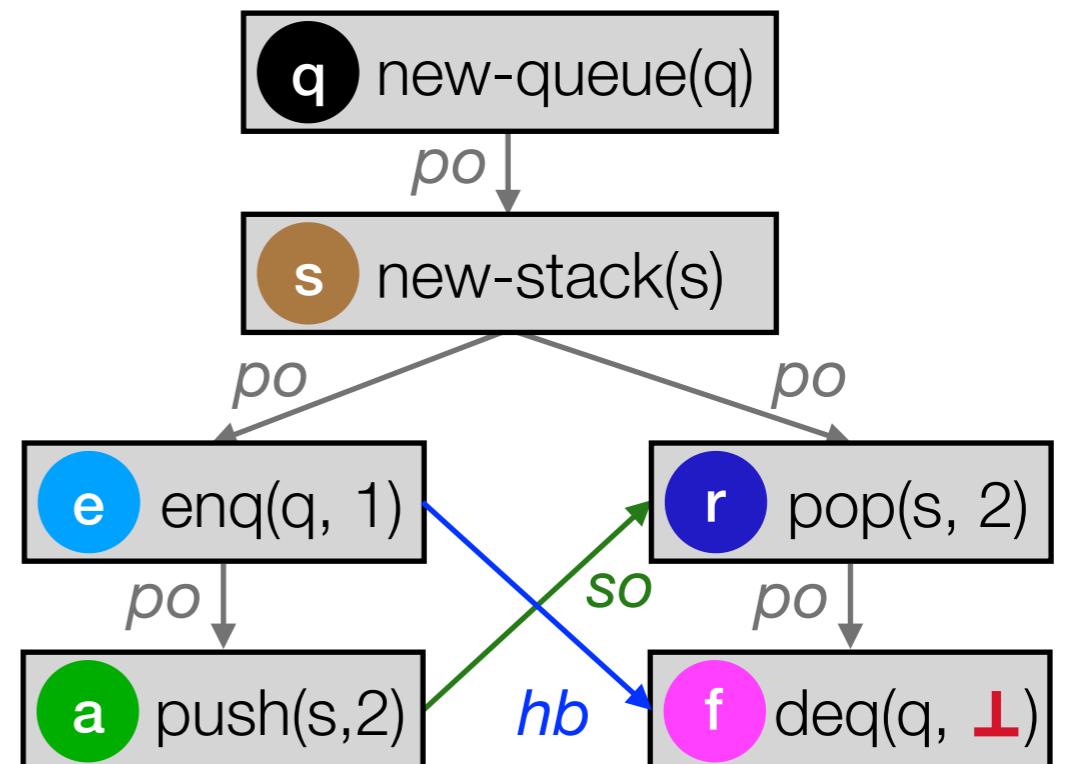
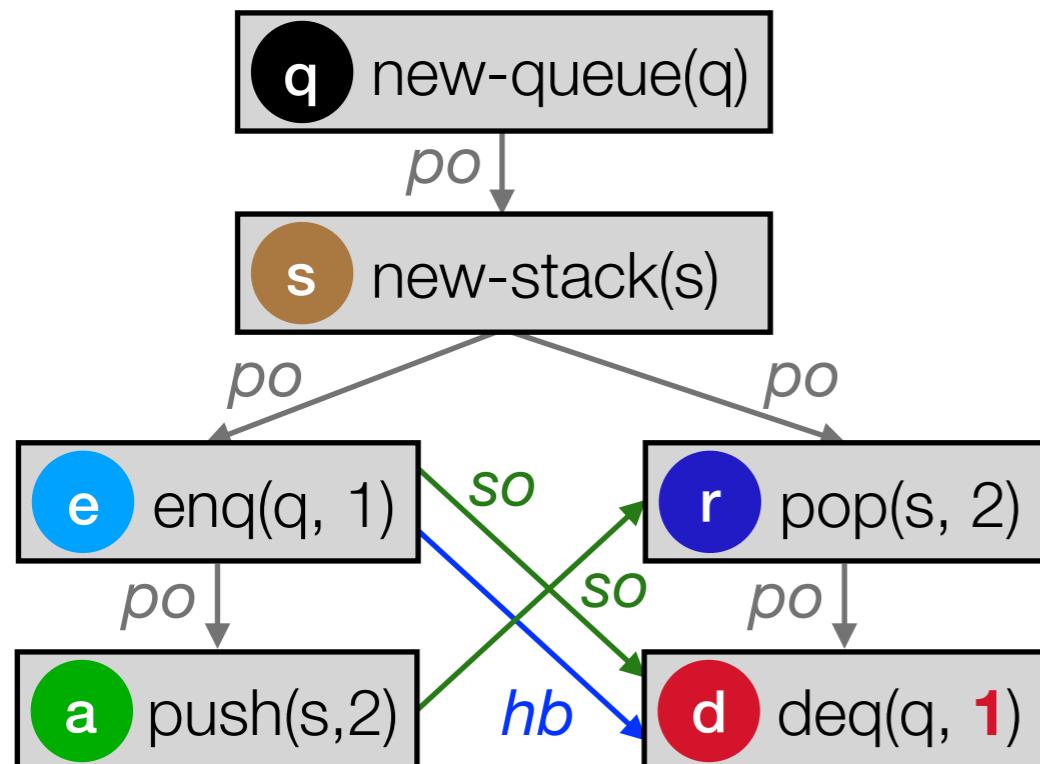
execution of  $P$

$$hb = (\text{po} \cup \text{so})^+$$

```

q:=new-queue();
s:=new-stack();

enq(q,1) || a:=pop(s);
push(s,2)   if(a==2)
              b:=deq(q) // should return 1
  
```



# Program Executions

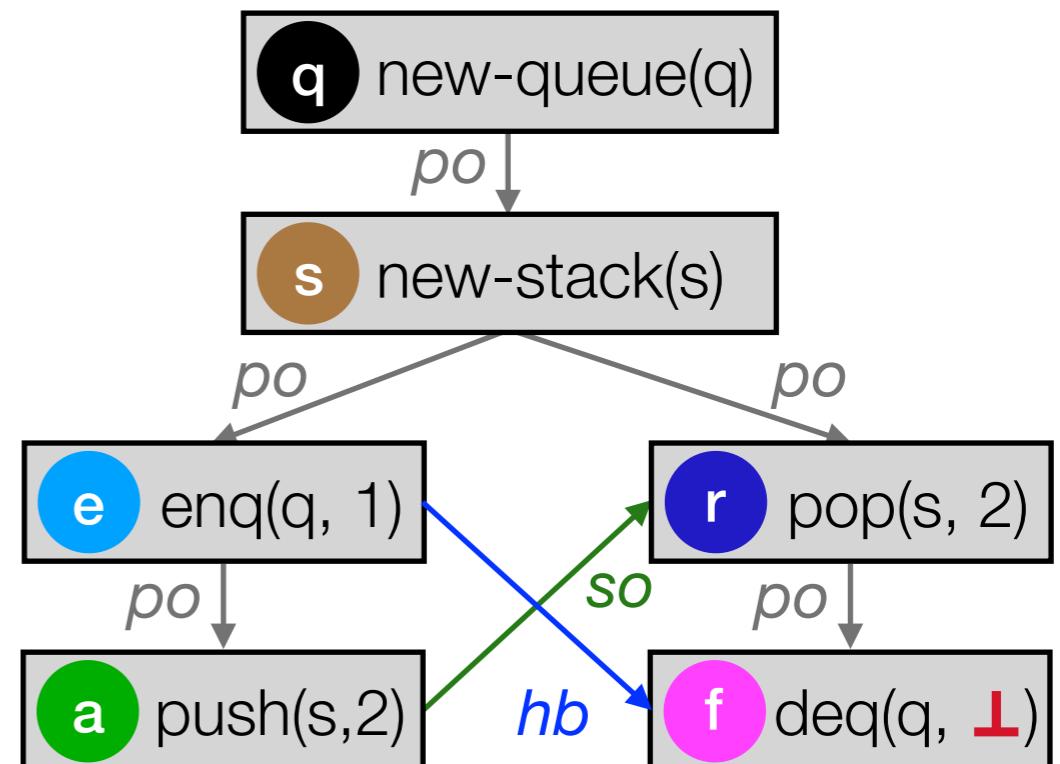
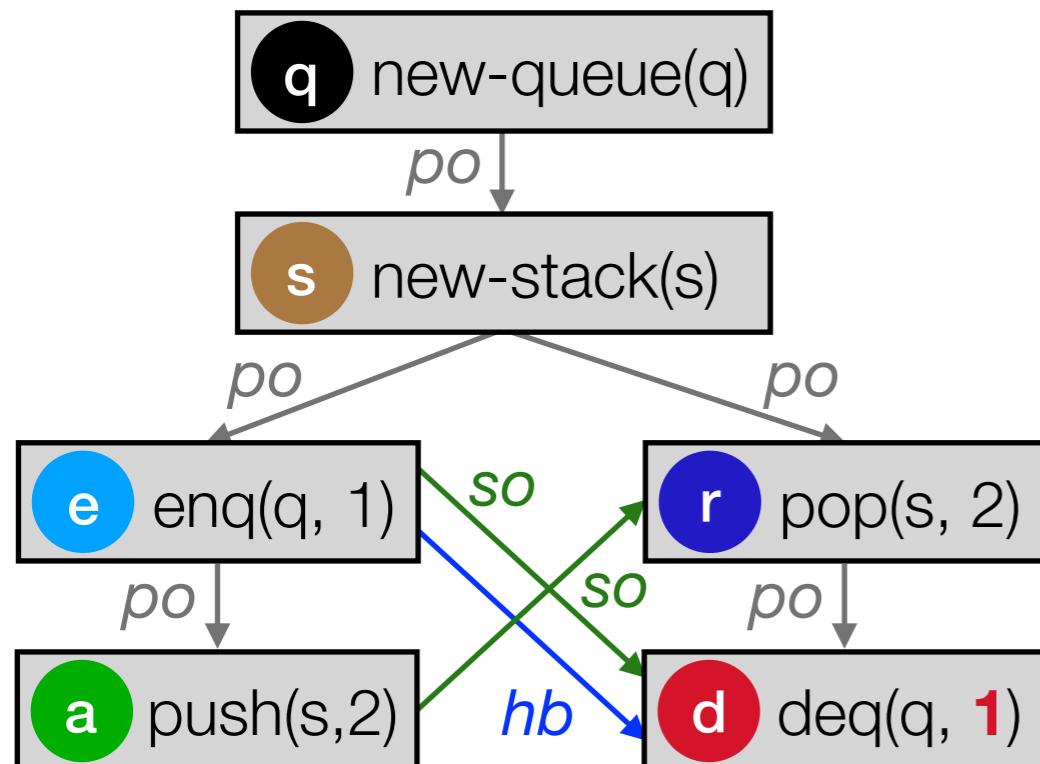
$$\llbracket P \rrbracket = \{ G_P = \langle E, \text{po} , \text{so} \rangle \mid \dots \}$$

semantics of  $P$

execution of  $P$

$hb = (\text{po} \cup \text{so})^+$

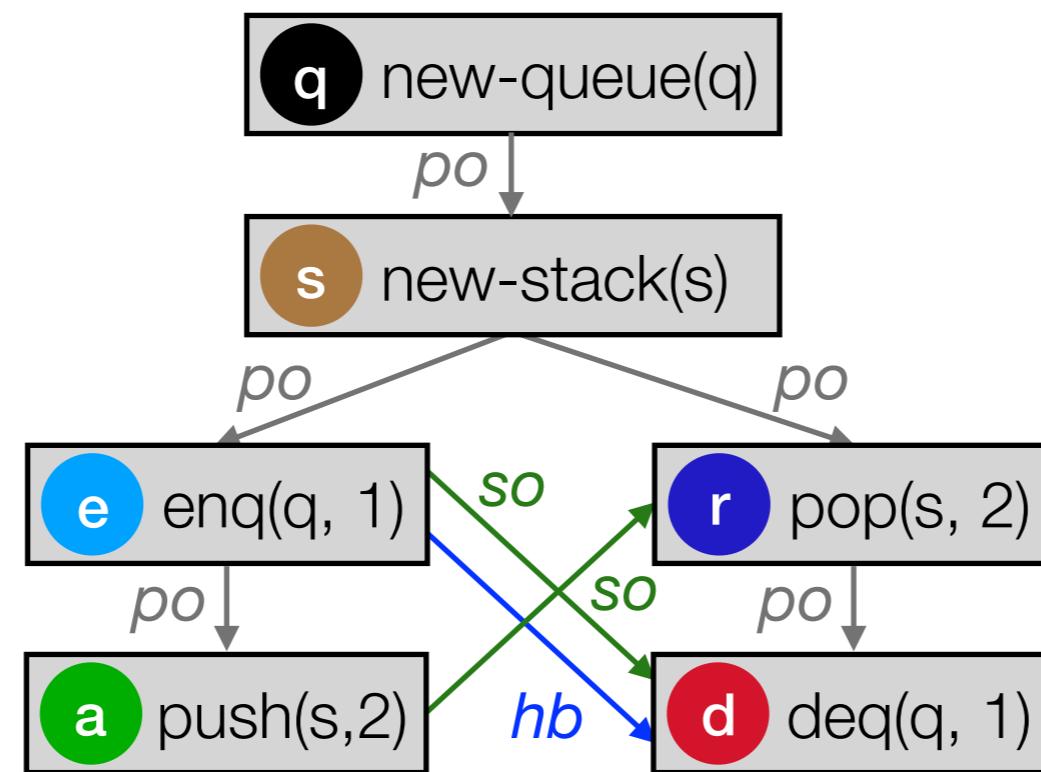
allow **libraries** to **constrain each other** via  $hb$ !  
How?  $hb$  defined on program executions



# From *Program* to *Library* Executions

$$G_P = \langle E, po, so \rangle$$

$$hb = (po \cup so)^+$$

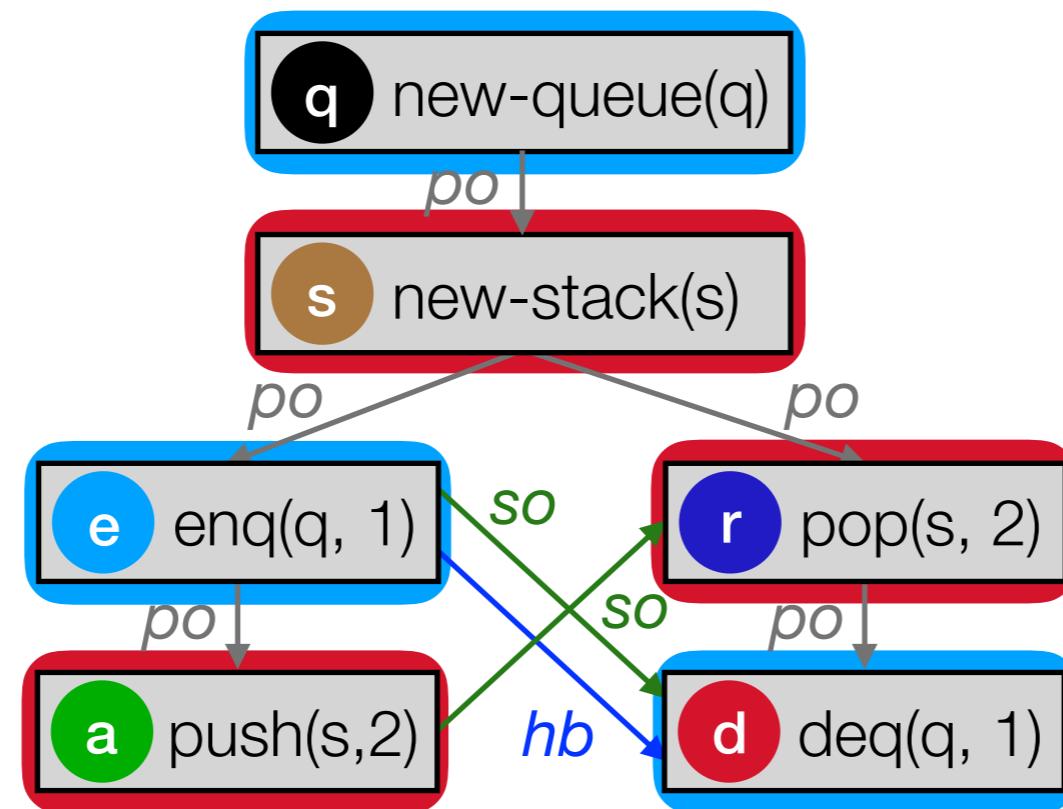


# From *Program* to *Library* Executions

$$G_P = \langle E, po, so \rangle$$

$E \equiv E_{queue} \cup E_{stack}$

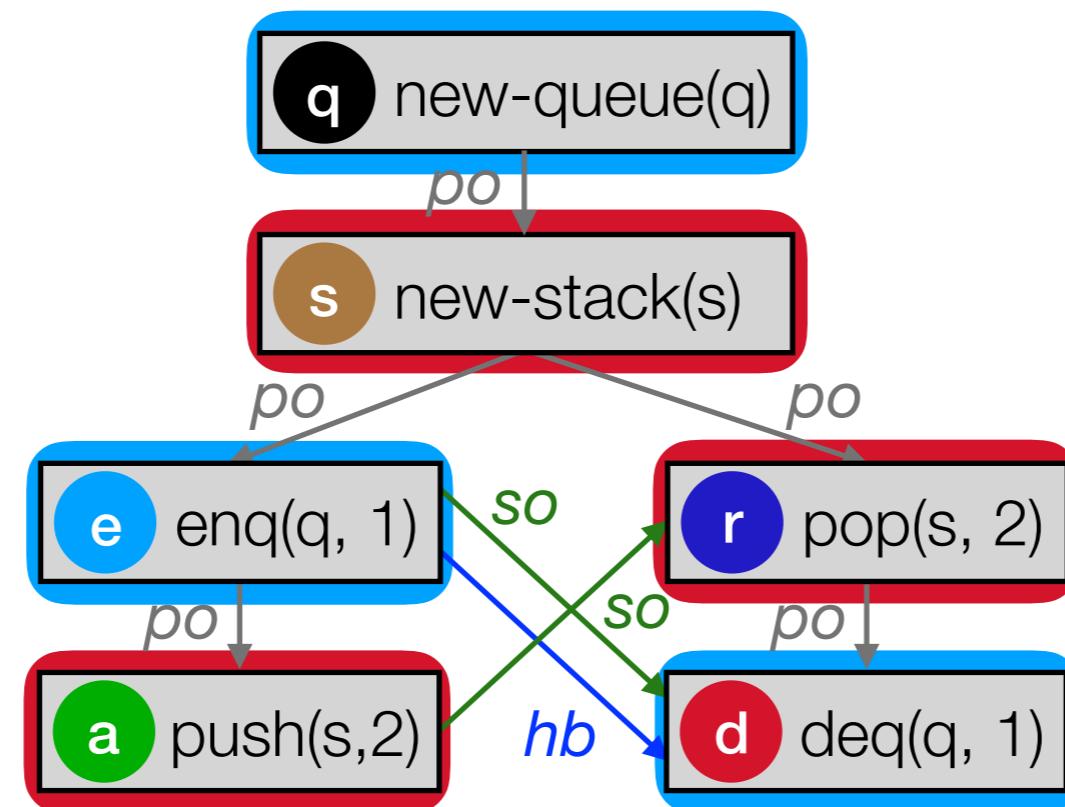
$$hb = (po \cup so)^+$$



# From *Program* to *Library* Executions

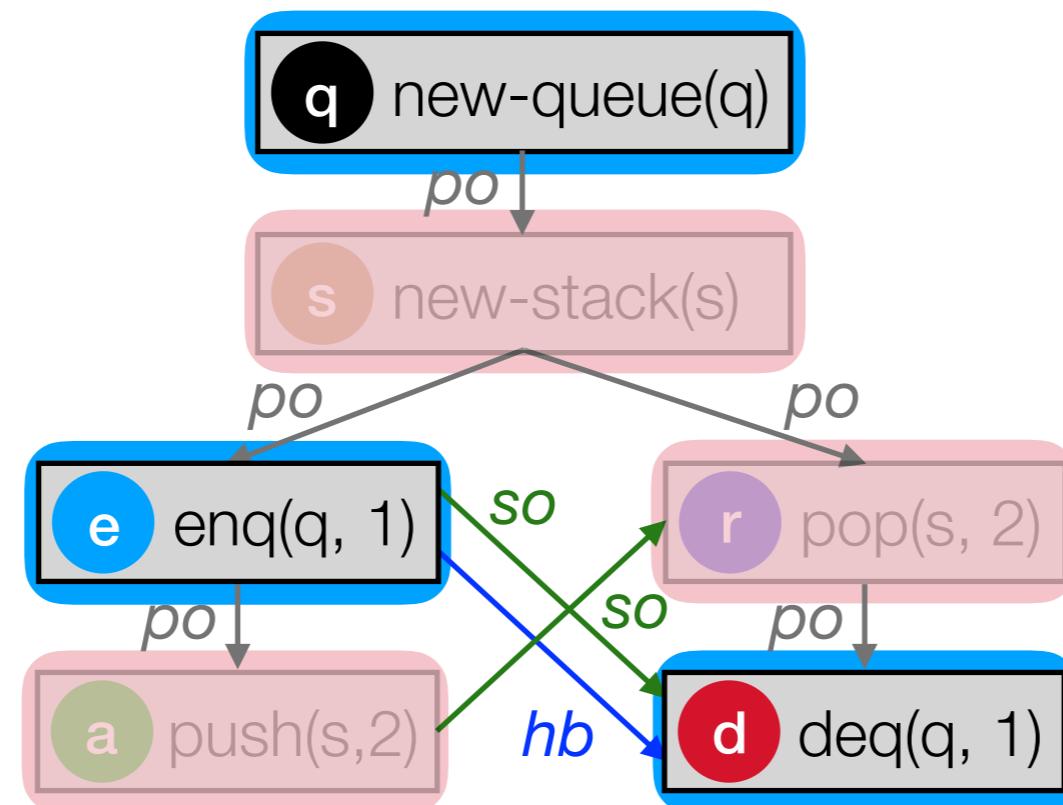
$$G_P = \langle \begin{array}{l} E \\ \downarrow \\ E_{\text{queue}} \cup E_{\text{stack}} \end{array}, po, so \rangle$$

$hb = (po \cup so)^+$



# From *Program* to *Library* Executions

$$G_P = \langle \begin{array}{l} E \\ \downarrow \\ E_{\text{queue}} \cup E_{\text{stack}} \end{array}, po, so \rangle$$
$$hb = (po \cup so)^+$$
$$G_{\text{queue}} \oplus G_{\text{stack}}$$



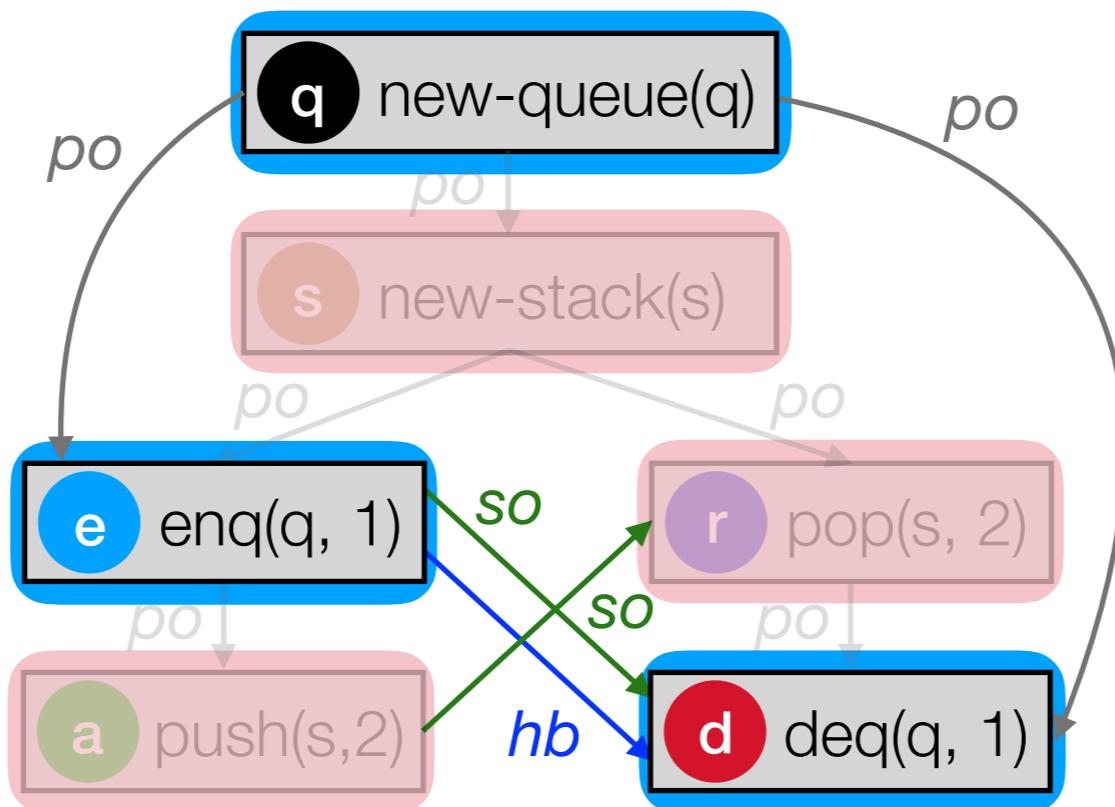
$$G_{\text{queue}} = \langle E_{\text{queue}}, \quad >$$

# From *Program* to *Library* Executions

$$G_P = \langle E, po, so \rangle$$

$E_{queue} \cup E_{stack}$

$$hb = (po \cup so)^+$$



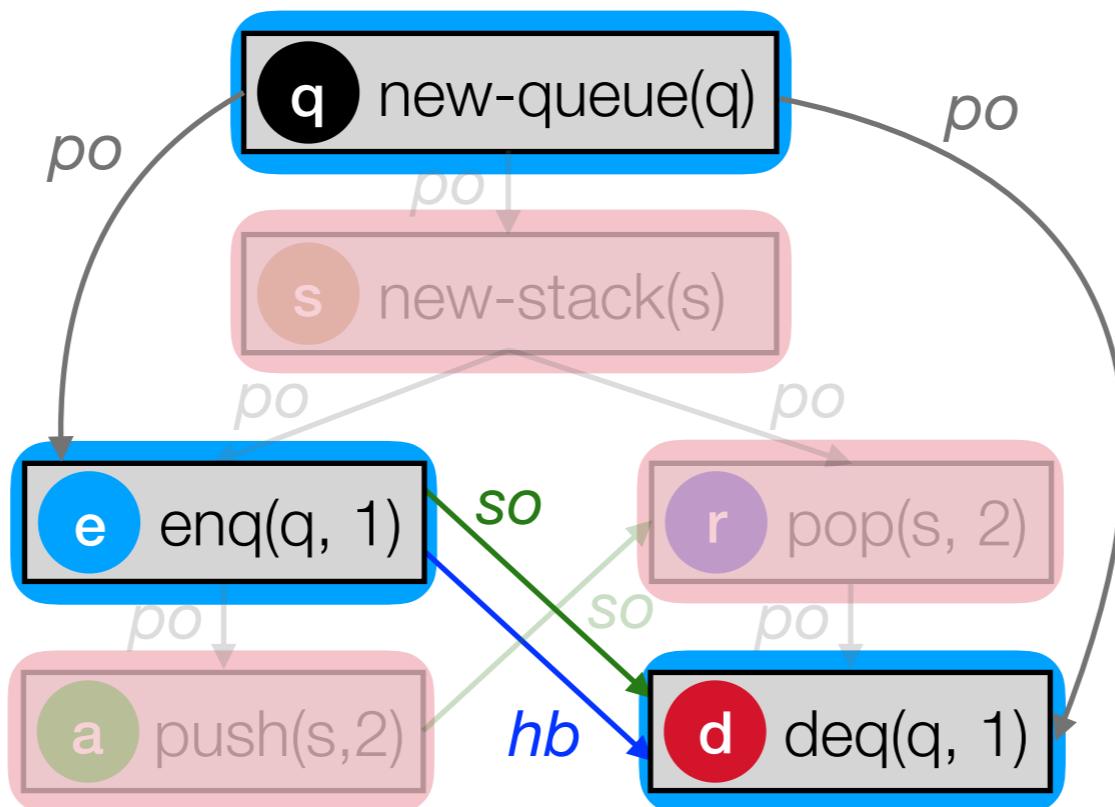
$$G_{queue} = \langle E_{queue}, po_{queue}, \rangle$$

# From *Program* to *Library* Executions

$$G_P = \langle E, po, so \rangle$$

$E_{queue} \cup E_{stack}$

$$hb = (po \cup so)^+$$



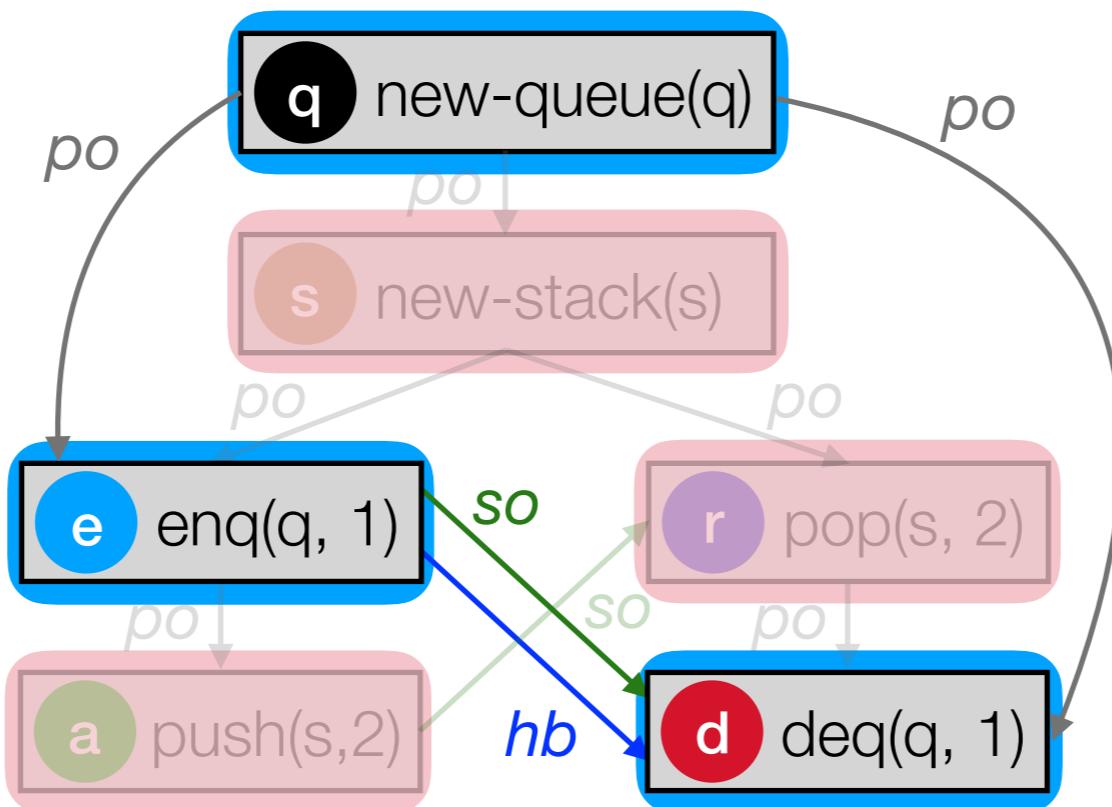
$$G_{queue} = \langle E_{queue}, po_{queue}, so_{queue}, \rangle$$

# From *Program* to *Library* Executions

$$G_P = \langle E, po, so \rangle$$

$E_{queue} \cup E_{stack}$

$$hb = (po \cup so)^+$$



$$G_{queue} = \langle E_{queue}, po_{queue}, so_{queue}, hb? \rangle$$

$|$   
 $hb_{queue}$

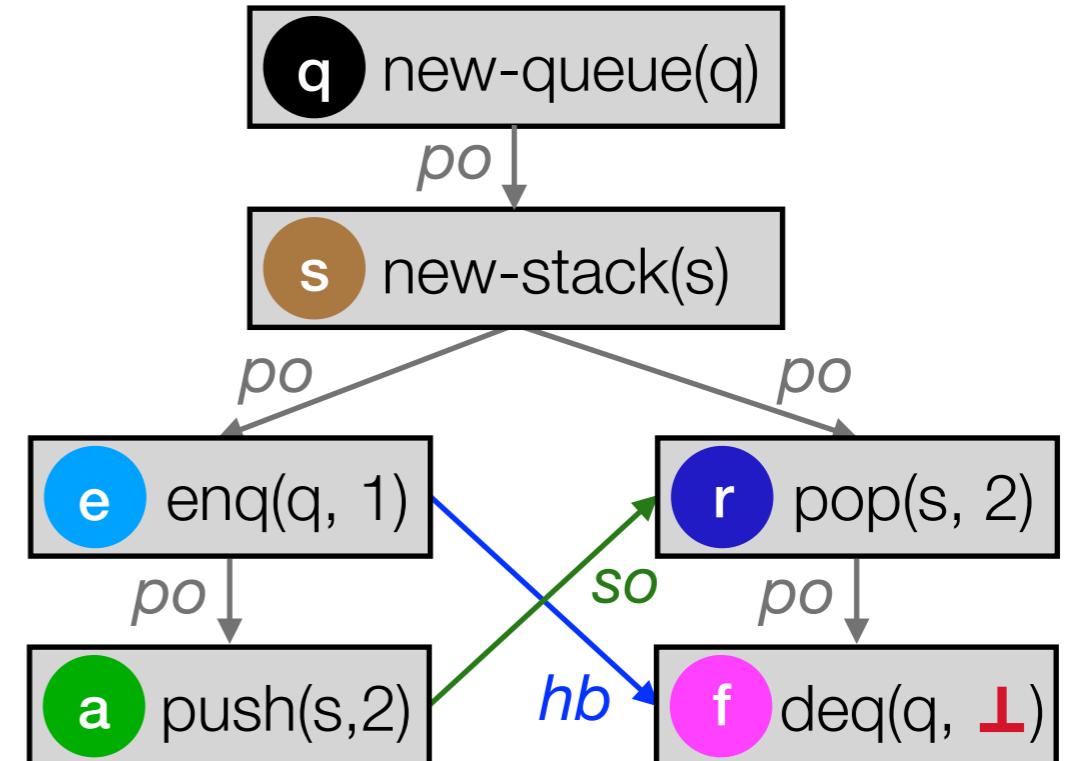
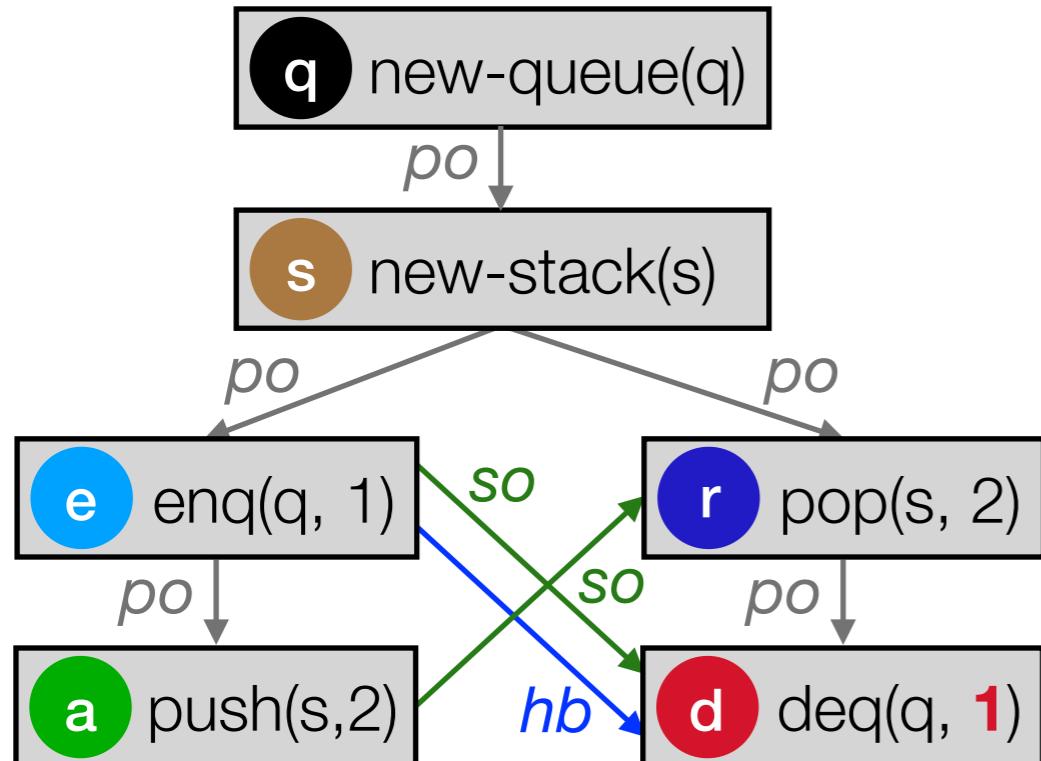
# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q,1); || a:=pop(s);
push(s,2)   || if(a==2)
                           b:=deq(q) // should return 1

```

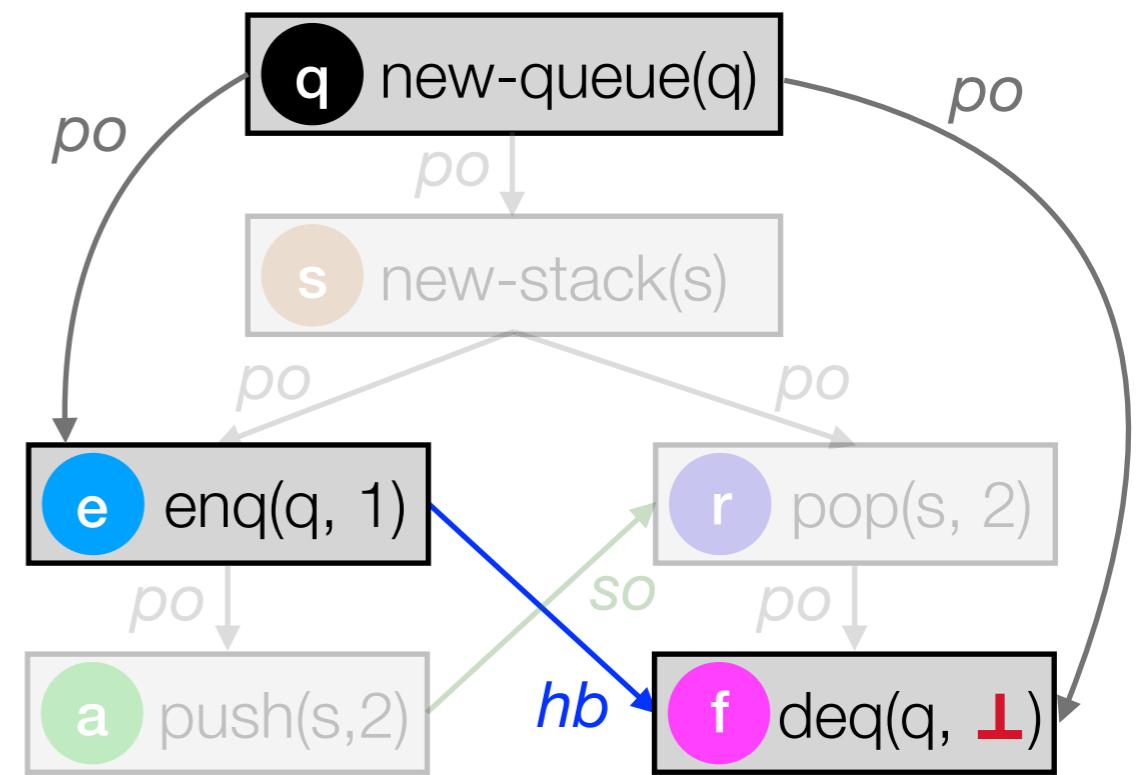
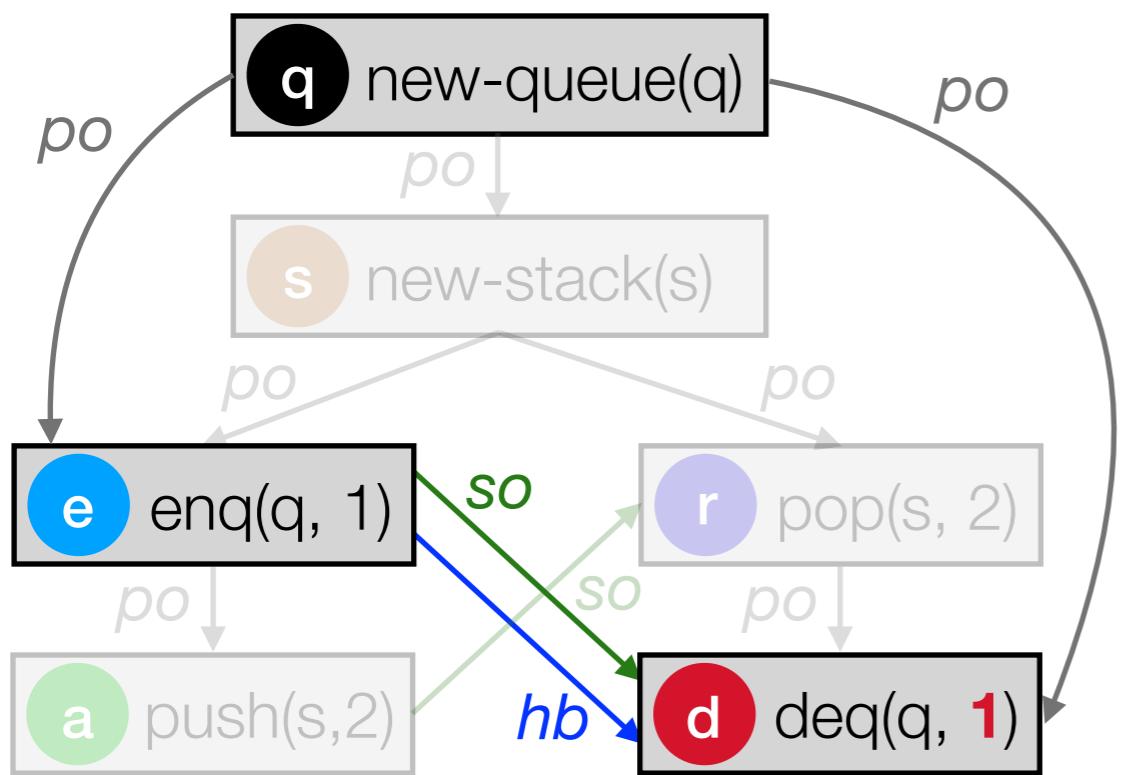


# Example Revisited

```

q:=new-queue();
s:=new-stack();

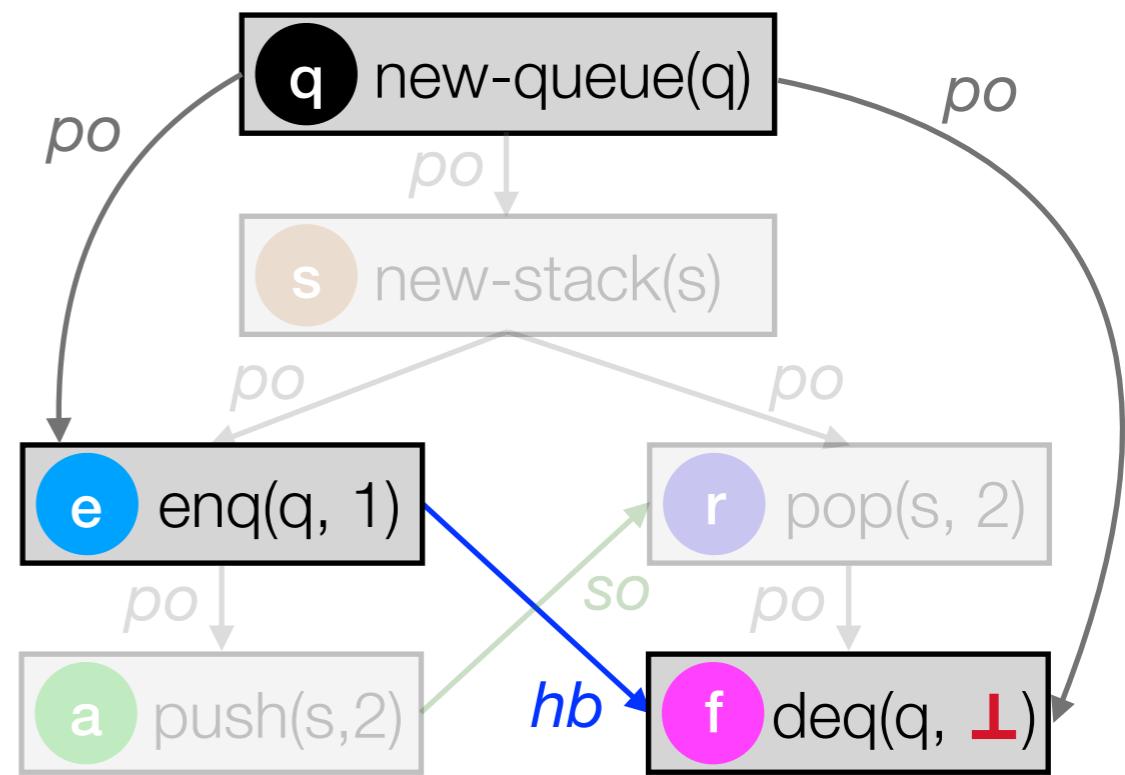
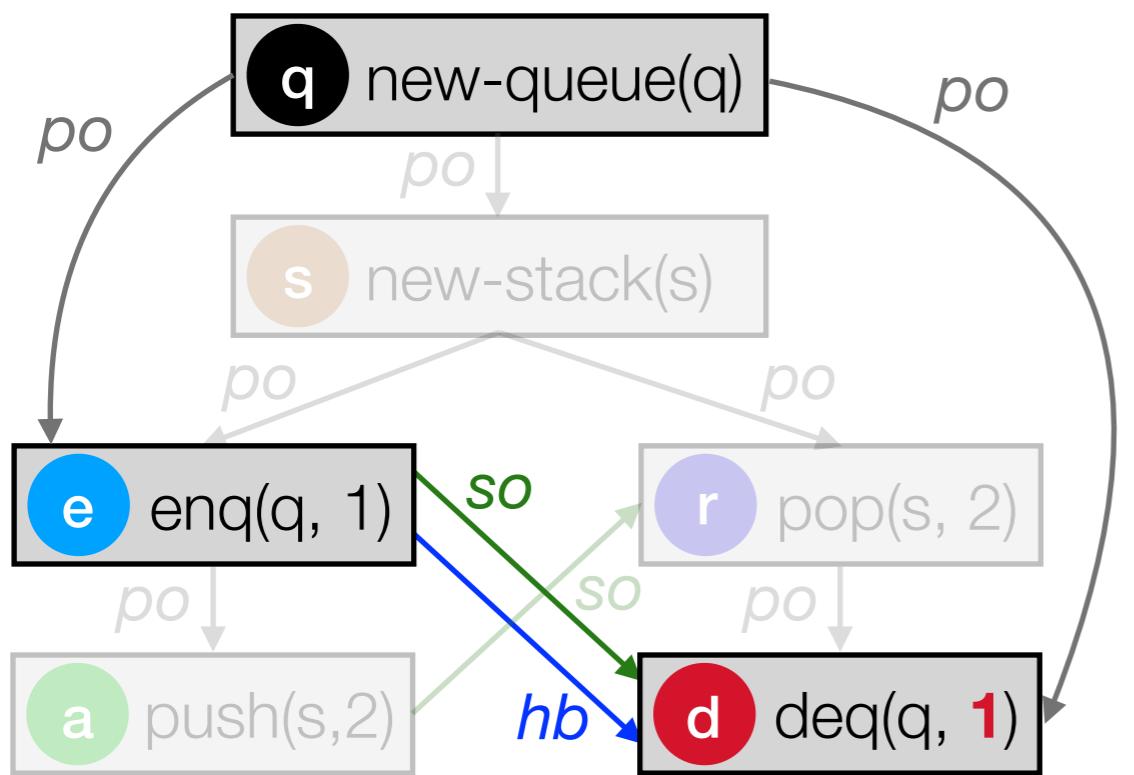
enq(q,1); || a:=pop(s);
push(s,2)   || if(a==2)
                           b:=deq(q) // should return 1
  
```



# Example Revisited

```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1); || a:=pop(s);  
push(s, 2) || if(a==2)  
                 b:=deq(q) // should return 1
```

How does *hb* **exclude** the *RHS* execution?  
👉 library **axioms!**



# Queue Axioms

$G = < E, po, so, hb >$  is a consistent **queue** execution iff:

1.  $E$  contains queue events

 new-queue(q)

 enq(q, v)

 deq(q, w)

 deq(q, ⊥)

$v, w \in Val$

# Queue Axioms

$G = < E, po, so, hb >$  is a consistent **queue** execution iff:

1.  $E$  contains queue events



2.  $so$  is **1-to-1**

$so$  relates **matching** enq/deq events;



# Queue Axioms

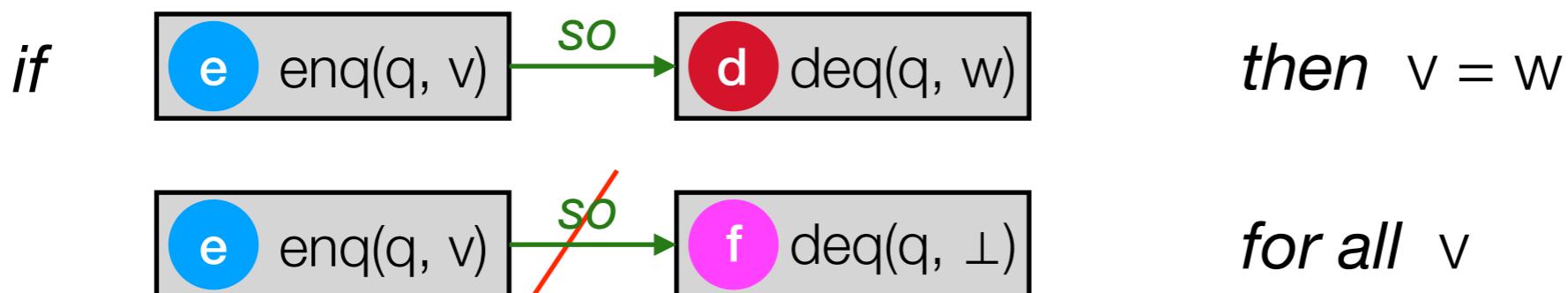
$G = < E, po, so, hb >$  is a consistent **queue** execution iff:

1.  $E$  contains queue events



2.  $so$  is **1-to-1**

$so$  relates **matching** enq/deq events;



# Queue Axioms

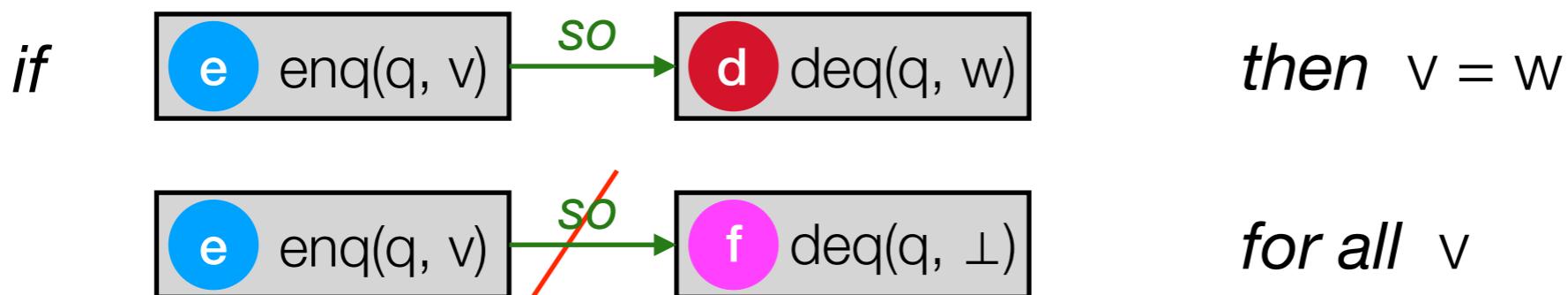
$G = \langle E, po, so, hb \rangle$  is a consistent **queue** execution iff:

1.  $E$  contains queue events



2.  $so$  is **1-to-1**

$so$  relates **matching** enq/deq events;

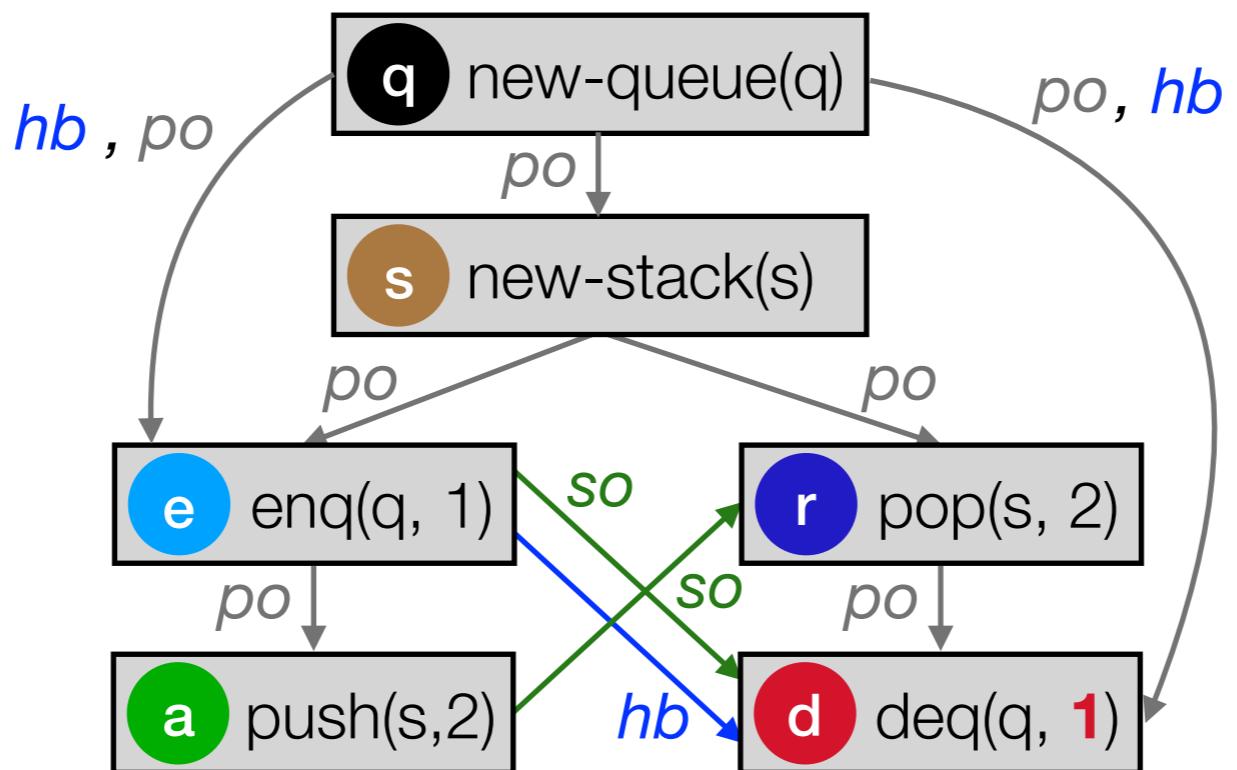


3.  $\exists$  **to.**  $to$  **totally** orders  $E$ ,

$hb \subseteq to$  and  $to$  is a FIFO sequence

# Example Revisited

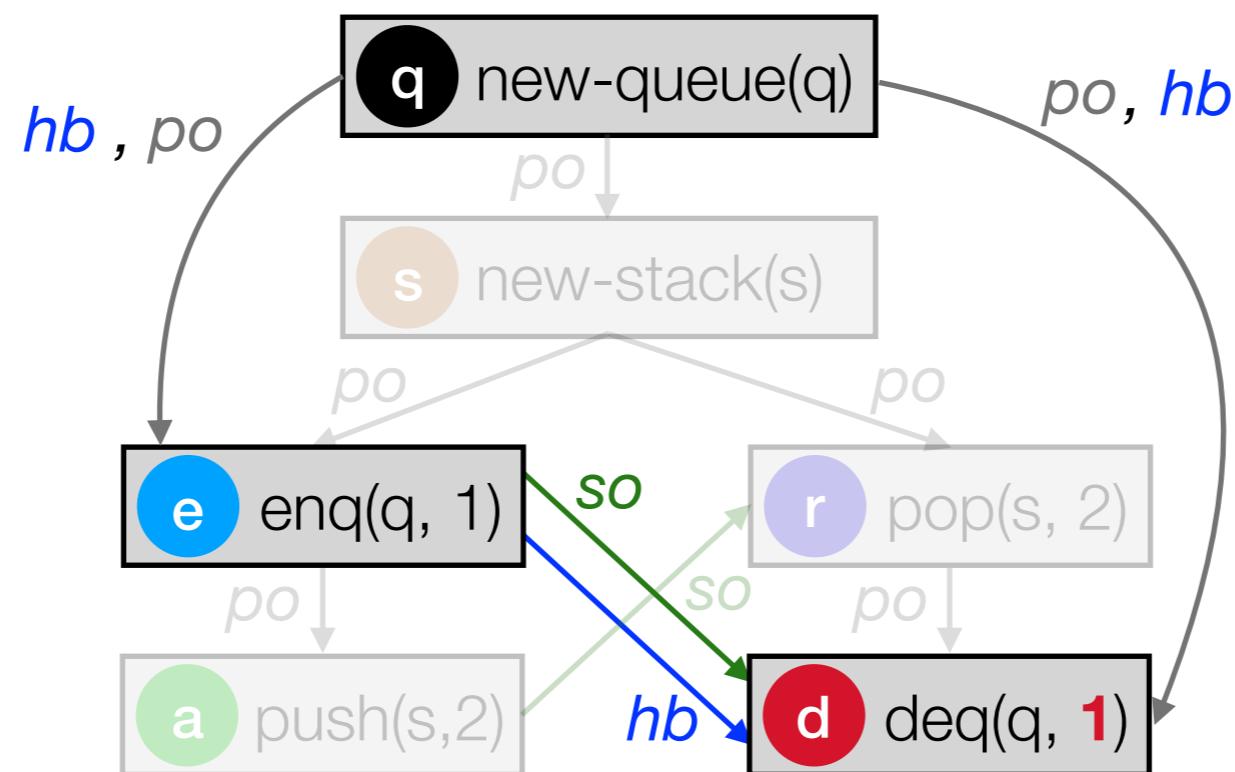
```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1); || a:=pop(s);  
push(s, 2) || if(a==2)  
                  b:=deq(q) // should return 1
```



$$hb = (po \cup so)^+$$

# Example Revisited

```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1); || a:=pop(s);  
push(s, 2) || if(a==2)  
                 b:=deq(q) // should return 1
```



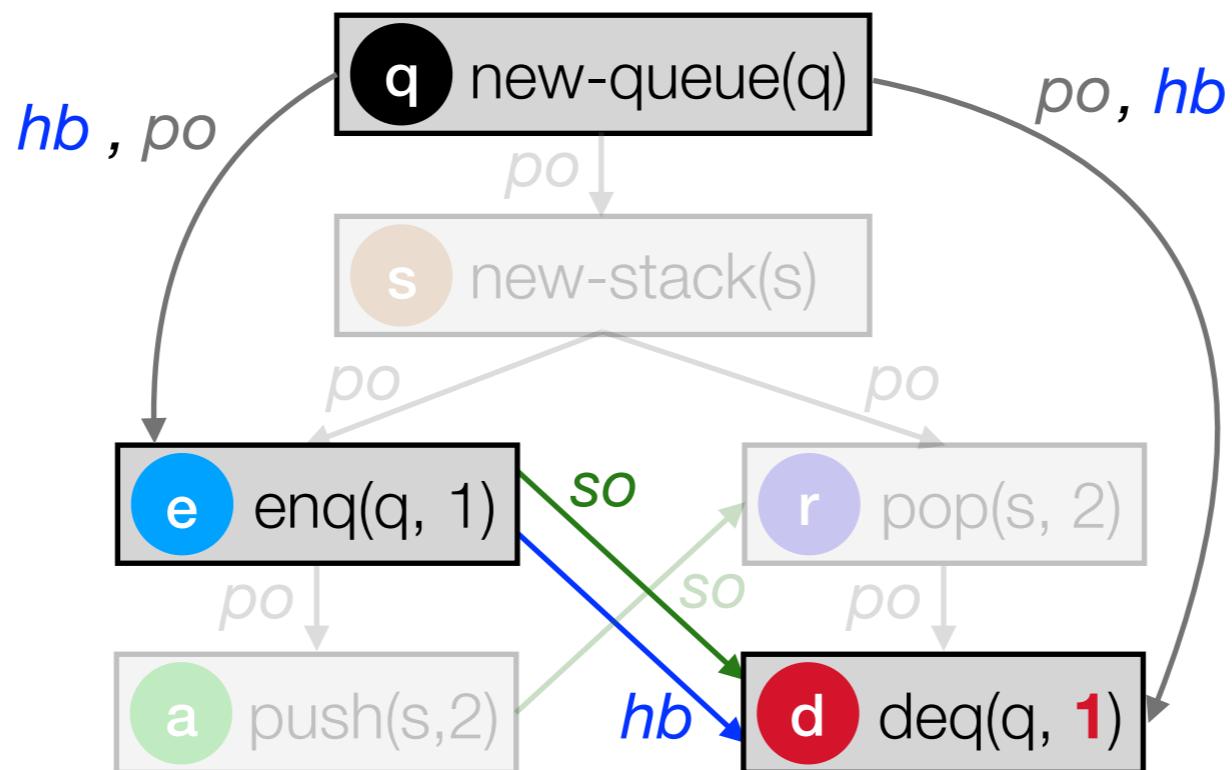
$$hb = (po \cup so)^+$$

# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q, 1); || a:=pop(s);
push(s, 2)   || if(a==2)
                b:=deq(q) // should return 1
  
```

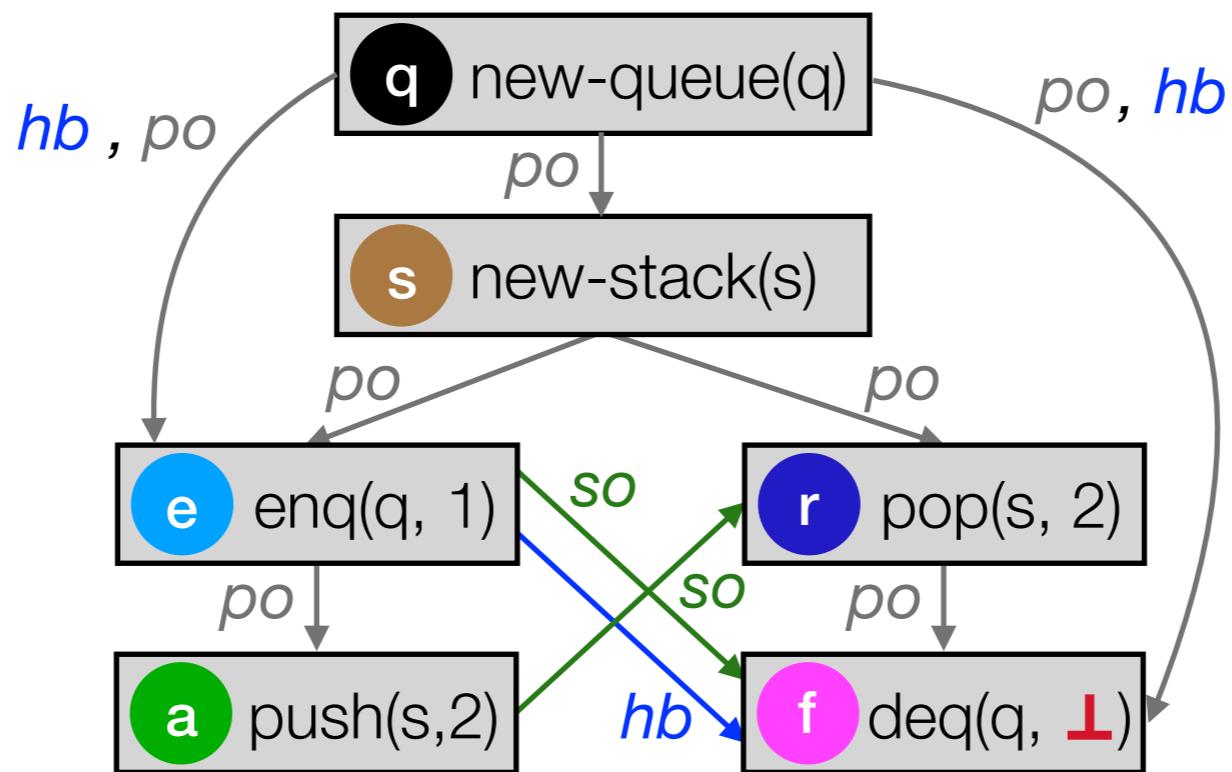


$hb = (po \cup so)^+$

$hb \subseteq to$

# Example Revisited

```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1); || a:=pop(s);  
push(s, 2) || if(a==2)  
                  b:=deq(q) // should return 1
```



$$hb = (po \cup so)^+$$

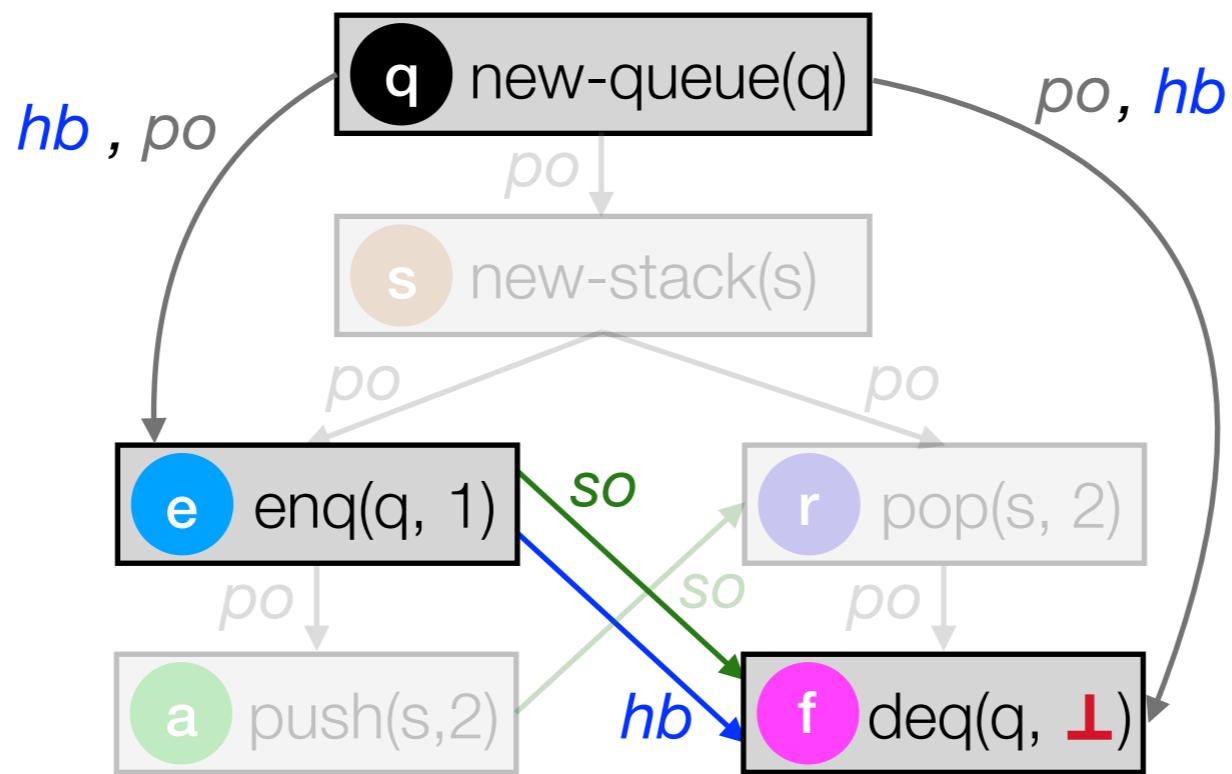
$$hb \subseteq to$$

# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q, 1);    || a:=pop(s);
push(s, 2)    || if(a==2)
                b:=deq(q) // should return 1
  
```



$$hb = (po \cup so)^+$$

$$hb \subseteq to$$

# Program Executions

$$[\![P]\!] = \{ G_P = \langle E, po, so \rangle \mid \dots \}$$

```
q:=new-queue();  
s:=new-stack();  
enq(q,1);   || a:=pop(s);  
push(s,2)   || if(a==2)  
              b:=deq(q)
```

# Program Executions

$$[\![P]\!] = \{ G_P = \langle E, po, so \rangle \mid \dots \}$$

?

```
q:=new-queue();  
s:=new-stack();  
enq(q,1);   || a:=pop(s);  
push(s,2)   || if(a==2)  
               b:=deq(q)
```

# Program Executions

$$[\![P]\!] = \{ G_P = \langle E, po, so \rangle \mid \dots \}$$

?

queue & stack calls

```
q:=new-queue();  
s:=new-stack();  
enq(q,1);    ||  a:=pop(s);  
push(s,2)    ||  if(a==2)  
                  b:=deq(q)
```

# Program Executions

$$\llbracket P \rrbracket = \{ G_P = \langle E, po, so \rangle \mid \begin{array}{l} G_{queue} \text{ sats. queue axioms} \\ G_{stack} \text{ sats. stack axioms} \end{array} \}$$

queue & stack calls

```
q:=new-queue();  
s:=new-stack();  
enq(q,1);    || a:=pop(s);  
push(s,2)    || if(a==2)  
                b:=deq(q)
```

# Program Executions

$\llbracket P \rrbracket = \{ G_P = \langle E, po, so \rangle \mid \begin{array}{l} G_{queue} \text{ sats. queue axioms} \\ G_{stack} \text{ sats. stack axioms} \end{array} \}$

queue & stack calls

```
q:=new-queue();  
s:=new-stack();  
  
enq(q, 1);   ||   a:=pop(s);  
push(s, 2)    ||   if(a==2)  
                  b:=deq(q)
```

$P$  calls  $L_1 \dots L_n$  and  $G \in \llbracket P \rrbracket$

$\iff$

$G_{L_1}$  sats.  $L_1$  axioms

...

$G_{L_n}$  sats.  $L_n$  axioms

# Queue Axioms

$G = \langle E, po, so, hb \rangle$  is a consistent **queue** execution iff:

1.  $E$  contains queue events
2.  $so$  is **1-to-1**;  $so$  relates **matching** enq/deq events
3.  $\exists$   $to$ .  $to$  **totally** orders  $E$ ,  
 $hb \subseteq to$  and  $to$  is a FIFO sequence

 too strong  
 difficult to find  $to$  witness

# **Strong** Queue Axioms

## C11 Herlihy-Wing Queue Implementation

*new-queue()*  $\triangleq$

let  $q = \text{alloc}(+\infty)$  in  $q$

*enq*( $q, v$ )  $\triangleq$

let  $i = \text{fetch-add}(q, 1, \text{rel})$  in  
 $\text{store}(q + i + 1, v, \text{rel});$

*deq*( $q$ )  $\triangleq$

loop

let  $\text{range} = \text{load}(q, \text{acq})$  in  
for  $i = 1$  to  $\text{range}$  do  
    let  $x = \text{atomic-xchg}(q + i, 0, \text{acq})$  in  
    if  $x \neq 0$  then break<sub>2</sub>  $x$



does **not** satisfy **strong** queue axioms

# **Strong** Queue Axioms

C11 Herlihy-Wing Queue Implementation

$\text{new-queue}() \triangleq$

let  $q = \text{alloc}(+\infty)$  in  $q$

$\text{enq}(q, v) \triangleq$

let  $i = \text{fetch-add}(q, 1, \text{rel})$  in  
 $\text{store}(q + i + 1, v, \text{rel});$

$\text{deq}(q) \triangleq$

loop

let  $\text{range} = \text{load}(q, \text{acq})$  in

for  $i = 1$  to  $\text{range}$  do

let  $x = \text{atomic-xchg}(q + i, 0, \cancel{\text{acq}})$  in  
if  $x \neq 0$  then break<sub>2</sub>  $x$

acqrel

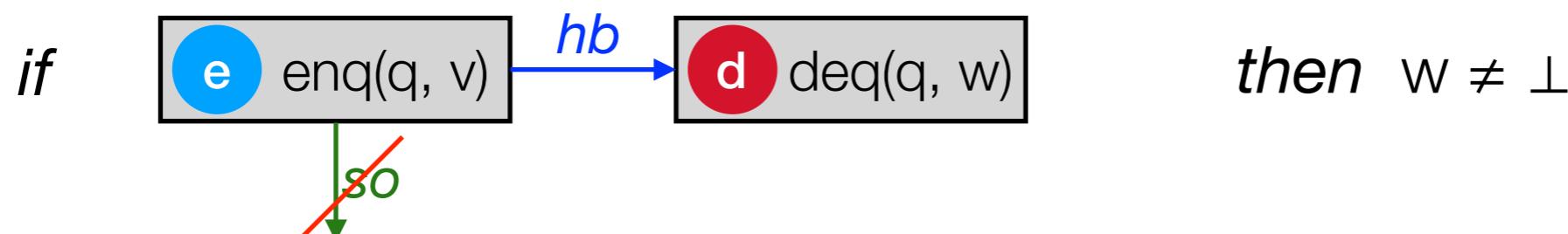


satisfies **strong** queue axioms

# Weak Queue Axioms

$G = \langle E, po, so, hb \rangle$  is a consistent **weak queue** execution iff:

1.  $E$  contains queue events
2.  $so$  is **1-to-1**;  $so$  relates **matching** enq/deq events
3. deq with ***hb*-earlier unmatched** enq cannot return  $\perp$

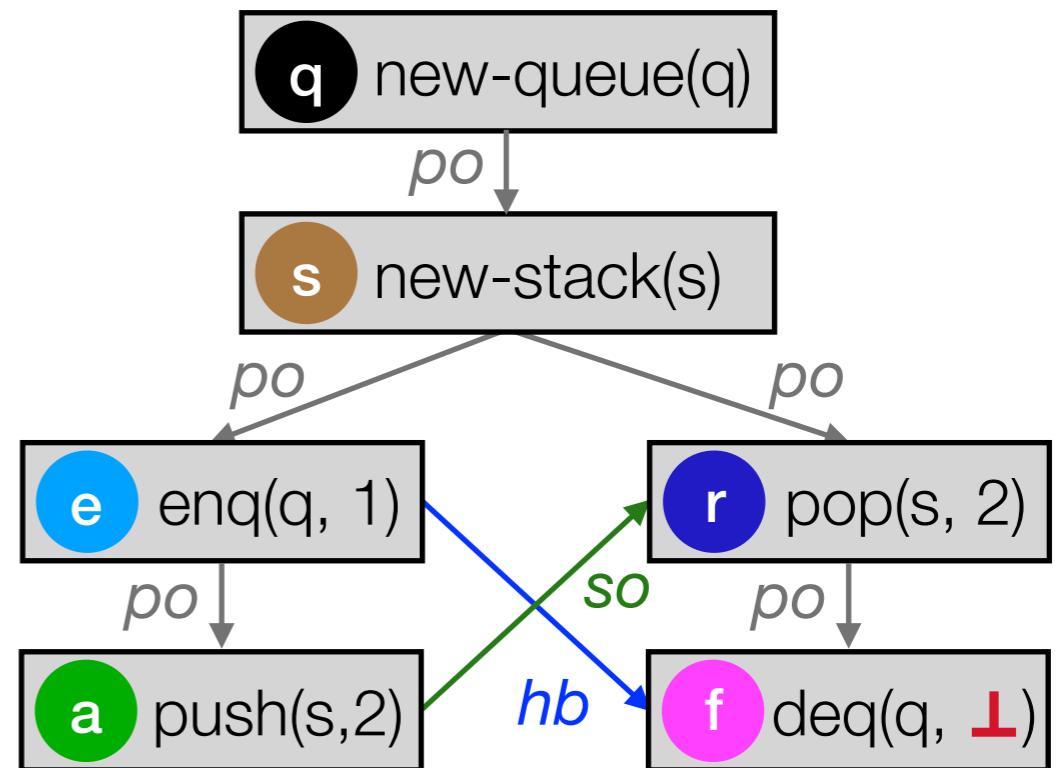
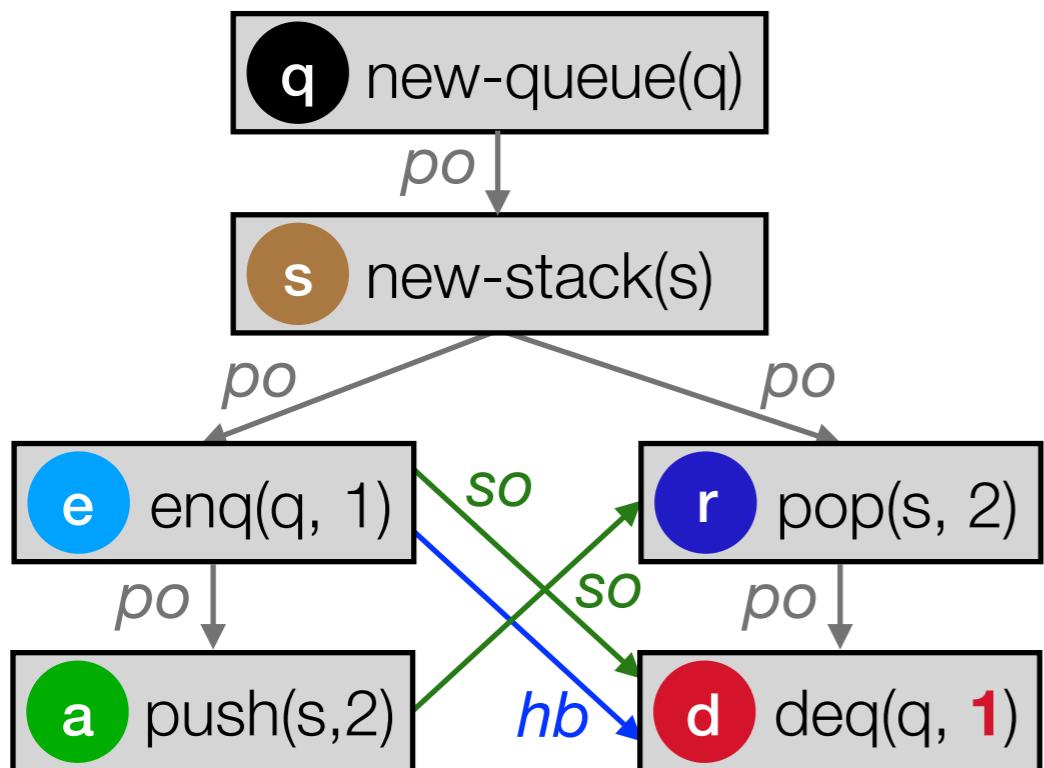


# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q,1); || a:=pop(s);
push(s,2)   || if(a==2)
              b:=deq(q) // should return 1
  
```

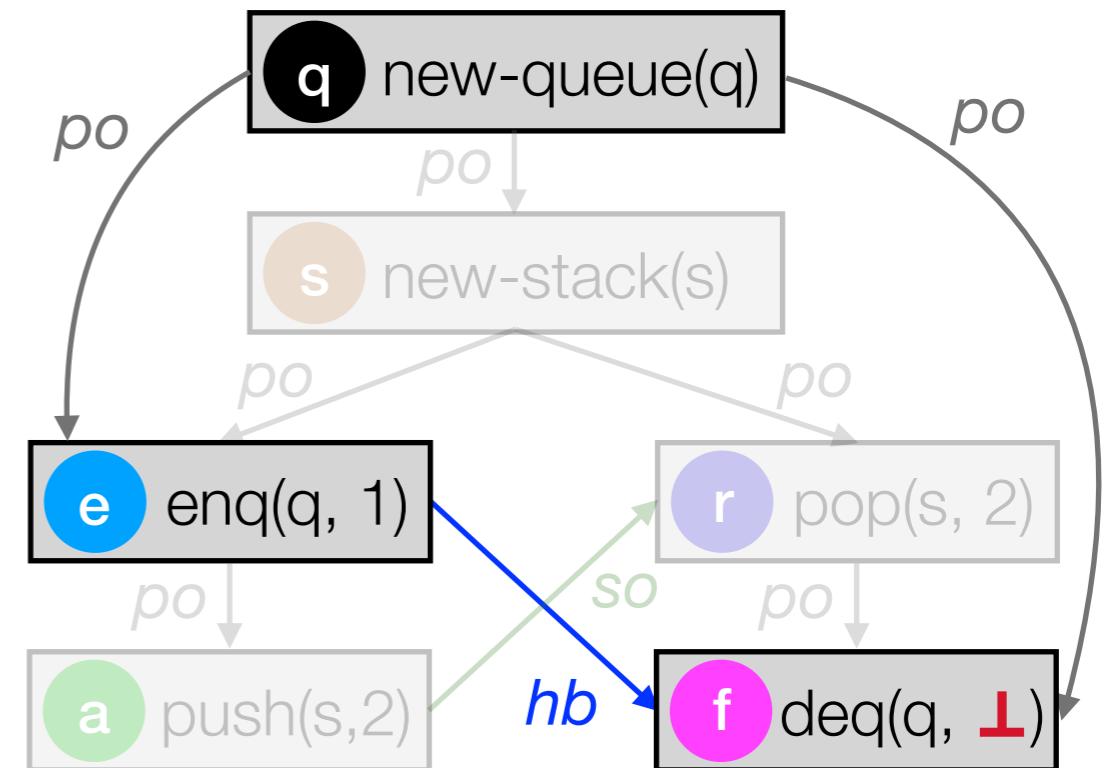
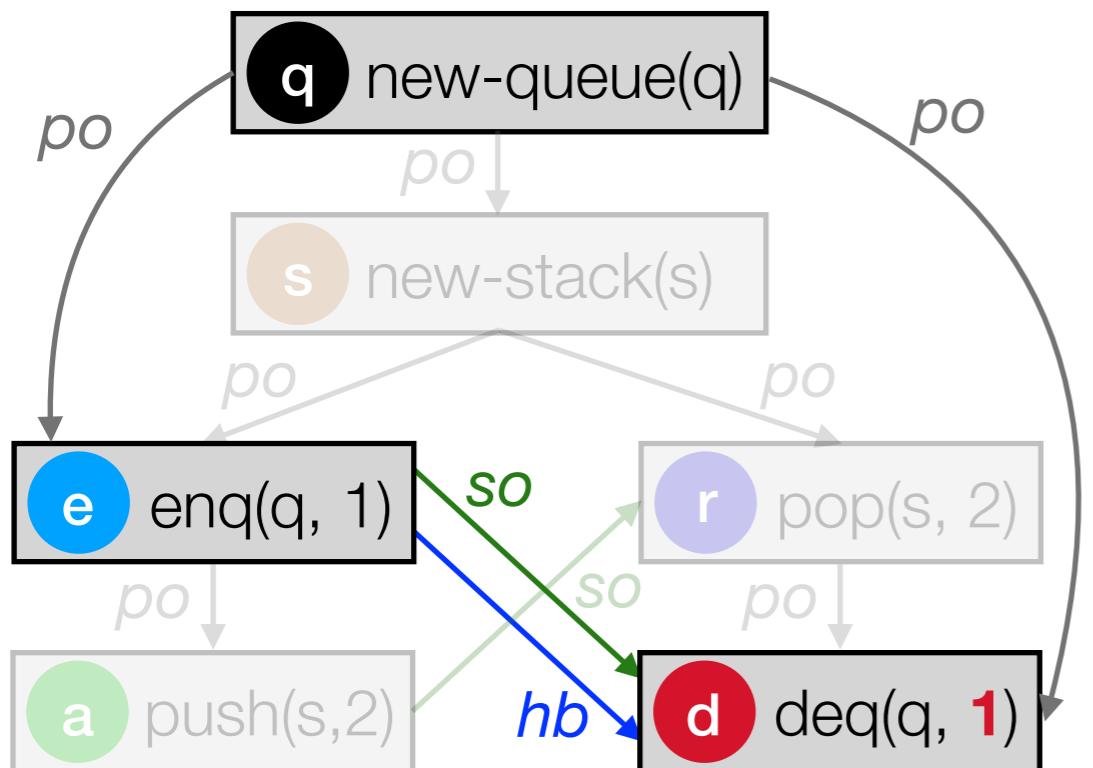


# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q, 1); || a:=pop(s);
push(s, 2)   || if(a==2)
                b:=deq(q) // should return 1
  
```

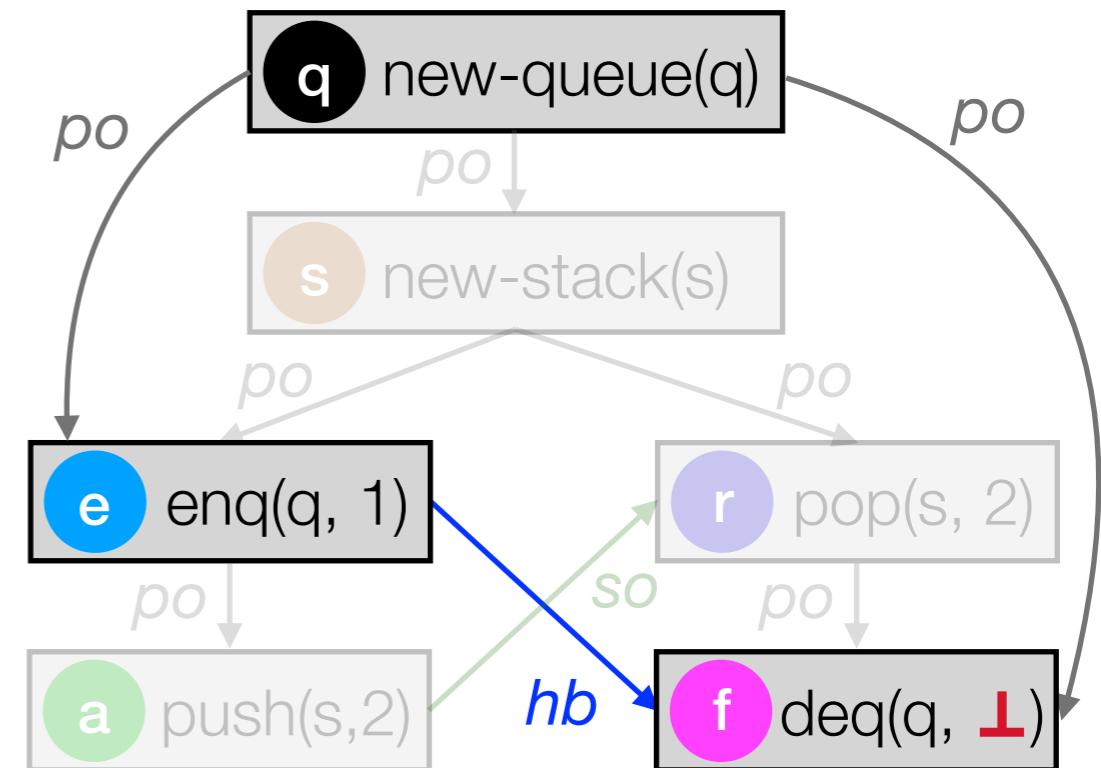
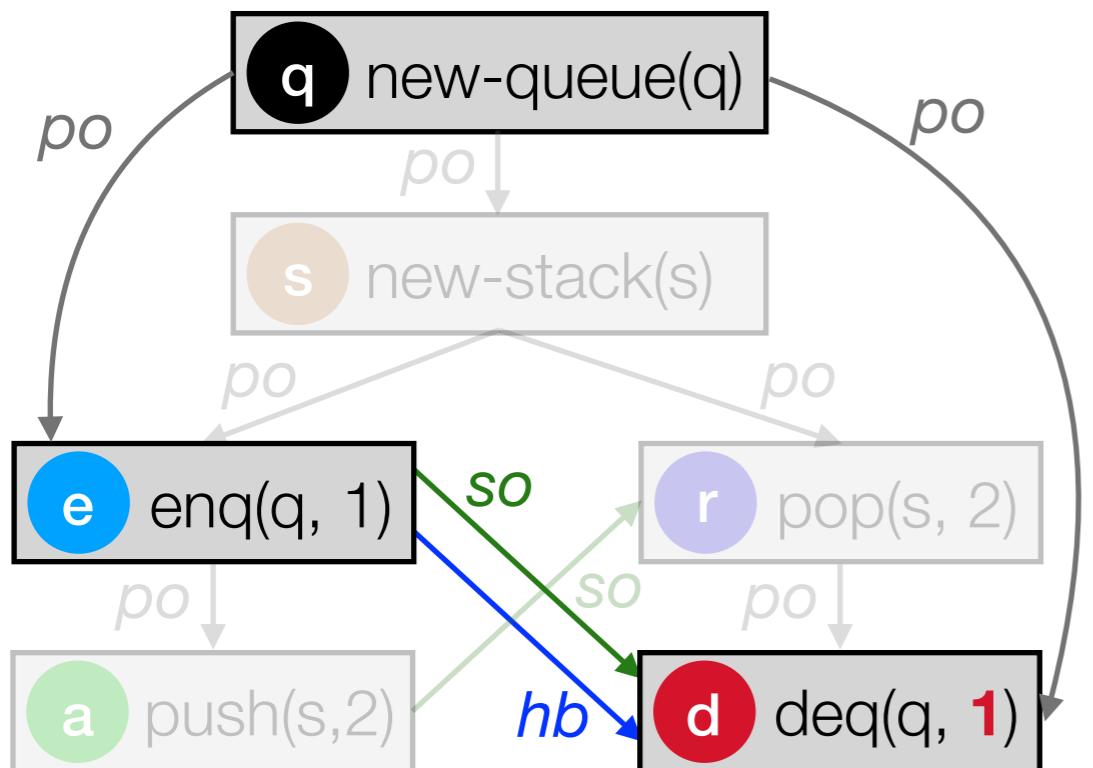


# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q,1); || a:=pop(s);
push(s,2)   || if(a==2)
               b:=deq(q) // should return 1
  
```

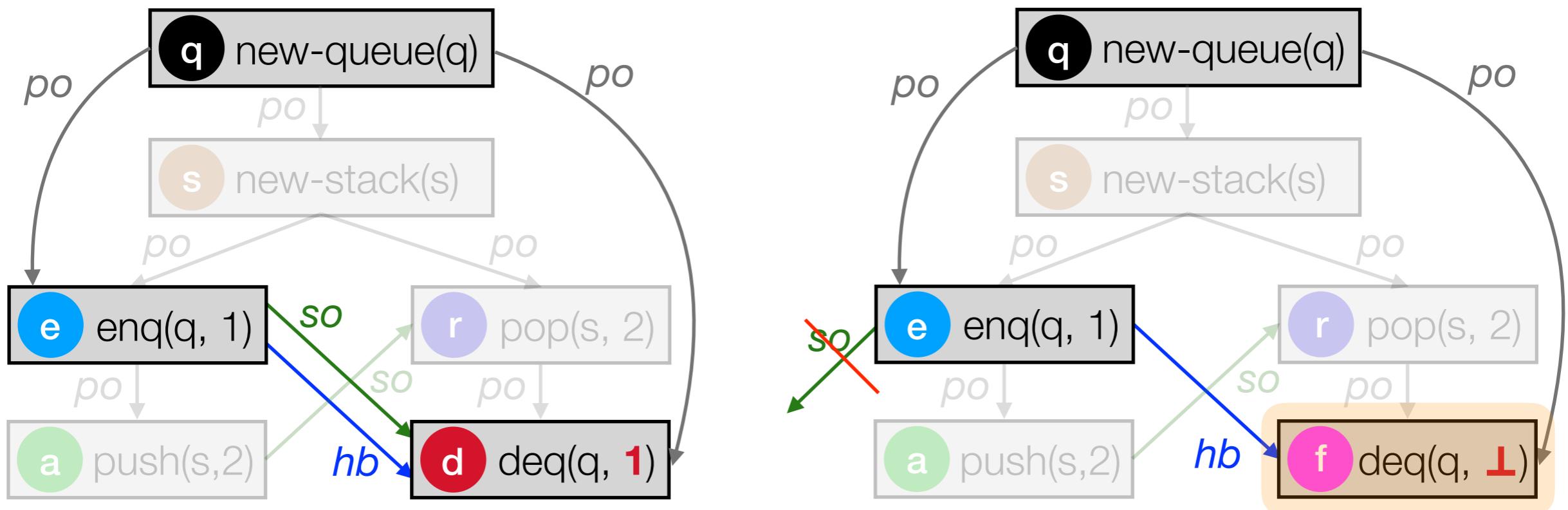


# Example Revisited

```

q:=new-queue();
s:=new-stack();

enq(q,1);    || a:=pop(s);
push(s,2)     || if(a==2)
                b:=deq(q) // should return 1
  
```



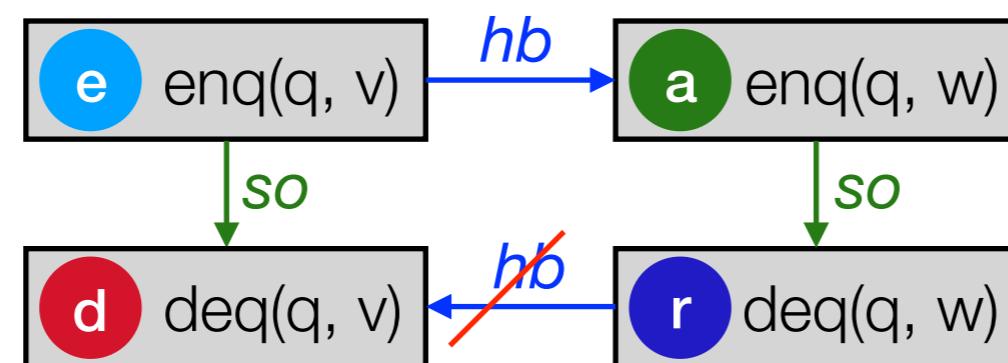
# Weak Queue Axioms

$G = \langle E, po, so, hb \rangle$  is consistent a **weak queue** execution iff:

1.  $E$  contains queue events
2.  $so$  is **1-to-1**;  $so$  relates **matching** enq/deq events
3. deq with ***hb*-earlier unmatched** enq cannot return  $\perp$



4. **weak** FIFO guarantee



# **Weak** vs. **Strong** Queue Axioms

- **Not** equivalent  
⇒ C11 **Herlihy-Wing** queue

*new-queue()*  $\triangleq$   
let  $q = \text{alloc}(+\infty)$  in  $q$

*enq*( $q, v$ )  $\triangleq$   
let  $i = \text{fetch-add}(q, 1, \text{rel})$  in  
*store*( $q + i + 1, v, \text{rel}$ );

*deq*( $q$ )  $\triangleq$   
loop  
let  $\text{range} = \text{load}(q, \text{acq})$  in  
for  $i = 1$  to  $\text{range}$  do  
let  $x = \text{atomic-xchg}(q + i, 0, \text{acq})$  in  
if  $x \neq 0$  then break<sub>2</sub>  $x$



satisfies **weak** axioms



does **not** satisfy **strong** axioms

# **Weak** vs. **Strong** Queue Axioms

- **Not** equivalent  
⇒ C11 **Herlihy-Wing** queue

$\text{new-queue}() \triangleq$   
 $\text{let } q = \text{alloc}(+\infty) \text{ in } q$

$\text{enq}(q, v) \triangleq$   
 $\text{let } i = \text{fetch-add}(q, 1, \text{rel}) \text{ in}$   
 $\text{store}(q + i + 1, v, \text{rel});$

$\text{deq}(q) \triangleq$   
loop  
  let  $\text{range} = \text{load}(q, \text{acq})$  in  
  for  $i = 1$  to  $\text{range}$  do  
    let  $x = \text{atomic-xchg}(q + i, 0, \text{acq})$  in  
    if  $x \neq 0$  then break<sub>2</sub>  $x$

acqrel

- ✓ satisfies **weak** axioms
- ✓ satisfies **strong** axioms

# **Weak** vs. **Strong** Queue Axioms

- **Not** equivalent  
⇒ C11 **Herlihy-Wing** queue

*new-queue( )*  $\triangleq$   
let  $q = \text{alloc}(+\infty)$  in  $q$

*enq( $q, v$ )*  $\triangleq$   
let  $i = \text{fetch-add}(q, 1, \text{rel})$  in  
*store*( $q + i + 1, v, \text{rel}$ );

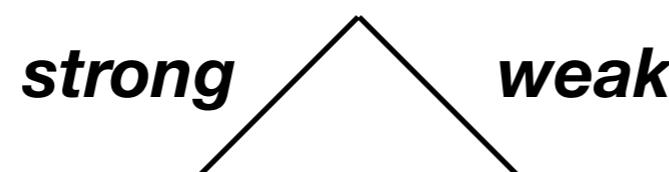
*deq( $q$ )*  $\triangleq$   
loop  
let  $\text{range} = \text{load}(q, \text{acq})$  in  
for  $i = 1$  to  $\text{range}$  do  
let  $x = \text{atomic-xchg}(q + i, 0, \text{acq})$  in  
if  $x \neq 0$  then break<sub>2</sub>  $x$

- Why weak axioms?  
⇒ **strong enough** for certain uses: **single-producer-single-consumer**

# Alternative Strong Queue Axioms

$G = \langle E, po, so, hb \rangle$  is a consistent **queue** execution iff:

1.  $E$  contains queue events
2.  $so$  is **1-to-1**;  $so$  relates **matching** enq/deq events

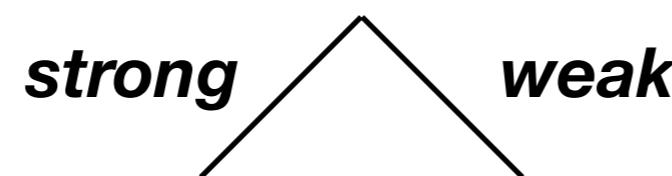


3.  $\exists \text{to. to totally}$  orders  $E$ ,  
 $hb \subseteq \text{to}$      $\text{to}$  is FIFO
3. deq with ***hb-earlier unmatched***  
enq cannot return  $\perp$
4. **weak** FIFO guarantee  
**(weak acyclicity axiom)**

# Alternative Strong Queue Axioms

$G = \langle E, po, so, hb \rangle$  is a consistent **queue** execution iff:

1.  $E$  contains queue events
2.  $so$  is **1-to-1**;  $so$  relates **matching** enq/deq events



3.  $\exists to. to$  **totally** orders  $E$ ,  
 $hb \subseteq to$      $to$  is FIFO

$\times$  too strong  $\rightarrow$  weak axioms  
 $\times$  difficult to find  $to$  witness

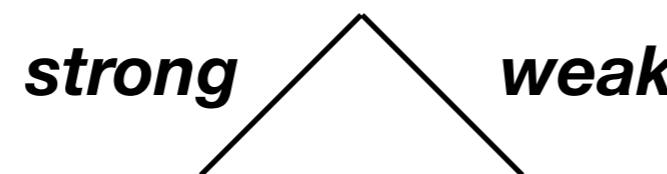
3. deq with ***hb-earlier unmatched***  
enq cannot return  $\perp$

4. **weak** FIFO guarantee  
**(weak acyclicity axiom)**

# Alternative Strong Queue Axioms

$G = \langle E, po, so, hb \rangle$  is a consistent **queue** execution iff:

1.  $E$  contains queue events
2.  $so$  is **1-to-1**;  $so$  relates **matching** enq/deq events



3.  $\exists to. to$  **totally** orders  $E$ ,  
 $hb \subseteq to$      $to$  is FIFO

✗ too strong ↗ weak axioms  
✗ difficult to find  $to$  witness



3. **strong** FIFO guarantee  
**(strong acyclicity axiom)**

⇒ see our paper!

3. deq with ***hb-earlier unmatched***  
enq cannot return  $\perp$

4. **weak** FIFO guarantee  
**(weak acyclicity axiom)**

# Recap

A declarative framework:

✓ **Agnostic** to memory model

- support both SC and WMC specs

✓ **General**

- port existing SC (linearisability) specs
- port existing WMC specs (e.g. C11, TSO)
- built from the ground up: assume no pre-existing libraries or specs

# Recap

A declarative framework:

## ✓ **Agnostic** to memory model

- support both SC and WMC specs

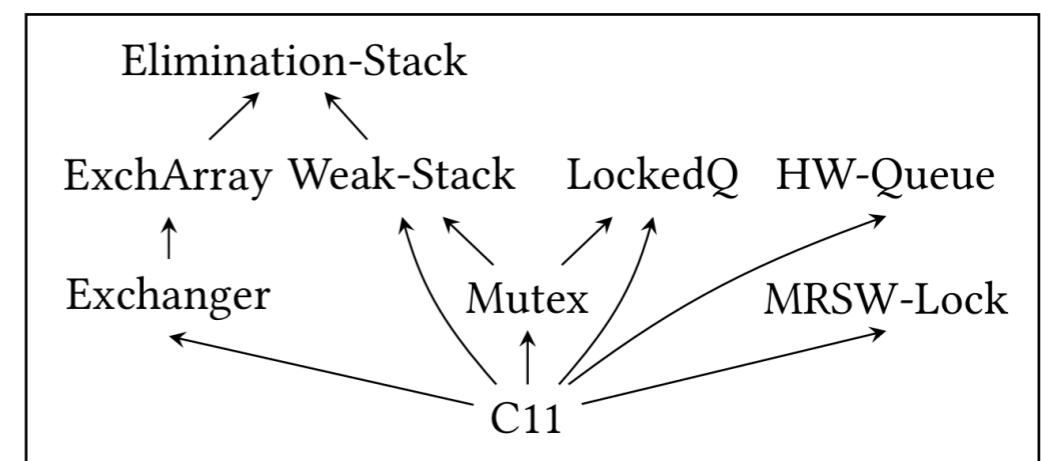
## ✓ **General**

- port existing SC (linearisability) specs
- port existing WMC specs (e.g. C11, TSO)
- built from the ground up: assume no pre-existing libraries or specs

## ? **Compositional**

- ? verify client programs
- ? verify library implementations

```
q:=new-queue();  
s:=new-stack();  
  
enq(q,1);    || a:=pop(s);  
push(s,2)    || if(a==2)  
                b:=deq(q)
```



# Part II.

## Concurrent Library *Verification* under WMC

# Program *Outcomes*

$\llbracket P \rrbracket = \{ G_P \mid \dots \}$

$outcomes(P) = \{ val(G_P) \mid G_P \in \llbracket P \rrbracket \}$

values of *local* variables

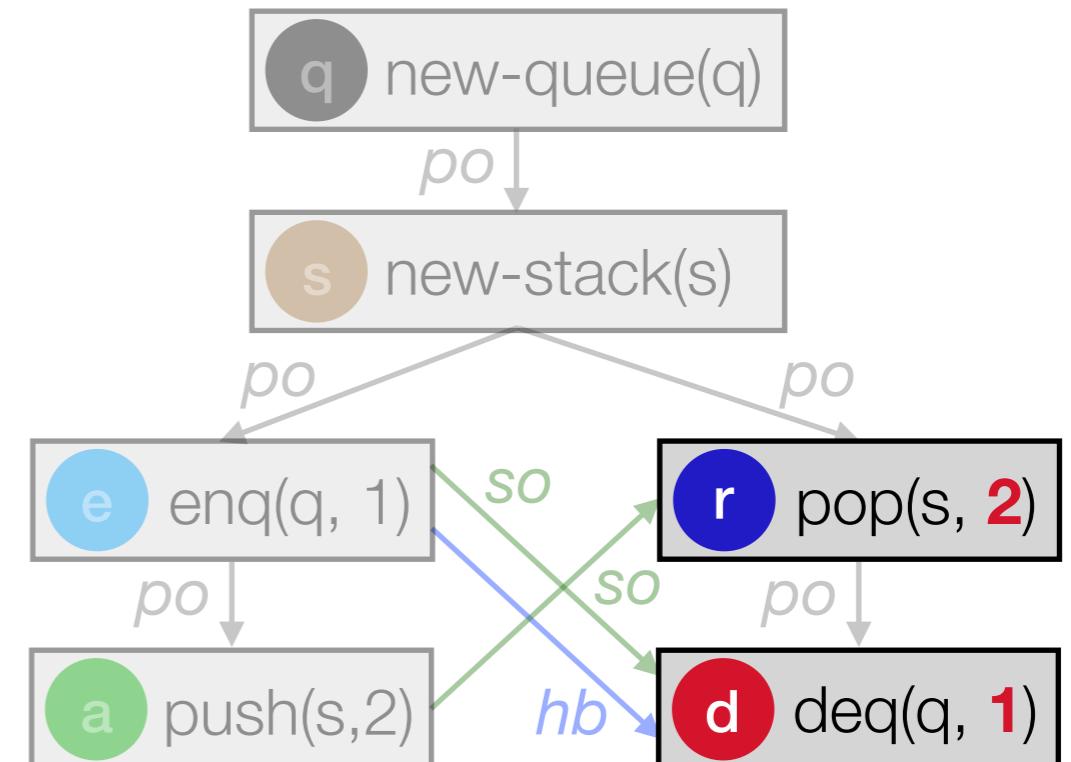
# Program *Outcomes*

$$[\![P]\!] = \{ G_P \mid \dots \}$$

$$\text{outcomes}(P) = \{ \text{val}(G_P) \mid G_P \in [\![P]\!] \}$$

values of *local* variables

```
q := new-queue();  
s := new-stack();  
enq(q, 1); || a := pop(s);  
push(s, 2)    if (a==2)  
                b := deq(q)
```



$$\mathbf{a = 2} \quad \mathbf{b = 1}$$

# **Client** Verification

$\llbracket P \rrbracket = \{ G_P \mid \dots \}$

$outcomes(P) = \{ \text{val}(G_P) \mid G_P \in \llbracket P \rrbracket \}$

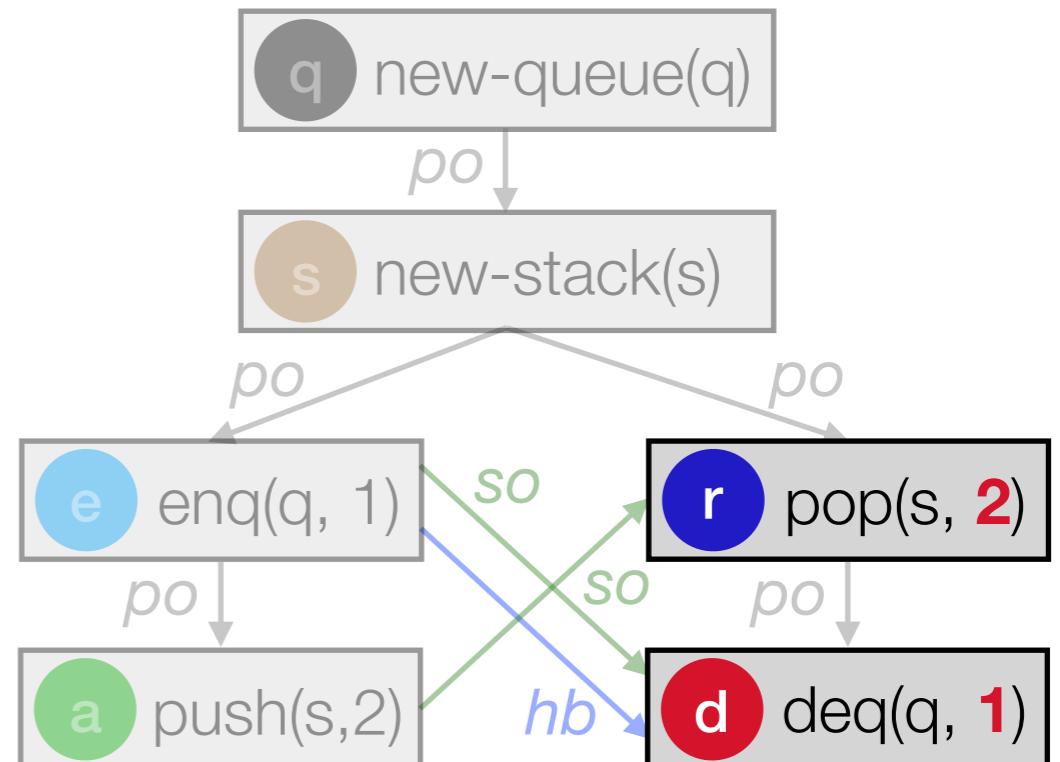
values of **local** variables

**assert** (*true*)

```

q:=new-queue () ;
s:=new-stack () ;
enq(q, 1) ; || a:=pop (s) ;
push(s, 2)    || if (a==2)
                b:=deq(q)
  
```

**assert** ( $\neg (a = 2 \wedge b = \perp)$ )



**a = 2    b = 1**

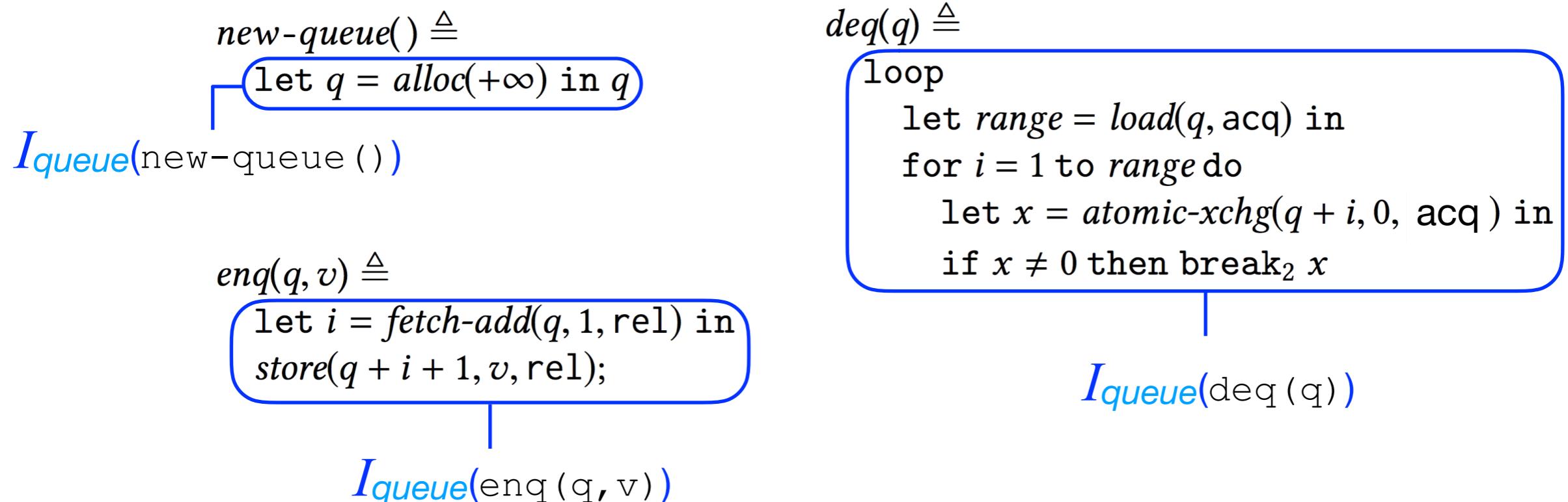
# *Implementation* Verification?

```
new-queue()  $\triangleq$  deq(q)  $\triangleq$ 
  let  $q = \text{alloc}(+\infty)$  in  $q$    loop
                                         let  $range = \text{load}(q, \text{acq})$  in
                                         for  $i = 1$  to  $range$  do
                                         let  $x = \text{atomic-xchg}(q + i, 0, \text{acq})$  in
                                         if  $x \neq 0$  then break2  $x$ 
enq(q, v)  $\triangleq$ 
  let  $i = \text{fetch-add}(q, 1, \text{rel})$  in
  store( $q + i + 1, v, \text{rel}$ );
```

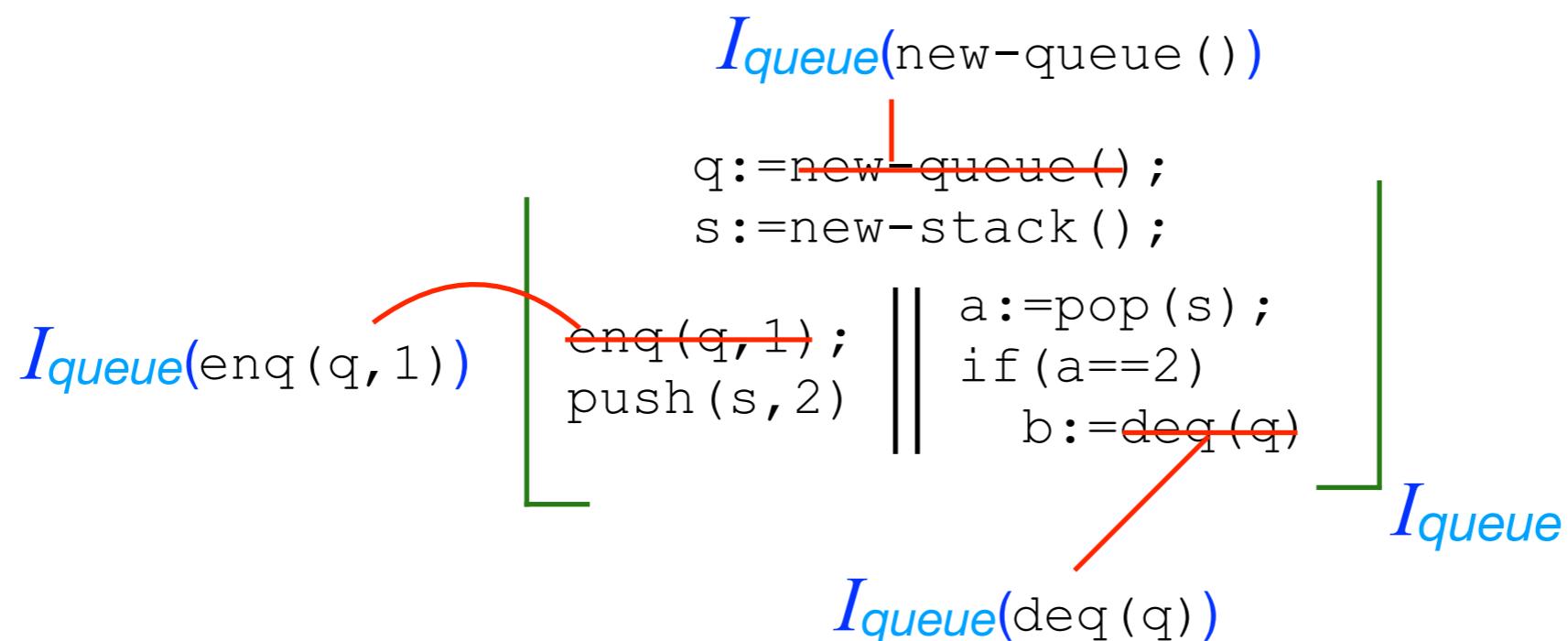
# Library *Implementation*

$I_{queue} : Method_{queue} \rightarrow \text{Prog}$

## C11 Herlihy-Wing Implementation (HWQ)



# Translation

$$\lfloor \cdot \rfloor_{I_{queue}} : Prog \rightarrow Prog$$


# Implementation **Soundness**

$I_{queue}$  **sound**  $\iff$  for all  $P$  :

$$outcomes(\lfloor P \rfloor_{I_{queue}}) \subseteq outcomes(P)$$

# Implementation **Soundness**

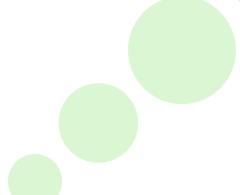
$I_{queue}$  **sound**  $\iff$  for all  $P$  :

$$outcomes(\llbracket P \rrbracket_{I_{queue}}) \subseteq outcomes(P)$$



$$\{ val(G_i) \mid G_i \in \llbracket \llbracket P \rrbracket_{I_{queue}} \rrbracket \} \subseteq \{ val(G_s) \mid G_s \in \llbracket P \rrbracket \}$$

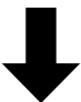
$$outcomes(P) = \{ val(G_P) \mid G_P \in \llbracket P \rrbracket \}$$



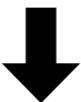
# Implementation **Soundness**

$I_{queue}$  **sound**  $\iff$  for all  $P$  :

$$outcomes(\llbracket P \rrbracket_{I_{queue}}) \subseteq outcomes(P)$$



$$\{ val(G_i) \mid G_i \in \llbracket \llbracket P \rrbracket_{I_{queue}} \rrbracket \} \subseteq \{ val(G_s) \mid G_s \in \llbracket P \rrbracket \}$$

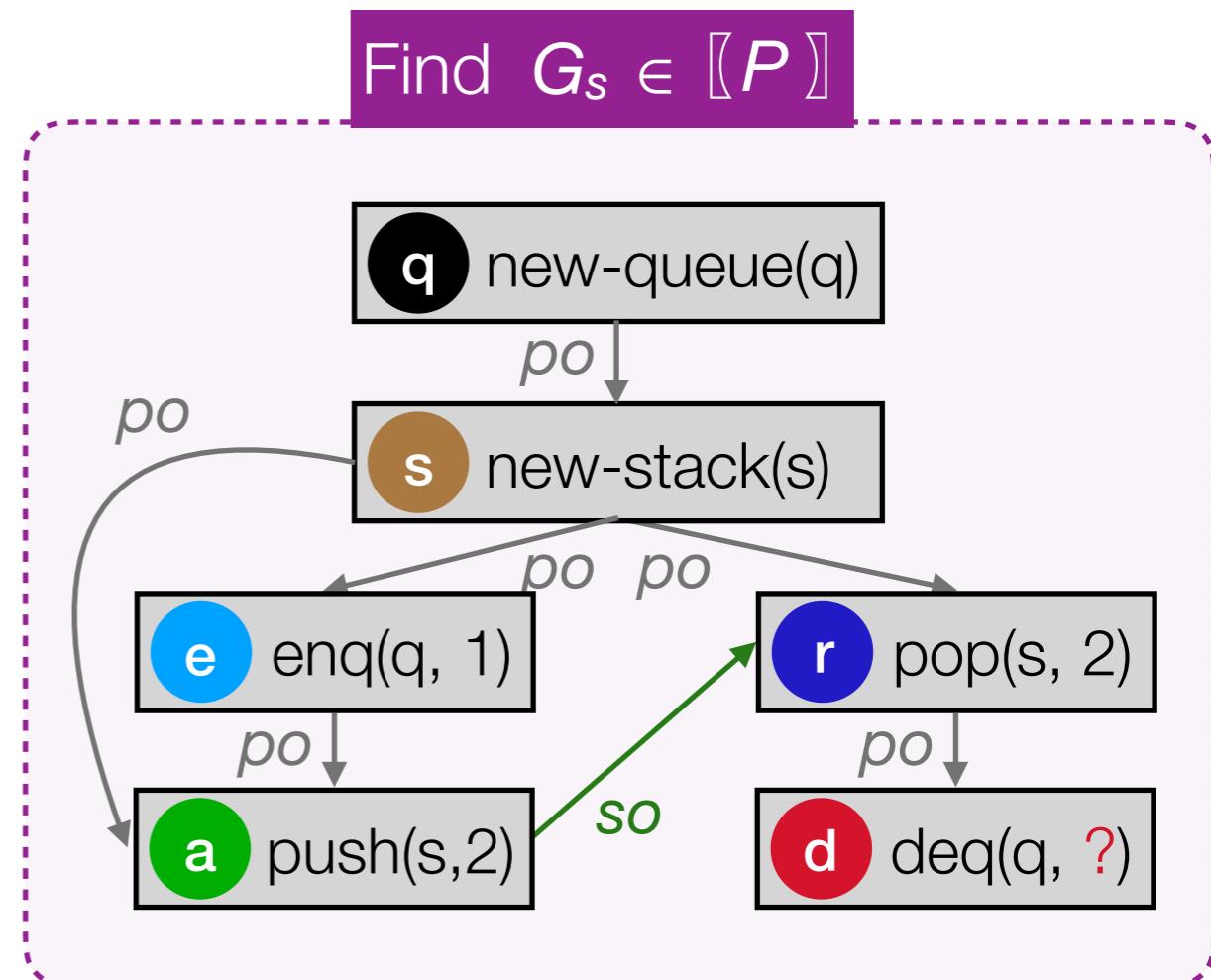
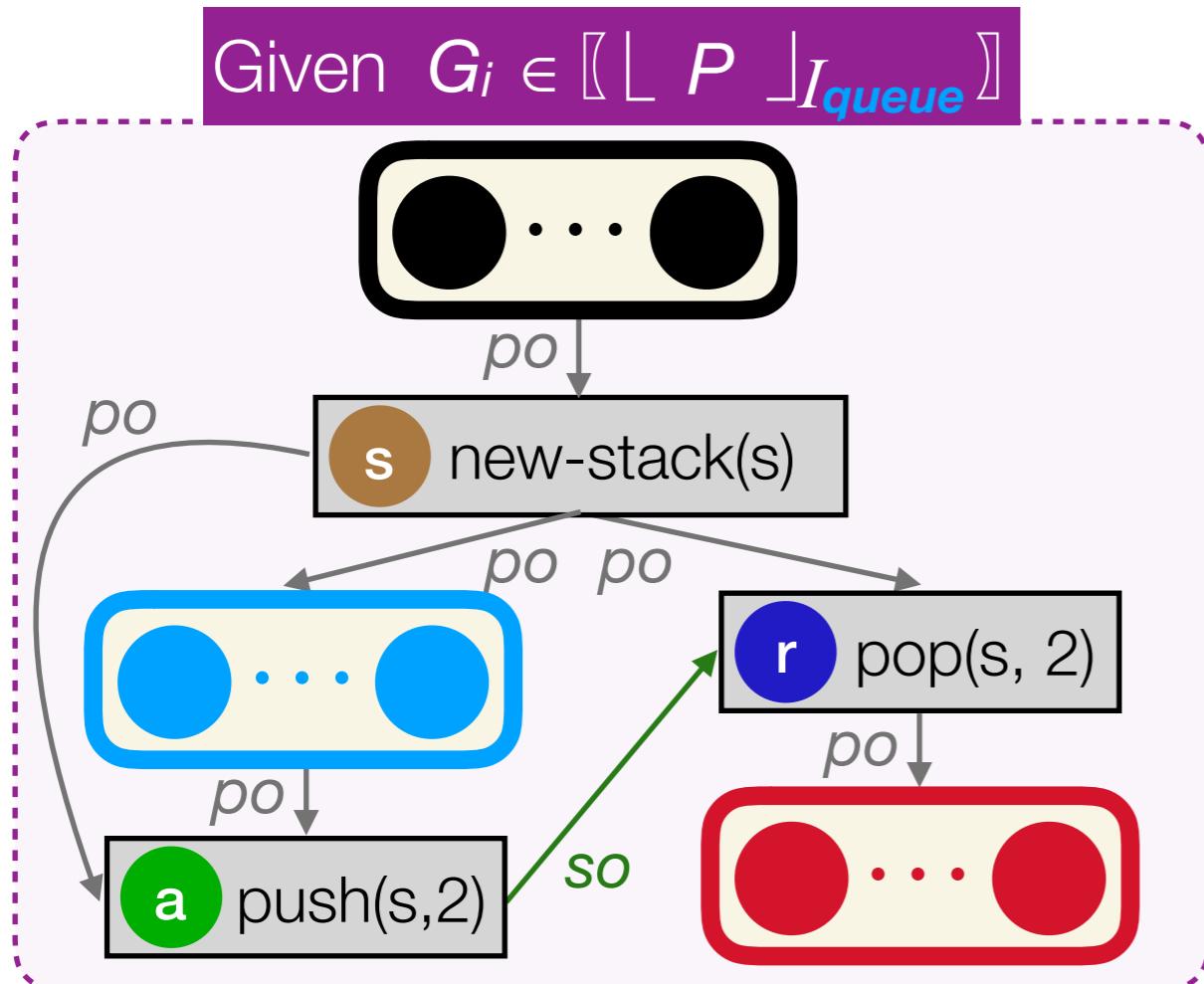
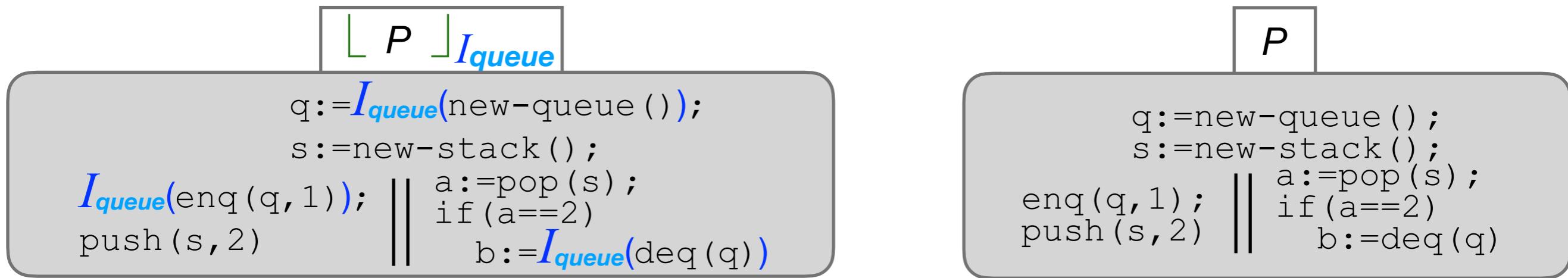


$$\forall G_i \in \llbracket \llbracket P \rrbracket_{I_{queue}} \rrbracket . \exists G_s \in \llbracket P \rrbracket . val(G_i) = val(G_s)$$

$$outcomes(P) = \{ val(G_P) \mid G_P \in \llbracket P \rrbracket \}$$

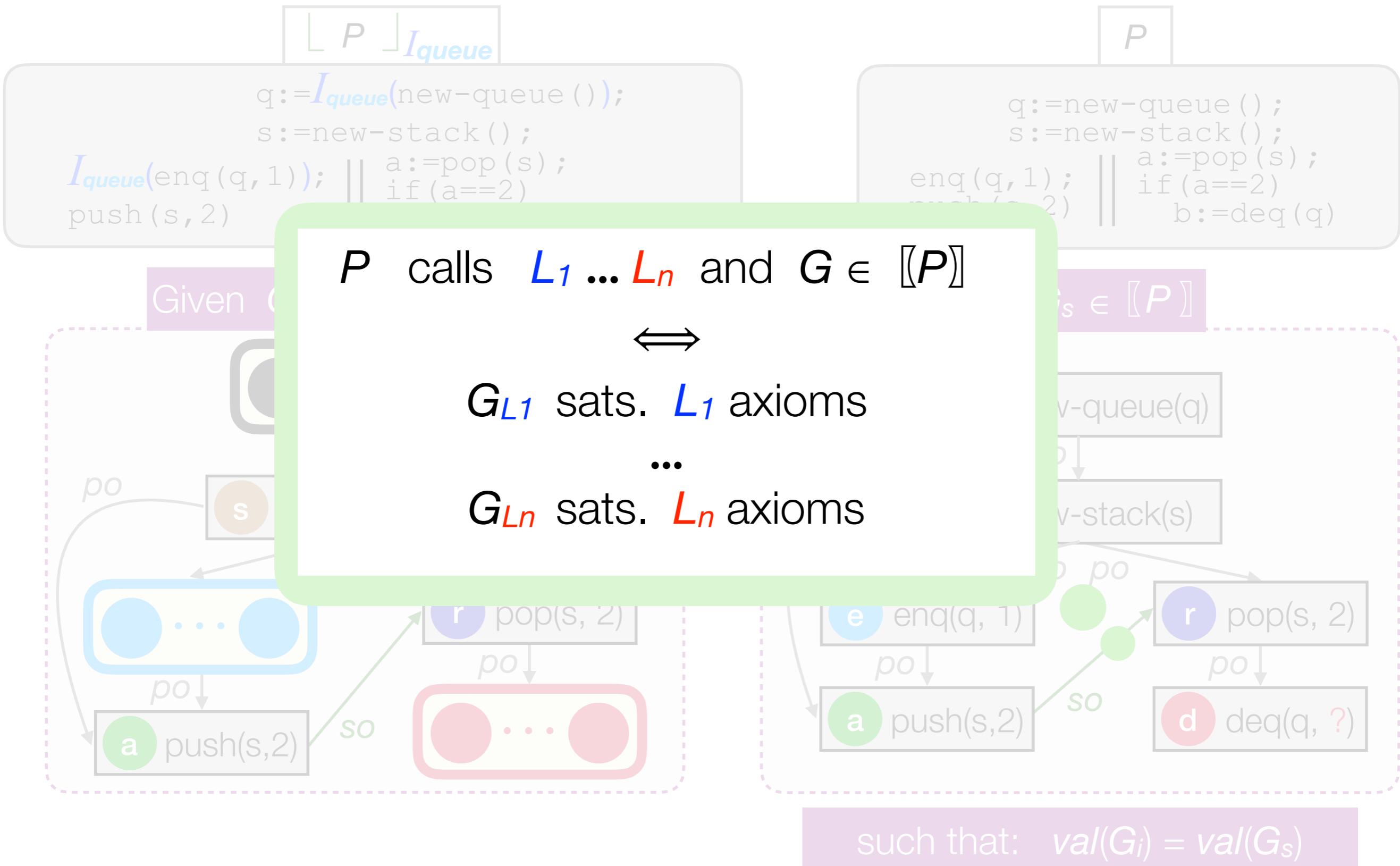


# Example: HWQ Soundness



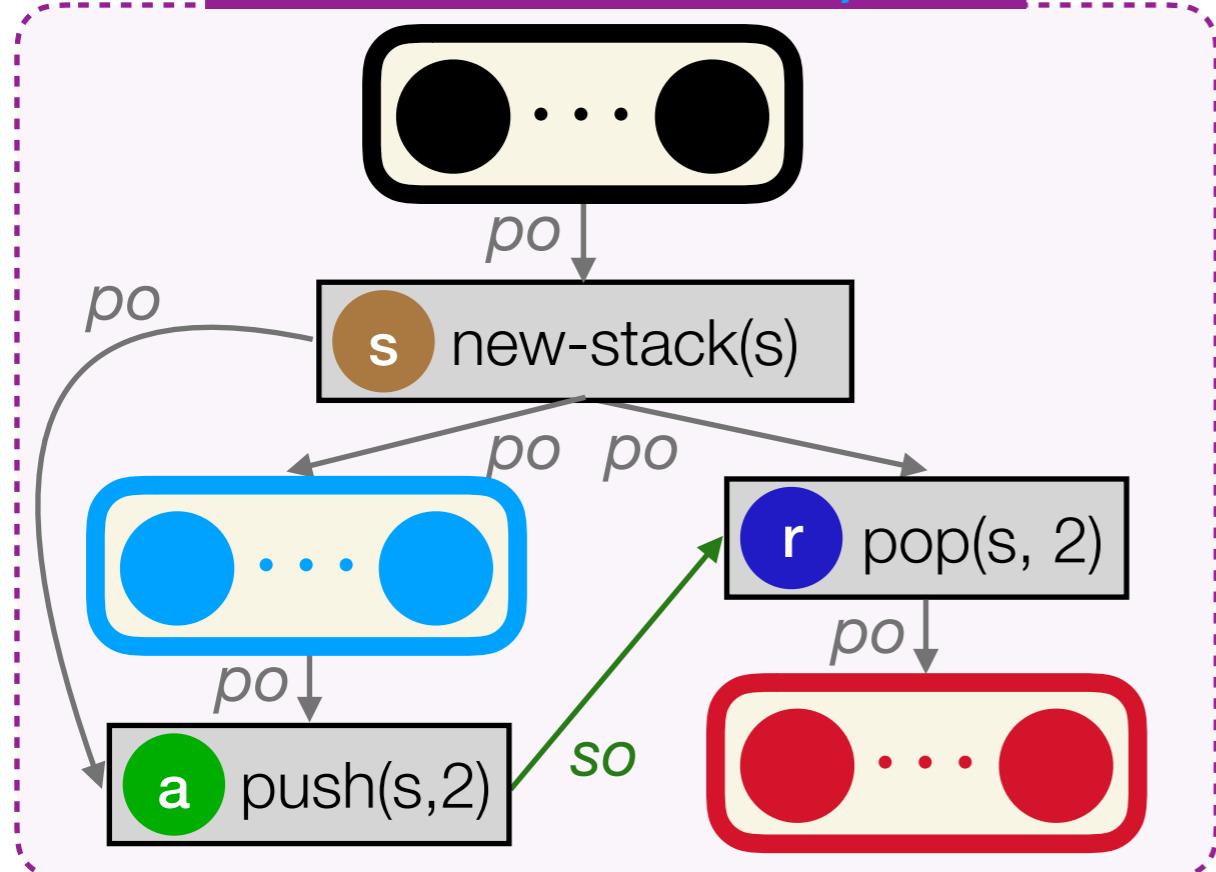
such that:  $val(G_i) = val(G_s)$

# Example: HWQ Soundness

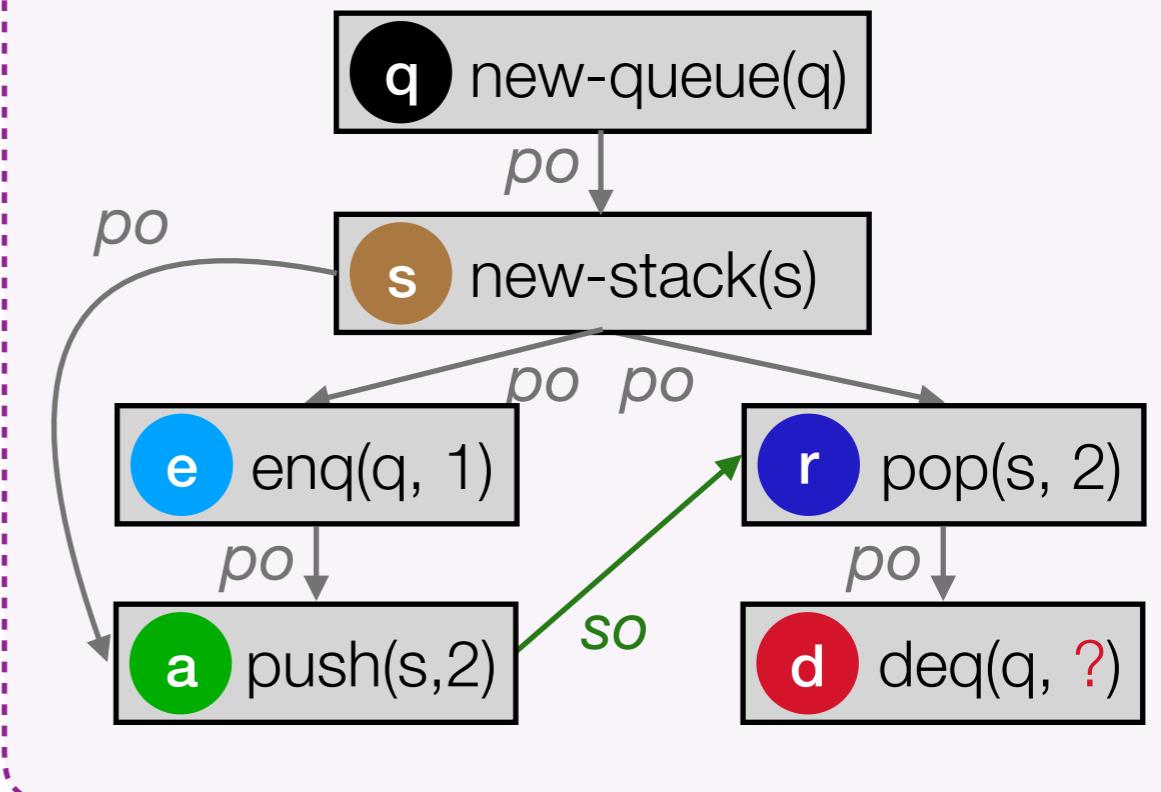


# Example: HWQ Soundness

Given  $G_i \in \llbracket \mathcal{L} P \rfloor_{I_{\text{queue}}^{\text{queue}}} \rrbracket$



Find  $G_s \in \llbracket P \rrbracket$



We know:

$(G_i)_{C11}$  sats.  $C11$  axioms

$(G_i)_{\text{stack}}$  sats.  $\text{stack}$  axioms

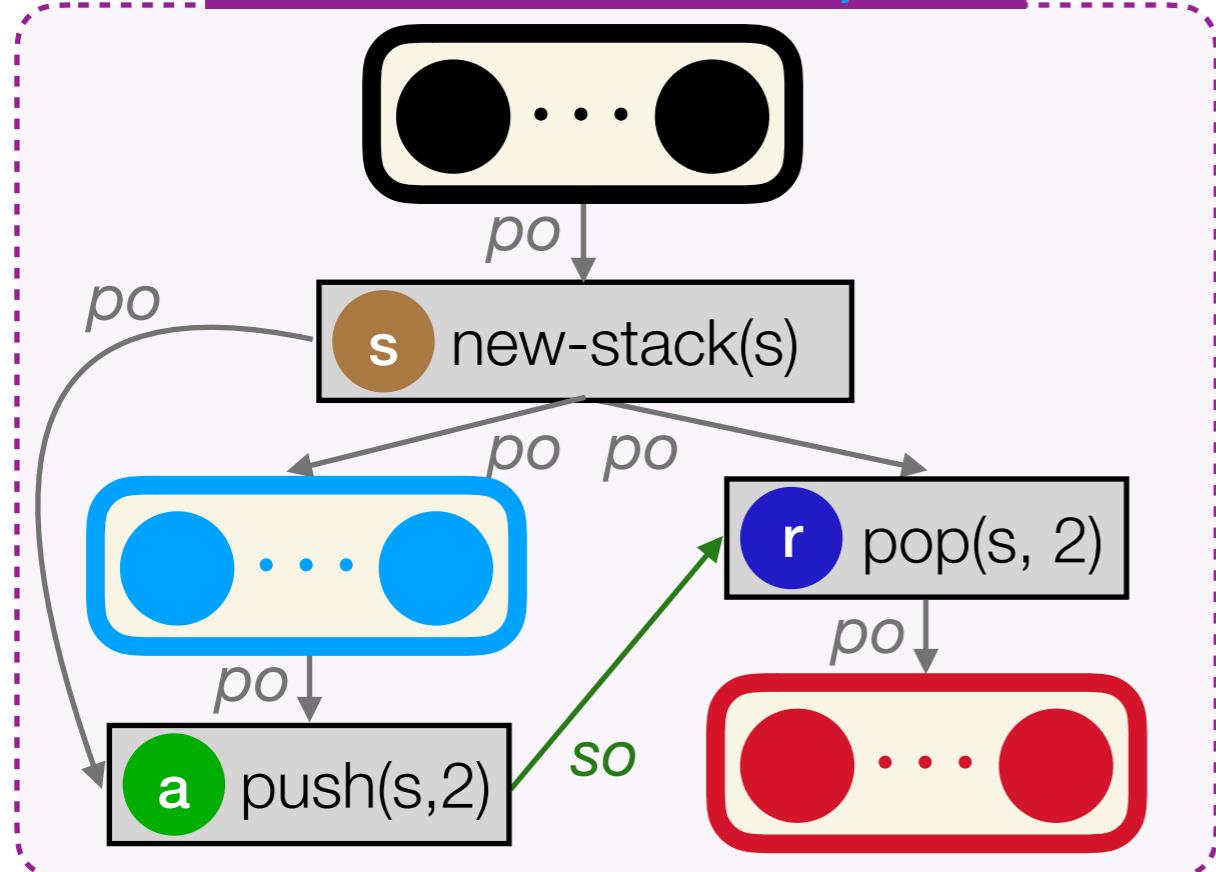
Show:

$(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms

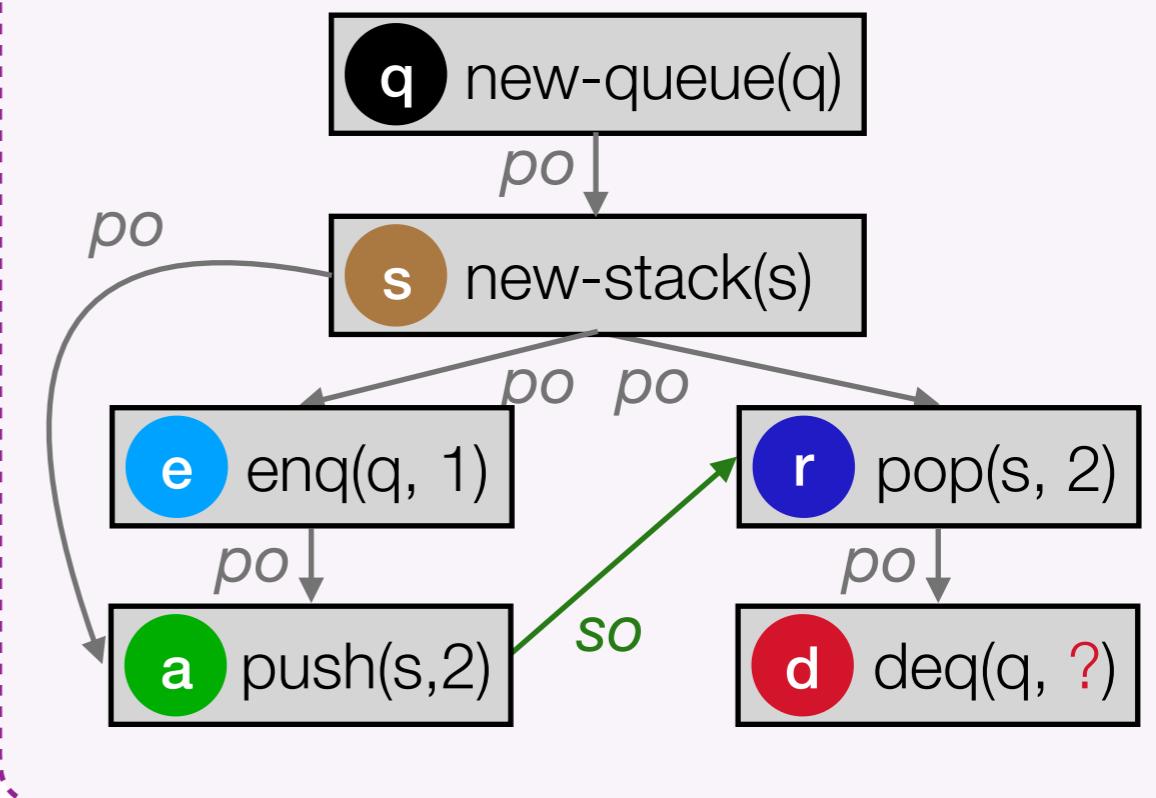
$(G_s)_{\text{stack}}$  sats.  $\text{stack}$  axioms

# Example: HWQ Soundness

Given  $G_i \in \llbracket \mathcal{L} P \rfloor_{I_{\text{queue}}^{\text{queue}}} \rrbracket$



Find  $G_s \in \llbracket P \rrbracket$



We know:

$(G_i)_{C11}$  sats.  $C11$  axioms

$(G_i)_{\text{stack}}$  sats.  $\text{stack}$  axioms

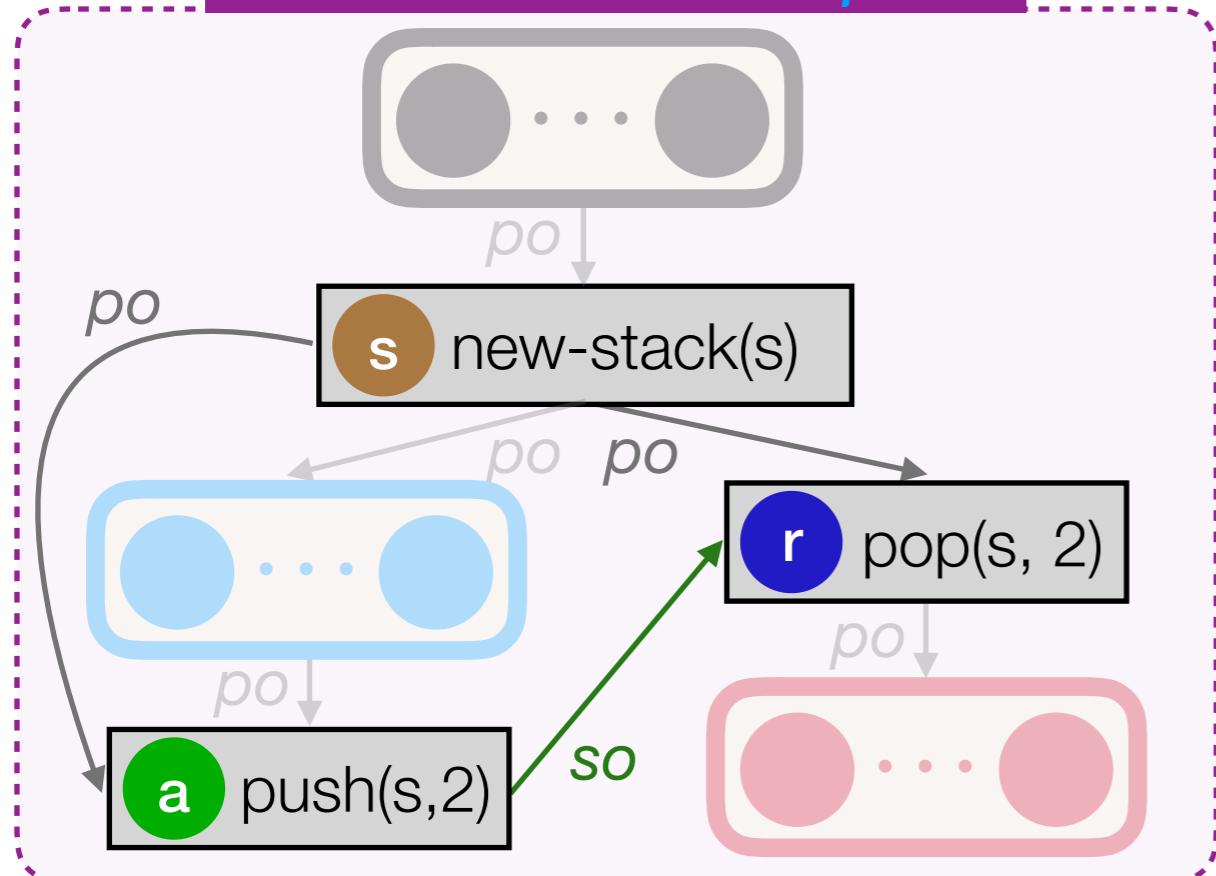
Show: **main** proof obligation

$(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms

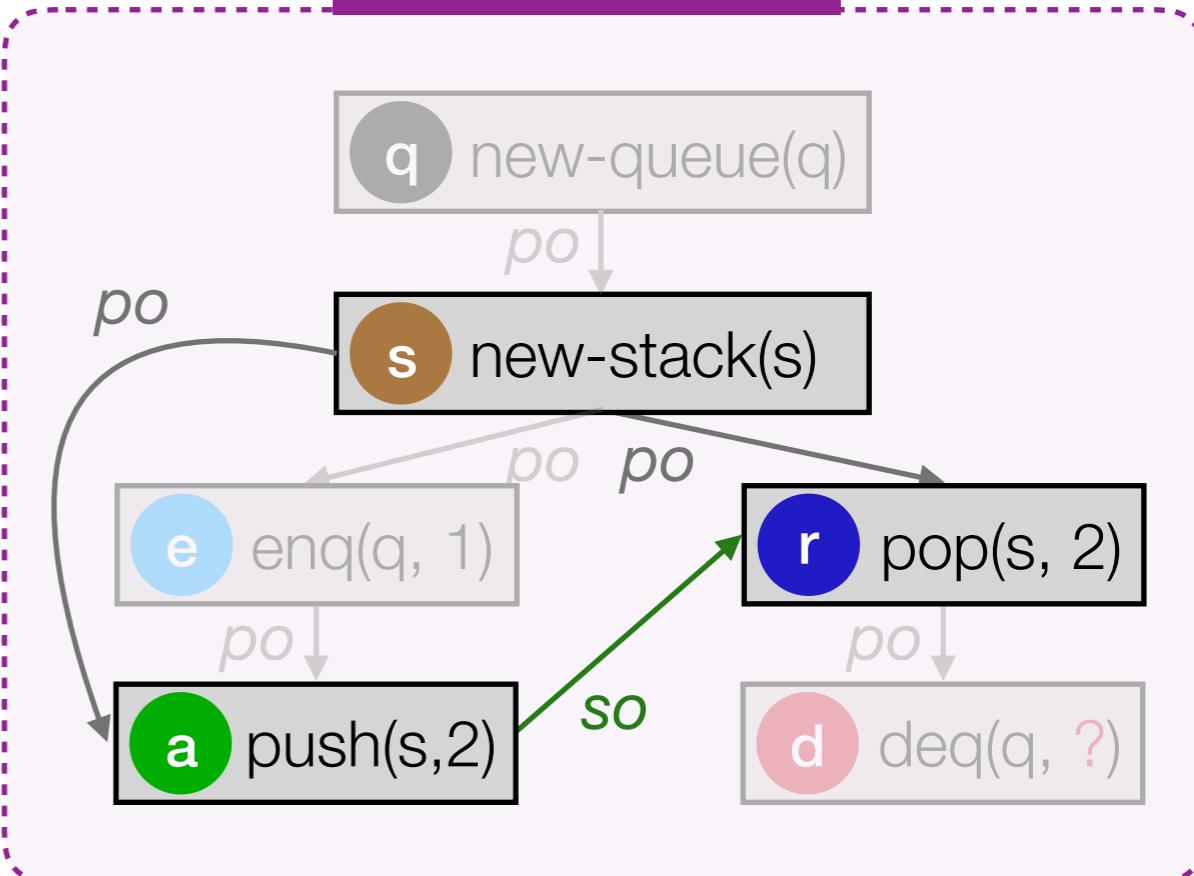
$(G_s)_{\text{stack}}$  sats.  $\text{stack}$  axioms

# Example: HWQ Soundness

Given  $G_i \in \llbracket L \mid P \rfloor_{I_{\text{queue}}^{\text{queue}}} \rrbracket$



Find  $G_s \in \llbracket P \rrbracket$



We know:

$(G_i)_{C11}$  sats.  $C11$  axioms

$(G_i)_{\text{stack}}$  sats.  $\text{stack}$  axioms

Show: **main** proof obligation

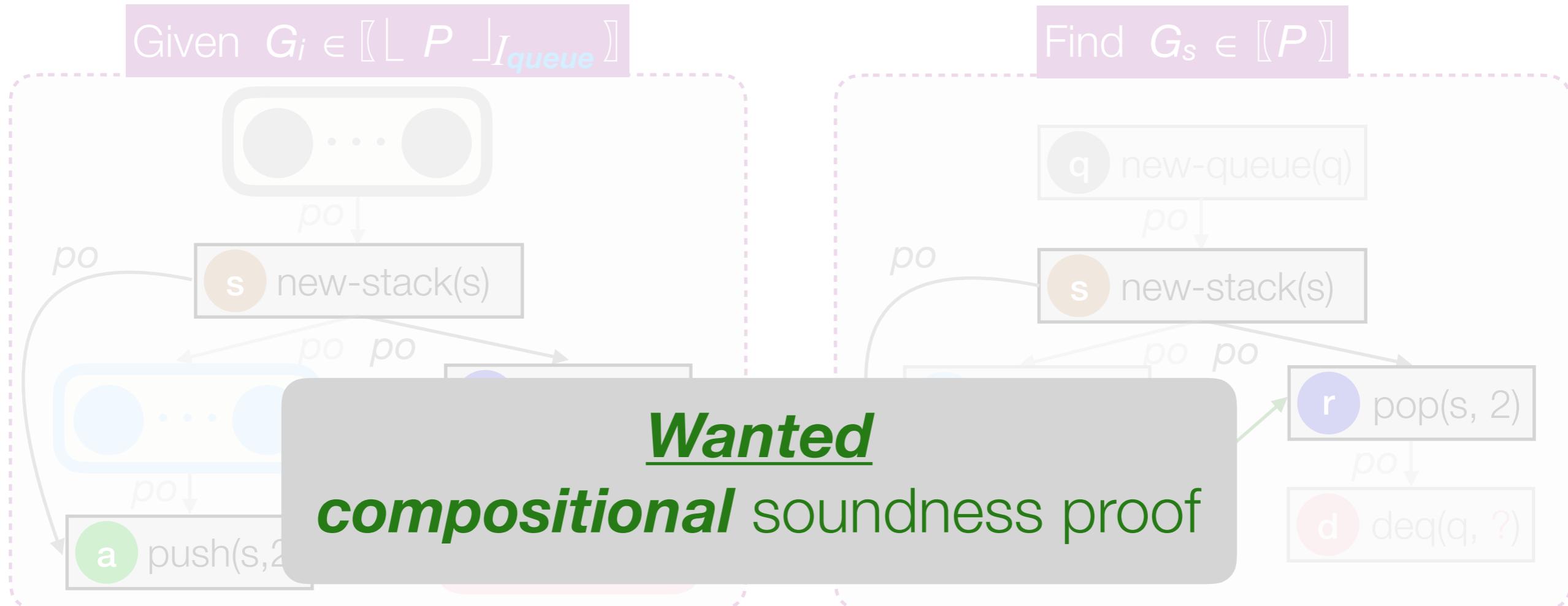
$(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms

$(G_s)_{\text{stack}}$  sats.  $\text{stack}$  axioms

**almost redundant :**

$(G_i)_{\text{stack}} =?=? (G_s)_{\text{stack}}$

# Example: HWQ Soundness



We know:

$(G_i)_{C11}$  sats.  $C11$  axioms

$(G_i)_{stack}$  sats.  $stack$  axioms

Show:  $\vdash$  **main** proof obligation

$(G_s)_{queue}$  sats.  $queue$  axioms

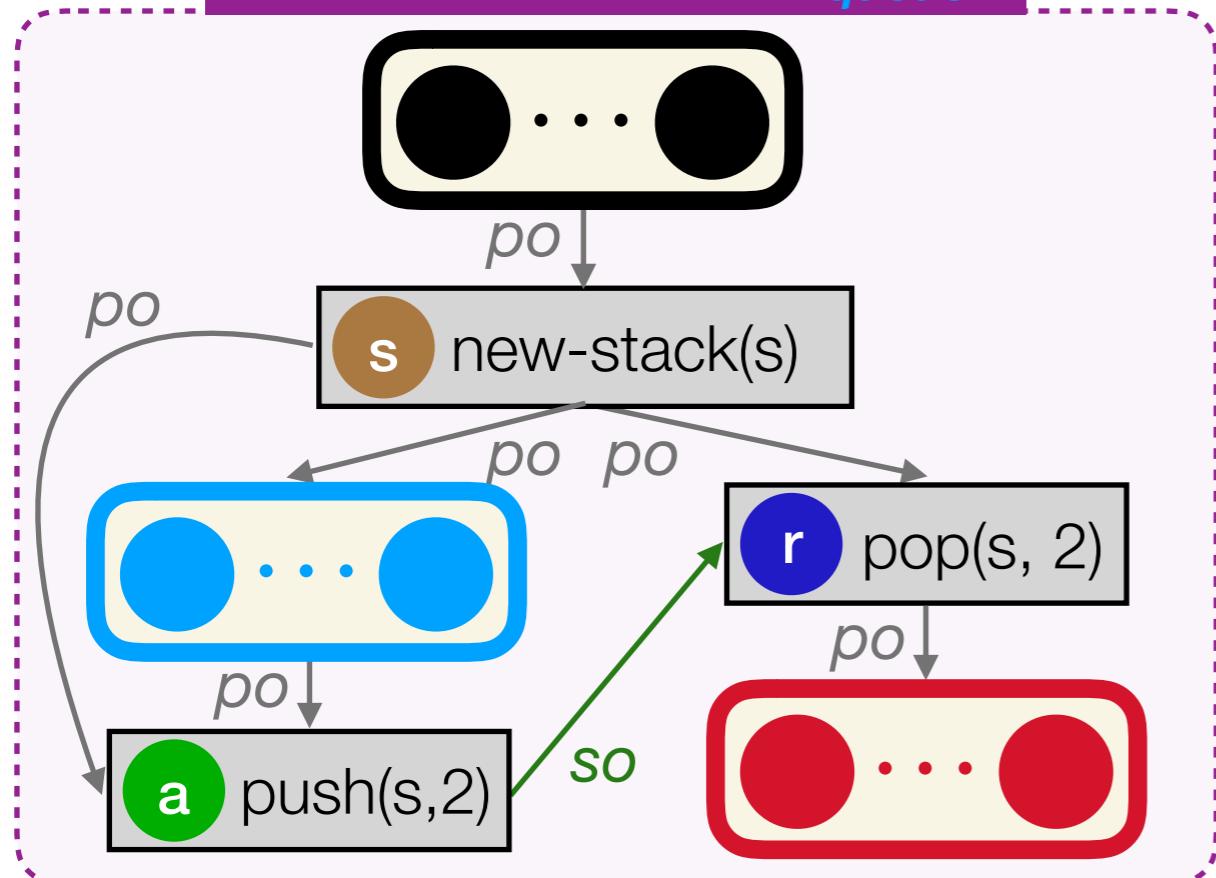
$(G_s)_{stack}$  sats.  $stack$  axioms

**almost redundant :**

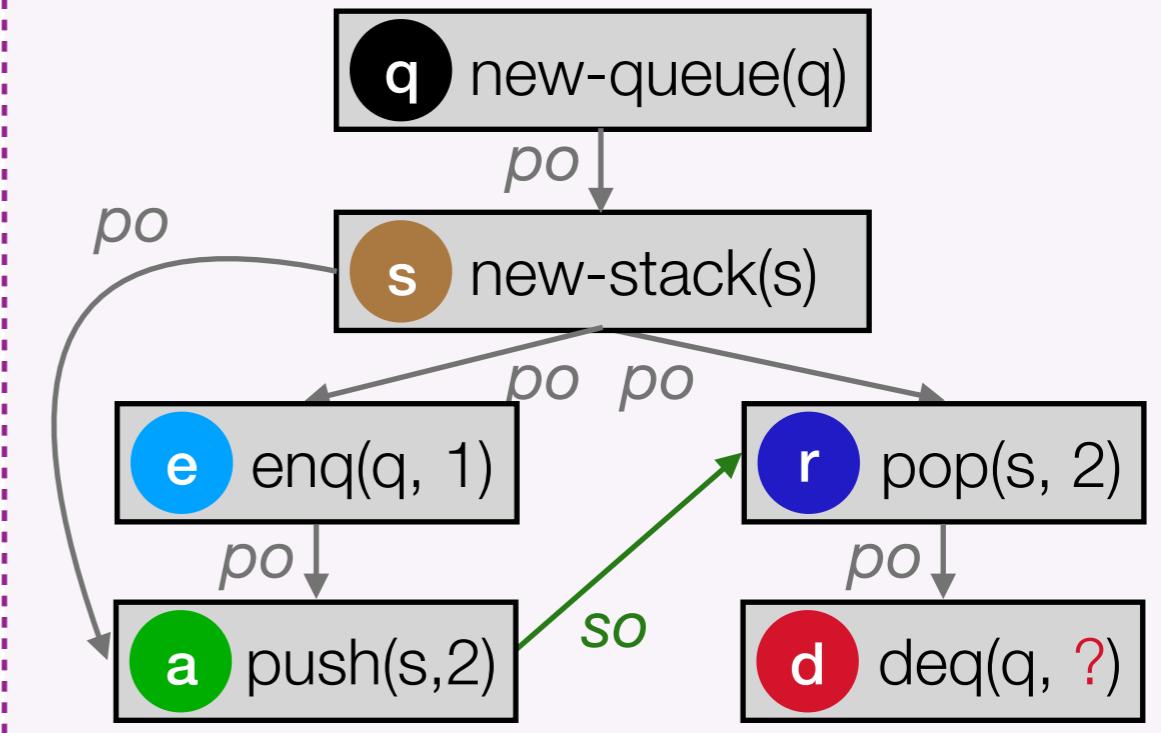
$(G_i)_{stack} =?=? (G_s)_{stack}$

# Example: HWQ Soundness

Given  $G_i \in \llbracket L \; P \; \rfloor_{I_{queue}}$



Find  $G_s \in \llbracket P \rrbracket$

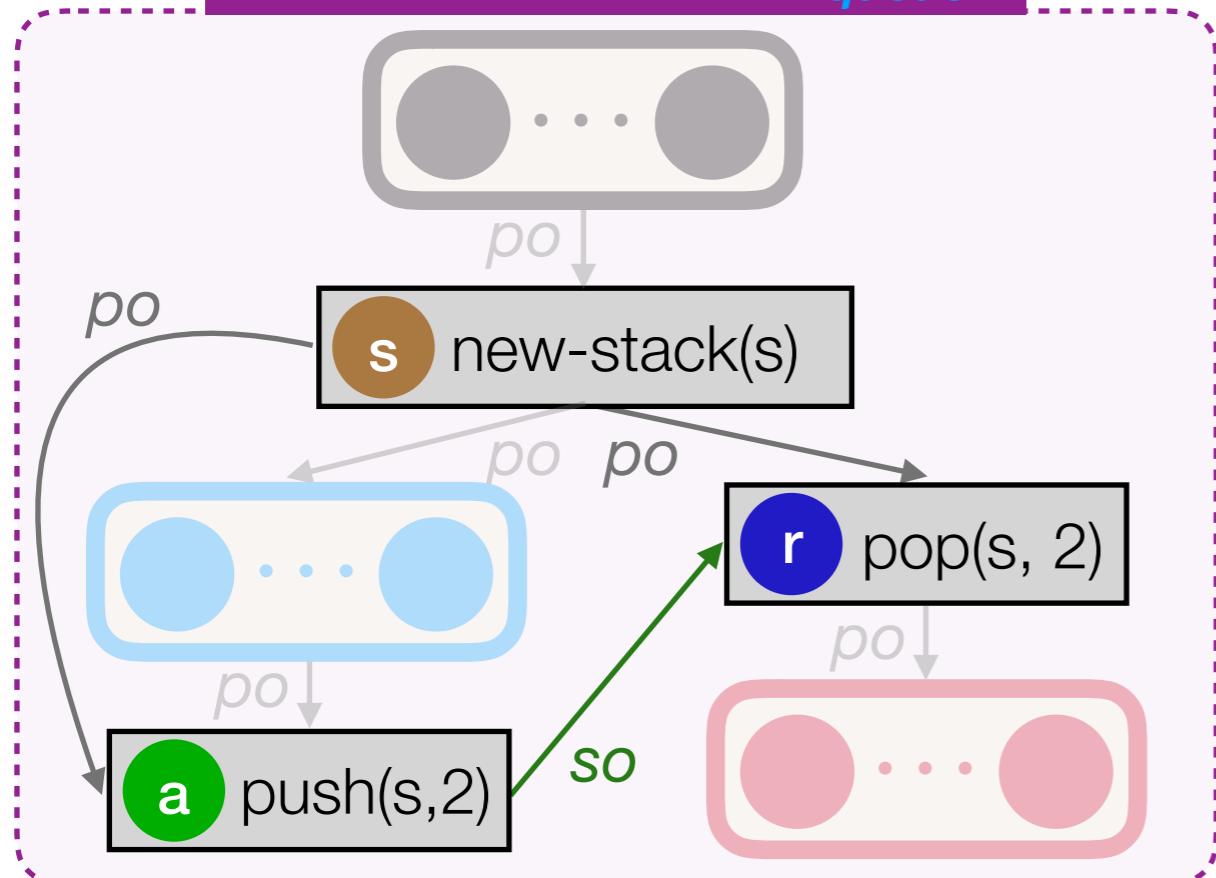


$(G_i)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_i)_{stack}>$

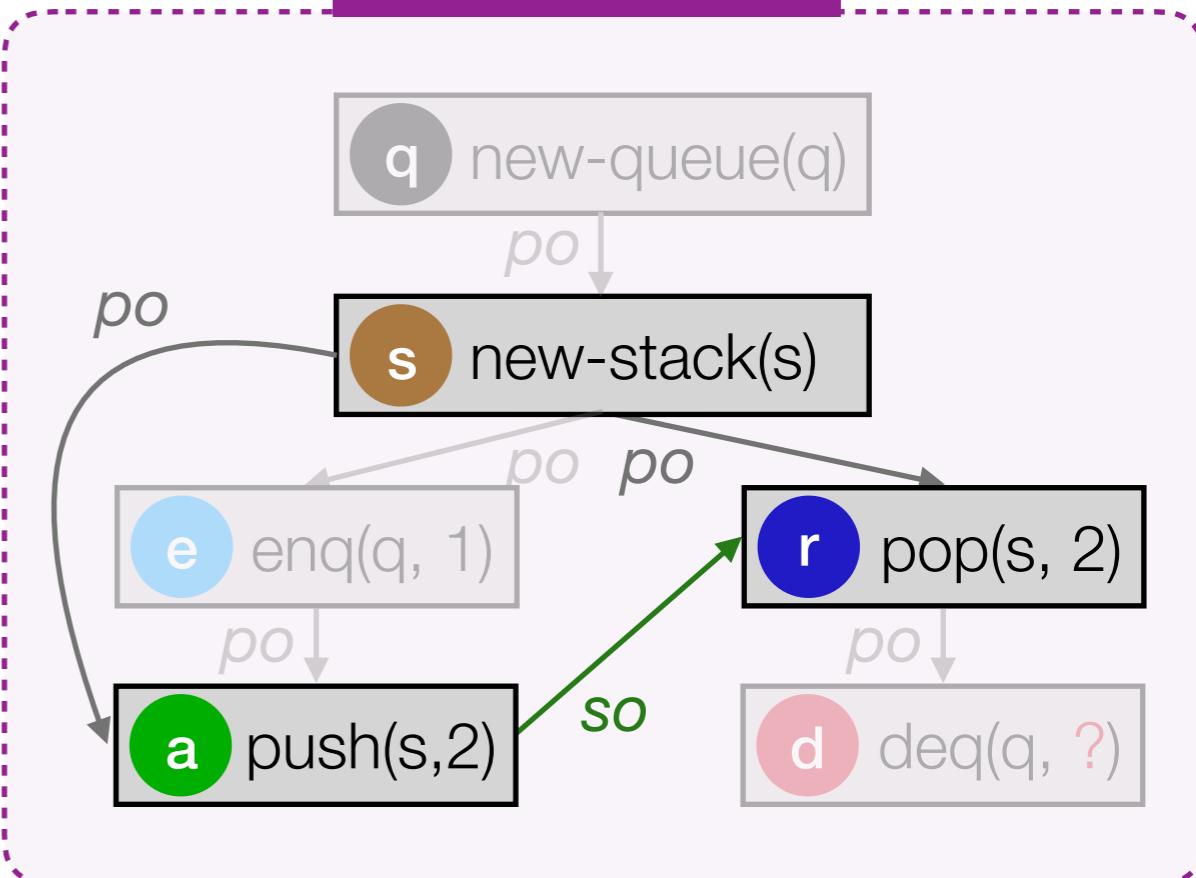
$(G_s)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_s)_{stack}>$

# Example: HWQ Soundness

Given  $G_i \in \llbracket L \mid P \rfloor_{I_{queue}}$



Find  $G_s \in \llbracket P \rrbracket$

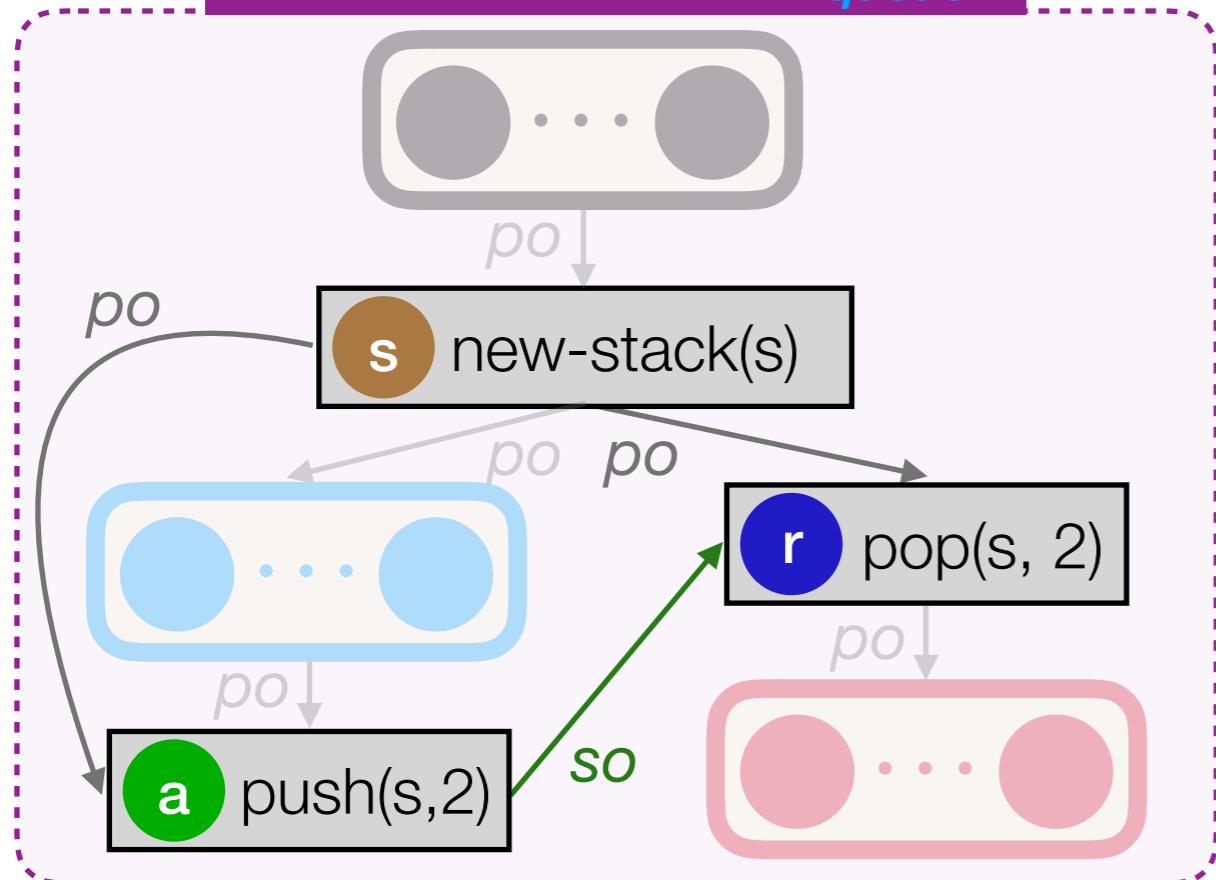


$(G_i)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_i)_{stack}>$

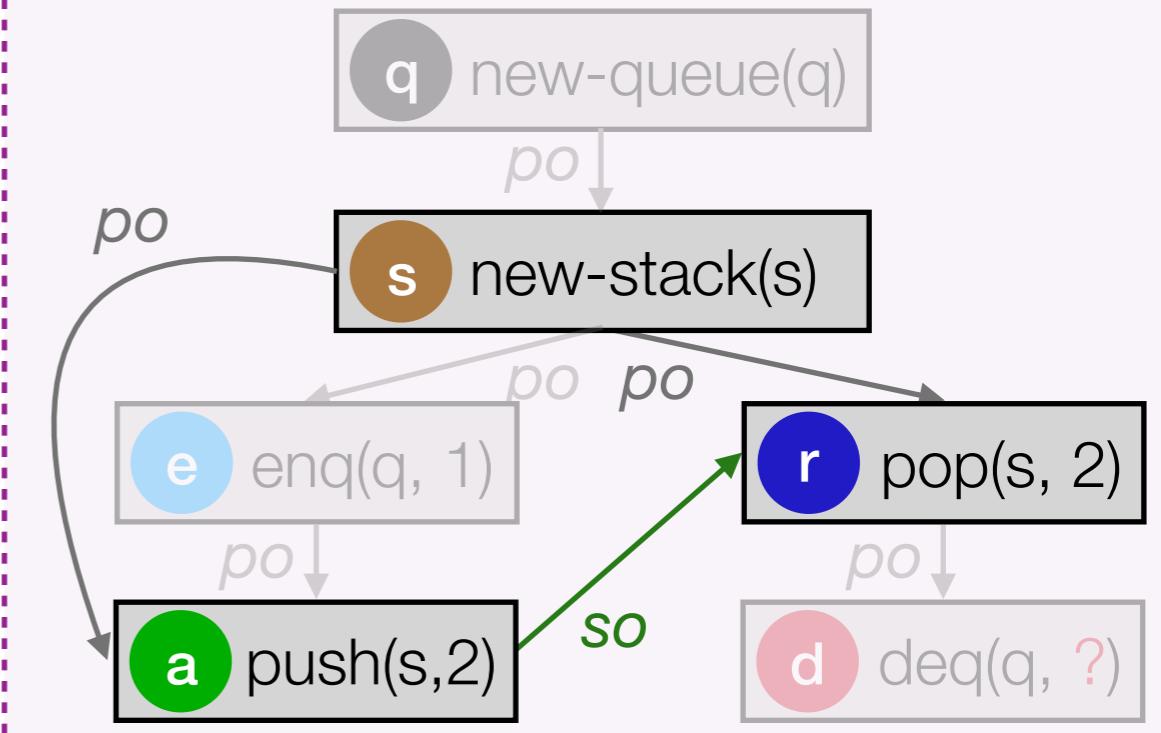
$(G_s)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_s)_{stack}>$

# Example: HWQ Soundness

Given  $G_i \in \llbracket L \mid P \rfloor_{I_{\text{queue}}^{\text{queue}}} \rrbracket$



Find  $G_s \in \llbracket P \rrbracket$



$(G_i)_{\text{stack}} = <E_{\text{stack}}, po_{\text{stack}}, so_{\text{stack}}, (hb_i)_{\text{stack}}>$

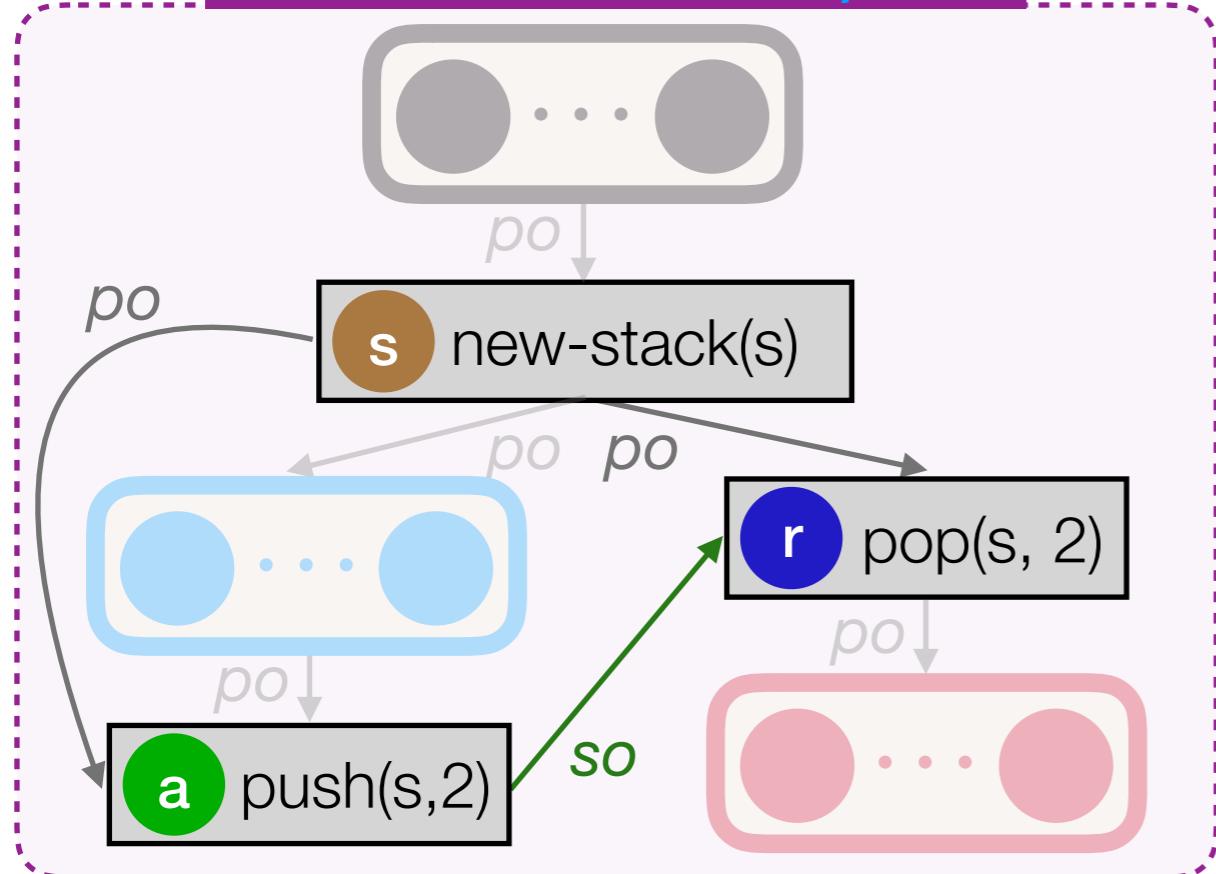
$(G_s)_{\text{stack}} = <E_{\text{stack}}, po_{\text{stack}}, so_{\text{stack}}, (hb_s)_{\text{stack}}>$

**show:**  $hb_i = hb_s$

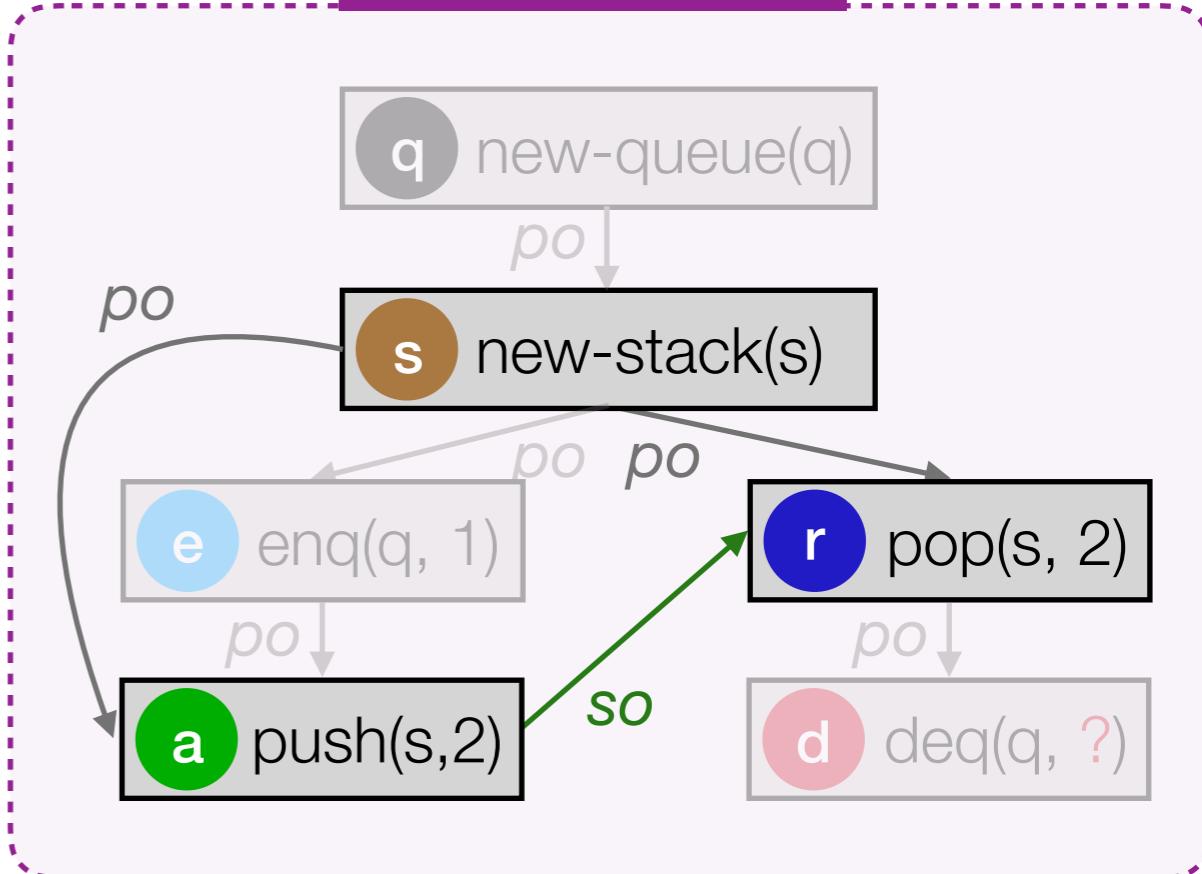
$$hb_s = (hb)_{\text{stack}} = ((po \cup so)^+ )_{\text{stack}}$$

# Example: HWQ Soundness

Given  $G_i \in \llbracket L \mid P \rfloor_{I_{queue}}$



Find  $G_s \in \llbracket P \rrbracket$



$(G_i)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_i)_{stack}>$

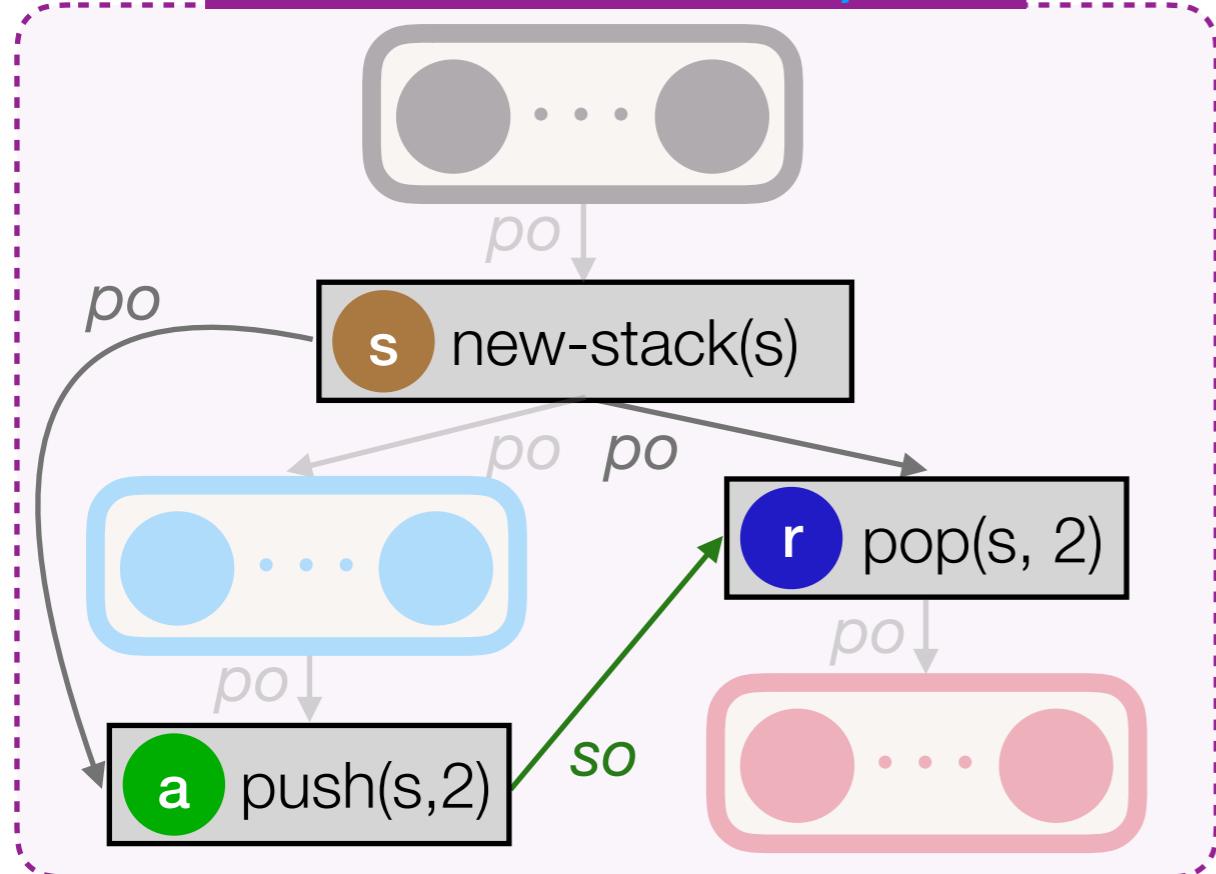
$(G_s)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_s)_{stack}>$

**show:**  $hb_i = hb_s$

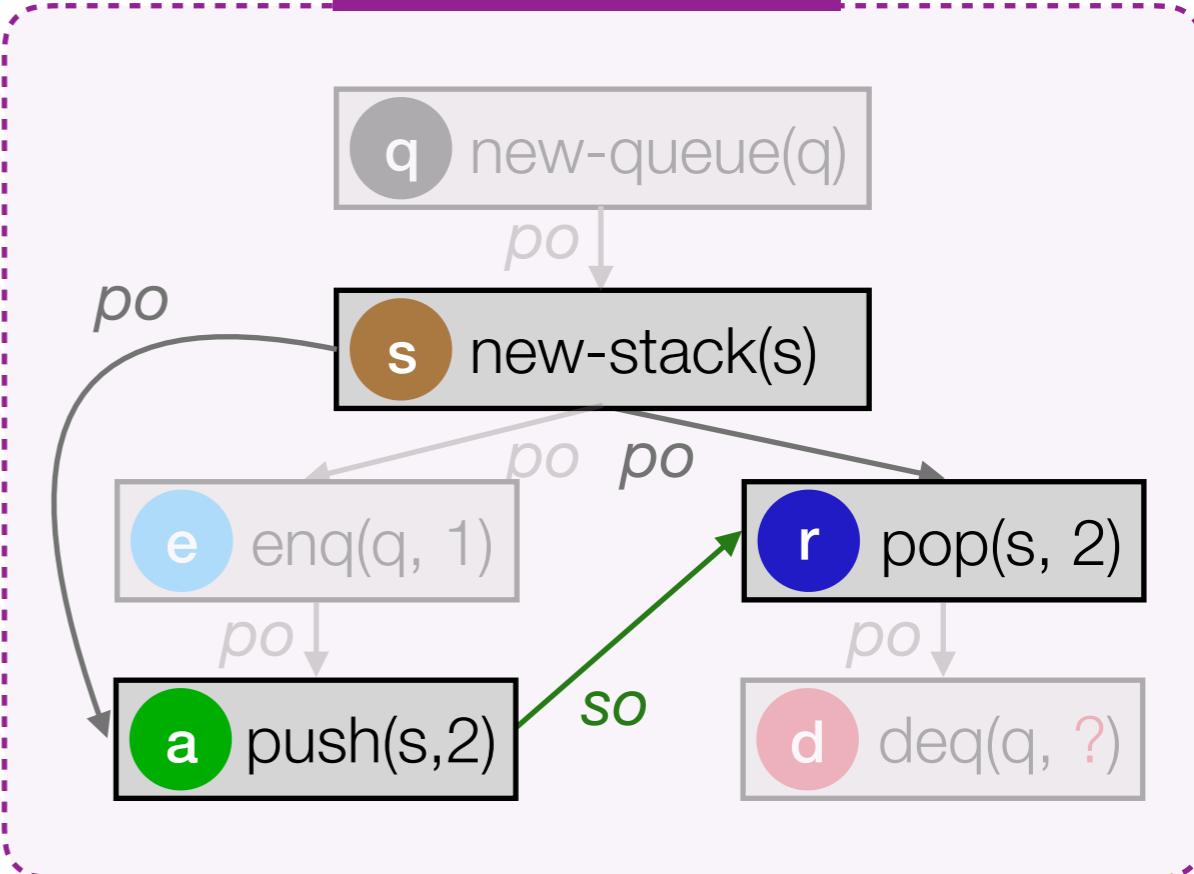
$$hb_s = (hb)_{stack} = ((po \cup so_{queue} \cup so_{stack})^+ )_{stack}$$

# Example: HWQ Soundness

Given  $G_i \in \llbracket L \mid P \rfloor_{I_{queue}}$



Find  $G_s \in \llbracket P \rrbracket$



$(G_i)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_i)_{stack}>$

$(G_s)_{stack} = <E_{stack}, po_{stack}, so_{stack}, (hb_s)_{stack}>$

**show:**  $hb_i = hb_s$

**sufficient:**  $G_s . so_{queue} \subseteq G_i . hb_i$

$$hb_s = (hb)_{stack} = ((po \cup so_{queue} \cup so_{stack})^+ )_{stack}$$

# **Local** Soundness

$I_{queue}$  sound  $\iff$  for all  $P$  :

$\forall G_i \in [\llbracket P \rfloor_{I_{queue}}]. \exists G_s \in [\![P]\!]. val(G_i) = val(G_s)$

# **Local** Soundness

$I_{queue}$  sound  $\iff$  for all  $P$  :

$$\forall G_i \in \llbracket \lfloor P \rfloor_{I_{queue}} \rrbracket. \exists G_s \in \llbracket P \rrbracket. val(G_i) = val(G_s)$$

↓ (when  $P$  calls  $queue, L_1, \dots, L_n$ )

$I_{queue}$  sound  $\iff$  for all  $P$  :

$$\forall G_i \in \llbracket \lfloor P \rfloor_{I_{queue}} \rrbracket. \exists G_s. val(G_i) = val(G_s)$$

$(G_s)_{queue}$  sats.  $queue$  axioms

$(G_s)_{L_1}$  sats.  $L_1$  axioms

$(G_s)_{L_n}$  sats.  $L_n$  axioms

...

# Local Soundness

(when  $P$  calls  $\text{queue}, L_1, \dots, L_n$ )

$I_{\text{queue}}$  sound  $\iff$  for all  $P$ :

$\forall G_i \in [\llbracket P \rfloor_{I_{\text{queue}}}]. \exists G_s . \text{val}(G_i) = \text{val}(G_s)$   
 $(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms  
 $(G_s)_{L_1}$  sats.  $L_1$  axioms  
...  
 $(G_s)_{L_n}$  sats.  $L_n$  axioms

$I_{\text{queue}}$  **locally sound**  $\iff$  for all  $P$ :

$\forall G_i \in [\llbracket P \rfloor_{I_{\text{queue}}}]. \exists G_s . \text{val}(G_i) = \text{val}(G_s)$   
 $(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms  
 $G_s . \text{so}_{\text{queue}} \subseteq G_i . \text{hb}$

# Local Soundness

(when  $P$  calls  $\text{queue}, L_1, \dots, L_n$ )

$I_{\text{queue}}$  sound  $\iff$  for all  $P$ :

$$\forall G_i \in [\llbracket P \rfloor_{I_{\text{queue}}}] . \exists G_s . \text{val}(G_i) = \text{val}(G_s)$$

$(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms

$(G_s)_{L_1}$  sats.  $L_1$  axioms

$(G_s)_{L_n}$  sats.  $L_n$  axioms

Given for free!

$I_{\text{queue}}$  **locally sound**  $\iff$  for all  $P$ :

$$\forall G_i \in [\llbracket P \rfloor_{I_{\text{queue}}}] . \exists G_s . \text{val}(G_i) = \text{val}(G_s)$$

$(G_s)_{\text{queue}}$  sats.  $\text{queue}$  axioms

$G_s . \text{so}_{\text{queue}} \subseteq G_i . \text{hb}$

# Compositionality

## Theorem (Compositionality)

$$\textcolor{blue}{I} \text{ locally-sound} \implies \textcolor{blue}{I} \text{ sound}$$

*Proof.* Mechanised in Coq.

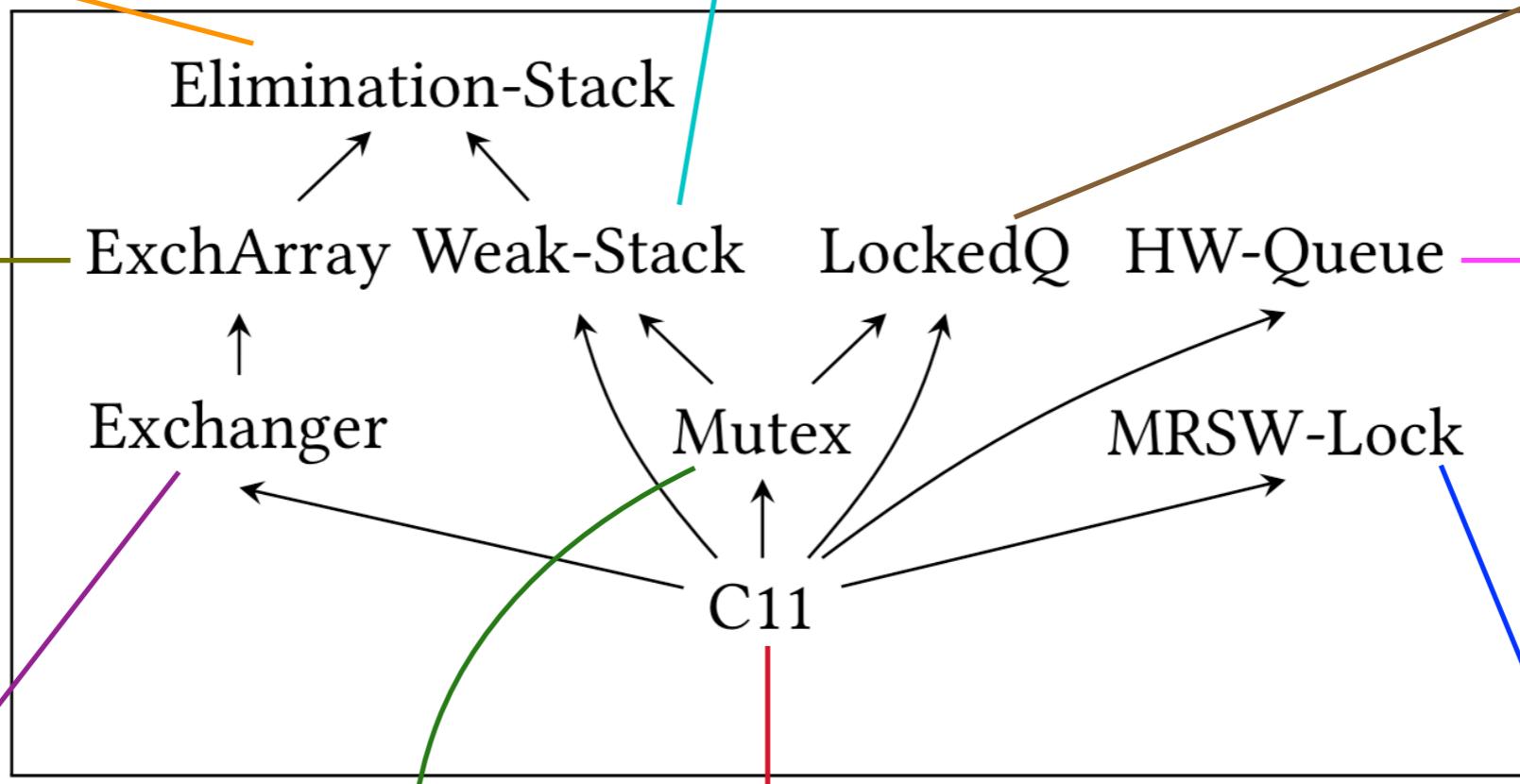
# Case Studies

**scalable** stack implementation

**push/pop** may **fail** if contention on stack top

**strong** locking queue

an array of exchanger objects



**weak & strong** Herlihy-Wing queue implementations

exchanger object from `java.util.concurrent`

ported **existing** declarative specification

multiple-reader-single-writer lock implementation

spinlock implementation using `CASacq` and `Wrel`

# Summary

A declarative ***specification*** and ***verification*** framework:

✓ ***Agnostic*** to memory model

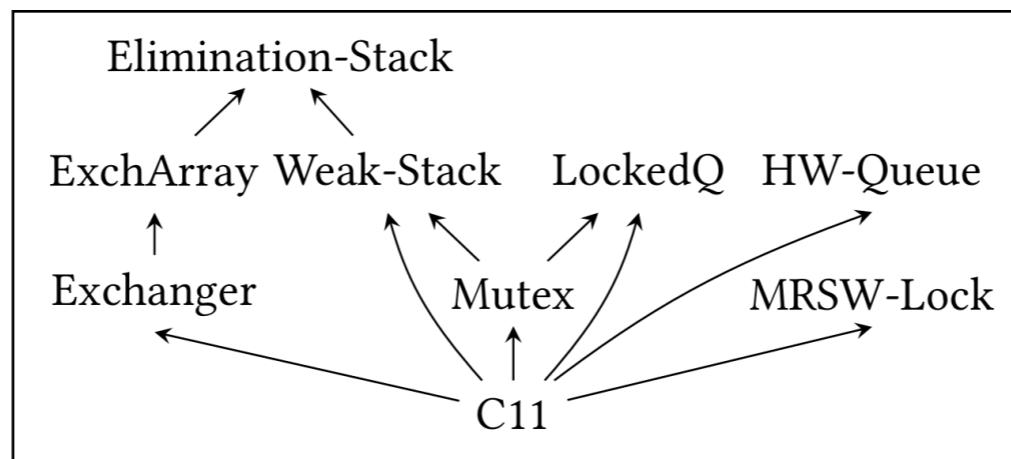
- support both SC and WMC specs

✓ ***General***

- port existing SC (linearisability) specs
- port existing WMC specs (e.g. C11, TSO)
- built from the ground up: assume no pre-existing libraries or specs

✓ ***Compositional***

- vertical composition to verify library implementations
- horizontal composition to verify client programs



# Future Work

- Support ***load buffering*** behaviour
  - allow  $(po \cup so)^+$  cycles
- MM-agnostic, general ***program logic***
  - port existing WMC logics, e.g. RSL, GPS, ...
- MM-agnostic (stateless) ***model-checking***
  - a generalisation of existing approaches, e.g. RCMC, ...

# Thank You for Listening!

A declarative ***specification*** and ***verification*** framework:

✓ ***Agnostic*** to memory model

- support both SC and WMC specs

✓ ***General***

- port existing SC (linearisability) specs
- port existing WMC specs (e.g. C11, TSO)
- built from the ground up: assume no pre-existing libraries or specs

✓ ***Compositional***

- vertical composition to verify library implementations
- horizontal composition to verify client programs

